# LC898213XC

Advance Information



# AF Controller

### Overview

This LSI is AF control LSI. It consists of 1 system of feed back circuit for AF control.

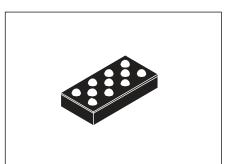
#### Features

- Built-in equalizer circuit using digital operation
  - AF control equalize circuit
  - Any coefficient can be specified by  $I^2 C \ I/F$
- I<sup>2</sup>C Interface
- Built-in A/D converter
  - Maximum 10-bit
  - Input 2 channel
- Built-in D/A converter
  - 8-bit
  - Output 2-channel (Hall offset, Constant current Bias)
- Built-in Hall Sensor
   Si Hall sensor
- Built-in VGAHall Amp
- Built-in OSC
   48MHz (Frequency adjustment function)
- Built-in PWM pulse generator circuit
   PWM circuit for AF control
- 1-chip motor driver
   Saturation drive H bridge 1 channel
- Package
  - WL-CSP 11-pin
  - Lead-free, halogen-free
- Supply voltage
  - Logic unit : Internal core (1.7V to 1.98V), AVDD (2.6V to 3.6V)
  - Driver unit : VM (2.6V to 3.6V)
- \* I<sup>2</sup>C Bus is a trademark of Philips Corporation.

This document contains information on a new product. Specifications and information herein are subject to change without notice.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 5 of this data sheet.



WLCSP11, 1.10x2.20

## **Pin Description**

ТҮРЕ							
I	INPUT	Ρ	Power supply, GND	NC	NOT CONNECT		
0	OUTPUT						
В	BIDIRECTION						
$\blacksquare$ I <sup>2</sup> C in	nterface		2				
	I2CCK	B $I^2C$ Clock pin					
I2CDT B I <sup>2</sup> C Data pin							
∎ Hall	<ul> <li>Hall interface</li> </ul>						
	HALL		Hall amp output				
D.							
■ Drive	<ul> <li>Driver interface</li> </ul>		• • • •				
	OUT1	0	_	Actuator output pin			
	OUT2	0	Actuator outpu	Actuator output pin			
- Douv	an annalt, ain						
Powe	Power supply pin		Digital norman				
	VDD			Digital power supply			
	VSS	P Digital GND					
	VDDO	Р	LDO power supply out				
	VM	Р	Motor power supply				
	PGND	Р	Power GND	Power GND			
- Tosta	nin						
■ Test pin							
	TEST O Test pin						

\*Process when pins are not used

PIN TYPE "O" – Ensure that it is set to OPEN.

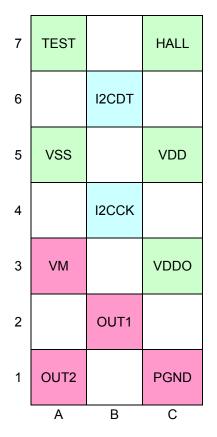
PIN TYPE "I" – OPEN is inhibited. Ensure that it is connected to the Vdd or Vss even when it is unused. (Please contact ON Semiconductor company for more information about selection of Vdd or Vss.) PIN TYPE "B" – If you are unsure about processing method on the pin description of pin layout table, please contact us.

Note that incorrect processing of unused pins may result in defects.

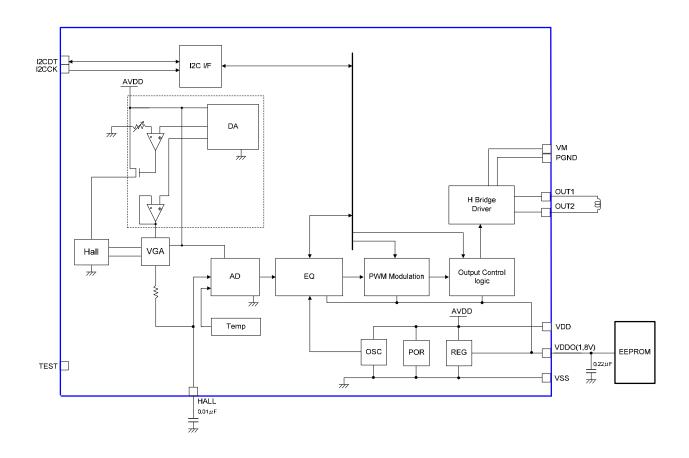
# Pin Layout

Circuit Name	Number of Pins	Circuit Name	Number of Pins
Analog	5	Driver	4
Logic	2		

Backside pin layout diagram (Top View from the mold side)



# **Block Diagram**

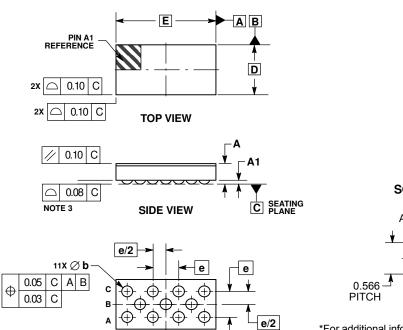


#### **Package Dimensions**

unit : mm

### WLCSP11, 1.10x2.20

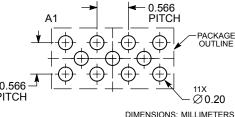
CASE 567HP ISSUE O



 NOTES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS. MILLIMETERS MIN MAX DIM 0.45 Α A1 0.03 0.13 b D 0.15 0.25 1.10 BSC 20 BS 0.566 BS e RECOMMENDED

NOTES

# SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **ORDERING INFORMATION**

2 3 4 5 6

**BOTTOM VIEW** 

Device	Package	Shipping (Qty / Packing)
LC898213XC-MH	WLP11(2.20X1.10) (Pb-Free / Halogen Free)	5000 / Tape & Reel

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