Surface Mount Monolithic Amplifier

DC-5 GHz

Features

- Miniature SOT-89 Package
- Frequency range, DC to 5 GHz
- Internally Matched to 50 Ohms
- Output power, 17.6 dBm typ.
- Excellent package for heat dissipation, exposed metal bottom
- Aqueous washable
- Protected by US Patent 6,943,629

Applications

- Cellular
- PCS
- Communication receivers & transmitters

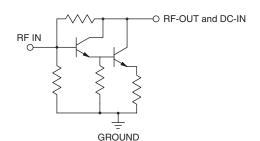


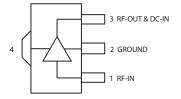
+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

Gali₅59+ (RoHS compliant) is a wideband amplifier offering high dynamic range. Lead finish is SnAgNi. It has repeatable performance from lot to lot, and is enclosed in a SOT-89 package. It uses patented Transient Protected Darlington configuration and is fabricated using InGaP HBT technology. Gali₅59+ is designed to be rugged for ESD and supply switch-on transients.

simplified schematic and pin description





Function	Pin Number	Description
RF IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

A Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document. B. Electrical specifications and performance data contained in this specification document are based on Nini-Circuit's applicable established test performance criteria and measurement instructions. G. The parts covered by this specification document are subject to Mini-Circuit shandard limited warranty and terms and conditions (collectively, "Standard Terms"), Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuit's website at www.minicircuits.com/MCLStore/terms.jsp REV. J M151107 D60-1117.DOC EC-9381/11 GALI-59+ RS/TD/CP/AM 150723 Page 1 of 4

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Electrical Specifications at 25°C and 65mA, unless noted

Parameter		Min.	Тур.	Max.	Units
Frequency Range*		DC		5	GHz
Gain	f=0.1 GHz	_	20.6	_	GHz
	f=1 GHz	_	19.7	_	
	f=2 GHz	16.3	18.3	_	
	f=3 GHz	_	16.7	_	
	f=4 GHz	_	15.4	_	
	f=5 GHz	_	14.0	_	
	f=7 GHz	_	10.2	_	
Input Return Loss	f= DC to 3 GHz		12.5		dB
	f= 3 to 5 GHz		14		
Output Return Loss	f= DC to 3 GHz		14		dB
	f= 3 to 5 GHz		11.5		
Output Power @ 1 dB compression	f=2 GHz	16.5	17.6	_	dBm
Output IP3	f=1 GHz		33.3		dBm
Noise Figure	f=2 GHz		4.3		dB
Recommended Device Operating Current			65		mA
Device Operating Voltage		4.3	4.8	5.2	V
Device Voltage Variation vs. Temperature at 65 m		-3.1		mV/°C	
Device Voltage Variation vs. Current at 25°C		6.2		mV/mA	
Thermal Resistance, junction-to-case ¹			103		°C/W

*Guaranteed specification DC-5 GHz. Low frequency cut off determined by external coupling capacitors.

Absolute Maximum Ratings

Parameter	Ratings	
Operating Temperature*	-45°C to 85°C	
Storage Temperature	-65°C to 150°C	
Operating Current	85mA	
Input Power	13dBm	

Note: Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.

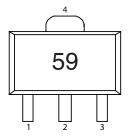
¹Case is defined as ground leads. *Based on typical case temperature rise 5°C above ambient.

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Product Marking



Markings in addition to model number designation may appear for internal quality control purposes.

Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: DF782

Plastic package, exposed paddle, lead finish: Matte-Tin

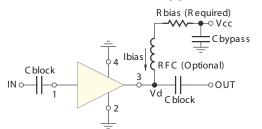
Tape & Reel: F55 7" reels with 20, 50, 100, 200, 500, 1K devices.

Suggested Layout for PCB Design: PL-019

Evaluation Board: TB-409-59+

Environmental Ratings: ENV08T2

Recommended Application Circuit



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS				
Vcc	"1%" Res. Values (ohms) for Optimum Biasing			
7	36.5			
8	51.1			
9	64.9			
10	80.6			
11	97.6			
12	113			
13	127			
14	143			
15	158			
16	174			
17	191			
18	205			
19	221			
20	237			

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ESD Rating

Human Body Model (HBM): Class 1B (500V to < 1000V) in accordance with ANSI/ESD STM 5.1 - 2001

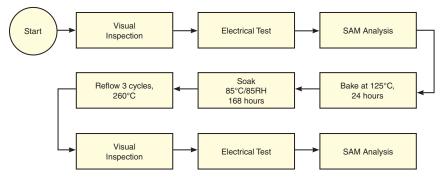
Machine Model (MM): Class M1 (< 100V) in accordance with ANSI/ESD STM 5.2 - 1999

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDECJ-STD-020C

No.	Test Required	Condition	Standard	Quantity
1	Visual Inspection	Low Power Microscope Magnification 40x	MIP-IN-0003 (MCT spec)	45 units
2	Electrical Test	Room Temperature	SCD (MCL spec)	45 units
3	SAM Analysis	Less than 10% growth in term of delamination	J-Std-020C (Jedec Standard)	45 units
4	Moisture Sensitivity Level 1	Bake at 125°C for 24 hours Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-Std-020C (Jedec Standard)	45 units

MSL Test Flow Chart



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