

SUD50N025-06P

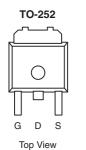
New Product

Vishay Siliconix

COMPLIANT

N-Channel 25-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ)	
25	0.0062 at V _{GS} = 10 V	78	20.5 nC	
25	0.010 at V_{GS} = 4.5 V	62	20.3110	



Drain Connected to Tab

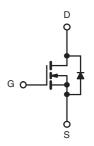
Ordering Information: SUD50N025-06P-E3 (Lead (Pb)-free)

FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_a Tested
- RoHS Compliant

APPLICATIONS

- DC/DC Conversion, Low-Side
 - Desktop PC



N-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S T _A = 25 °C, unle	ess otherwise r	noted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	25	v
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		78 ^{a, e}	
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 70 °C		65 ^{a, e}	
Continuous Drain Current $(T_j = 175 \text{ C})$	T _A = 25 °C	I _D	32 ^{b, c}	
	T _A = 70 °C		25 ^{b, c}	A
Pulsed Drain Current		I _{DM}	100	
Continuous Source-Drain Diode Current	T _C = 25 °C	L.	43	
	T _A = 25 °C	T _A = 25 °C	7.1 ^{b, c}	
Avalanche Current Pulse L = 0.1 mH Single Pulse Avalanche Energy L = 0.1 mH		I _{AS}	35	
		E _{AS}	61.25	mJ
	T _C = 25 °C		65 ^a	
Maximum Power Dissipation	T _C = 70 °C		45 ^a	w
	T _A = 25 °C	P _D	10.7 ^{b, c}	V
	T _A = 70 °C	[7.5 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 10$ sec	R _{thJA}	11	14	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	1.9	2.3	°C/vv	

Notes:

a. Based on $T_C = 25$ °C. b. Surface Mounted on 1" x 1" FR4 board. c. t = 10 sec. d. Maximum under Steady State conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 50 A.

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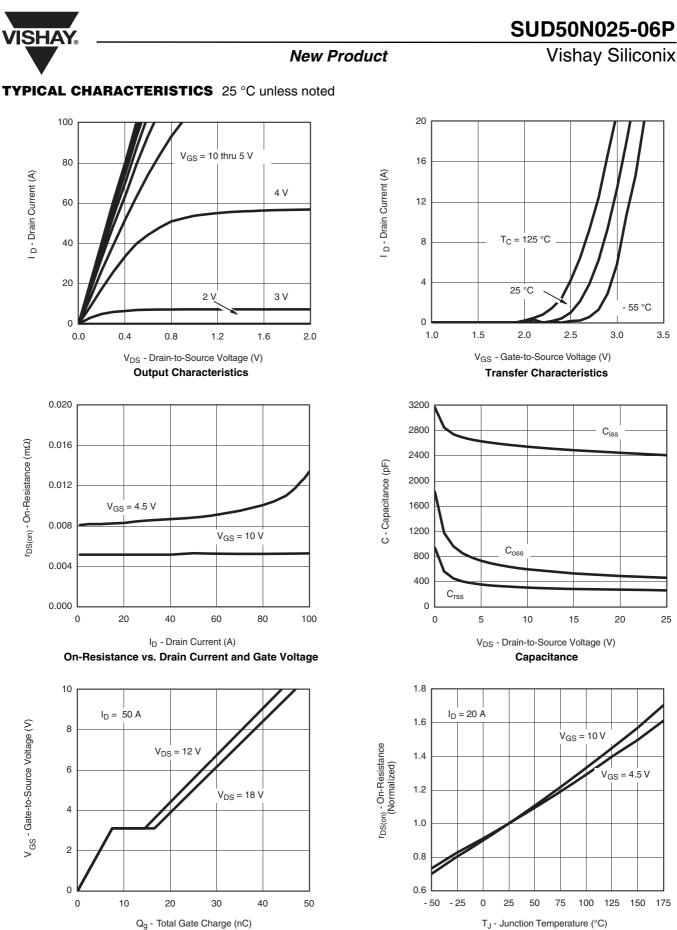
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static				•			
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	25			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L _ 250 uA		20		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.4		2.4	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
	· ·	$V_{DS} = 25 V, V_{GS} = 0 V$			1	μA	
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 25 V, V_{GS} = 0 V, T_{J} = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0051	0.0062	Ω	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 15 A		0.0081	0.010		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		55		S	
Dynamic ^b				I		1	
Input Capacitance	C _{iss}			2490			
Output Capacitance	C _{oss}	V _{DS} = 12 V, V _{GS} = 0 V, f = 1 MHz		530		pF	
Reverse Transfer Capacitance	C _{rss}			280			
		$V_{DS} = 12$ V, $V_{GS} = 10$ V, $I_{D} = 50$ A		44	66	nC	
Total Gate Charge	Qg	Q _g		20.5	31		
Gate-Source Charge	Q _{gs}	V_{DS} = 12 V, V_{GS} = 4.5 V, I_{D} = 50 A		7.5			
Gate-Drain Charge	Q _{gd}			7.0			
Gate Resistance	R _g	f = 1 MHz	0.55	1.1	1.65	Ω	
Turn-On Delay Time	t _{d(on)}			19	28		
Rise Time	t _r	$V_{DD} = 12 \text{ V}, \text{ R}_{L} = 0.24 \Omega$		12	18	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 50$ Å, $V_{GEN} = 4.5$ V, $R_g = 1$ Ω		18	27		
Fall Time	t _f			7	11		
Turn-On Delay Time	t _{d(on)}			9	14	ns	
Rise Time	t _r	$V_{DD} = 12 \text{ V}, \text{ R}_{L} = 0.24 \Omega$		11	16.5		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		24	36		
Fall Time	t _f			8	12		
Drain-Source Body Diode Characteristic	s					L	
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			43	•	
Pulse Diode Forward Current ^a	I _{SM}				100	A	
Body Diode Voltage	V _{SD}	I _S = 30 A		0.9	1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			20	30	nC	
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		13.5	1		
Reverse Recovery Rise Time	t _b			16.5		ns	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

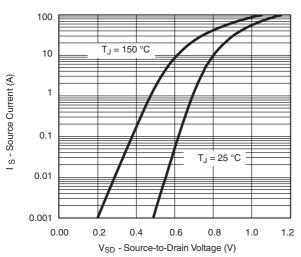


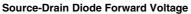
Gate Charge

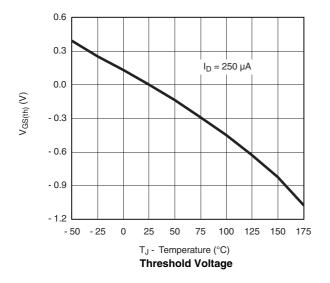
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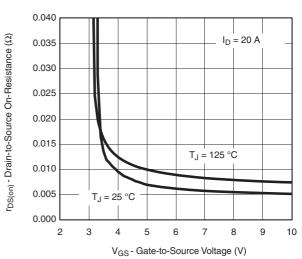


TYPICAL CHARACTERISTICS 25 °C unless noted

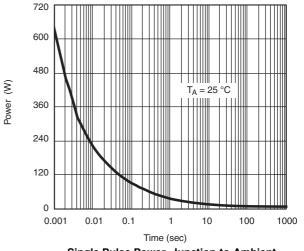




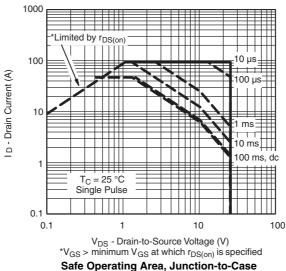




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



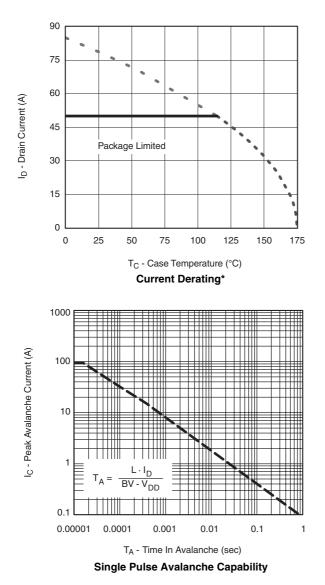


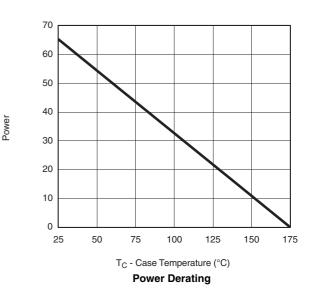
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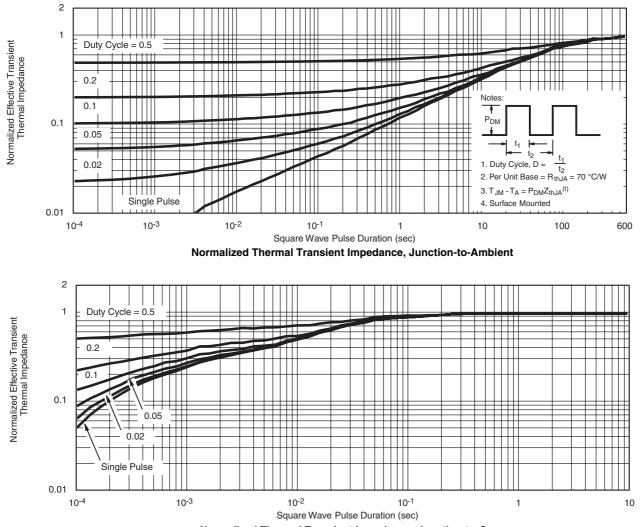


*The power dissipation P_D is based on $T_{J(max)} = 175 \text{ °C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS 25 °C unless noted



Normalized Thermal Transient Impedance, Junction-to-Case

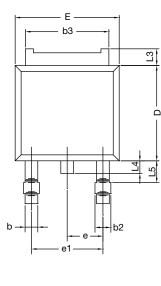
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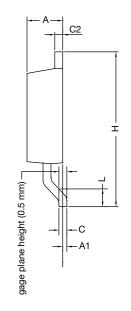


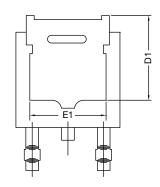


TO-252AA Case Outline

VERSION 1: FACILITY CODE = Y







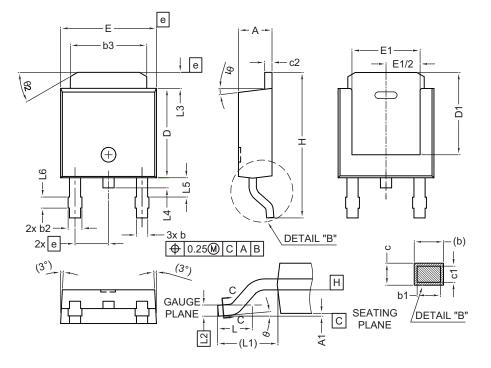
	MILLIMETERS		
DIM.	MIN.	MAX.	
A	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
E	6.35	6.73	
E1	4.32	-	
Н	9.40	10.41	
е	2.28	BSC	
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

Note

• Dimension L3 is for reference only



VERSION 2: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
A	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
E	6.35	6.73	
E1	4.32 -		
e	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74 ref.		
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

Notes

• Dimensioning and tolerance confirm to ASME Y14.5M-1994

• All dimensions are in millimeters. Angles are in degrees

• Heat sink side flash is max. 0.8 mm

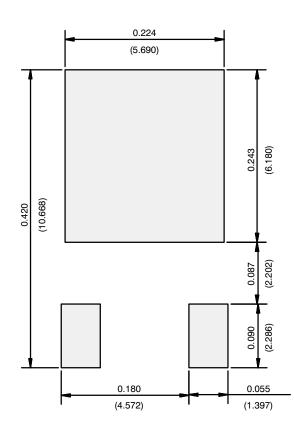
Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022 DWG: 5347

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RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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