3-INPUT / 2-INPUT VIDEO SWITCH

■ GENERAL DESCRIPTION

The **NJM2508** is video switch for video and audio signal. It contanins 3 input-1 output and 2 input-1 output video switch. One input terminal has clamp function and so is applied to fixed DC level of video signal. Its operating voltage is 4.75 to 13V and bandwidth is 10MHz. Crosstalk is 75dB (at f = 4.43MHz)

■ FEATURES

- Operating Voltage (+4.75V to +13V)
- 3 Input-1 Output and 2 Input-1 Output
- Crosstalk 75dB (at 4.43MHz)
- Wide Frequency Range 10MHz (2V_{P-P} Input)
- Package Outline DIP16, DMP16, SSOP16

 V^+

• Bipolar Technology

■ RECOMMENDED OPERATING CONDITION

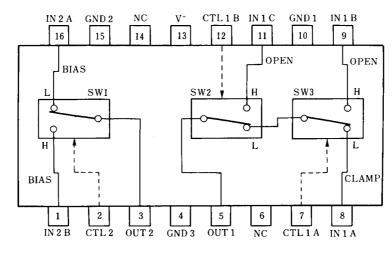
• Operating Voltage

4.75V to 13.0V

■ APPLICATION

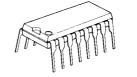
• VCR, Video Camera, AV-TV, Video Disk Player.

BLOCK DIAGRAM



NJM2508D NJM2508M NJM2508V

PACKAGE OUTLINE



NJM2508D



NJM2508M



NJM2508V

■ ABSOLUTE MAXIMUM RAT	(T _a = 25°C)		
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V*	14	V
Power Dissipation	P _D	(DIP16) 700 (DMP16) 350 (SSOP16) 300	mW mW mW
Operating Temperature Range	T _{opr}	-40 to +85	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

■ ELECTRICAL CHARACTERISTICS

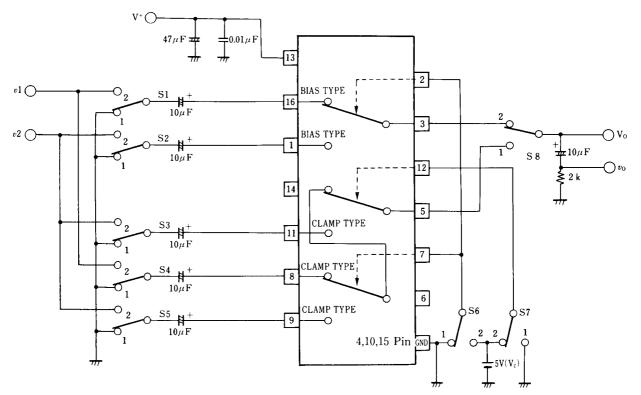
 $(V^+ = 5V, T_a = 25^{\circ}C)$

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current 1	I _{CC1}	$V^+ = 5V$ (Note1)	6.6	9.4	12.3	mA
Operating Current 2	I _{CC2}	$V^+ = 9V$ (Note1)	8.0	11.5	15.0	mA
Voltage Gain	Gv	$V_1 = 2V_{P-P} / 100 \text{khz}, V_0 / V_1$	-0.6	-0.1	+0.4	dB
Frequency Response	G _f	V ₁ = 2V _{P-P} , V _O (10MHz / 100kHz)	-1.0	0	+1.0	dB
Differential Gain	DG	V _I = 2V _{P-P} , Staircase Signal	-	0.3	-	%
Differential Phasa	DP	V _I = 2V _{P-P} , Staircase Signal	-	0.3	-	deg
Output offset Voltage	Vos	(Note2)	-10	0	+10	mV
Crosstalk	СТ	$V_{I} = 2V_{P-P}, 4.43MHz, V_{O} / V_{I}$	-	-75	-	dB
Switch Change Voltage	V _{CH}	All inside SW : ON	2.5	-	-	V
Switch Change Voltage	V _{CL}	All inside SW : OFF	-	-	1.0	V

(Note1) S1 = S2 = S3 = S4 = S5 = S6 = S7 = 1

(Note2) Output DC Voltage Difference is tested on S6 = $1 \rightarrow 2$, S1 = S2 = S3 = S4 = S5 = 1, S8 = 2 and S7 = 1

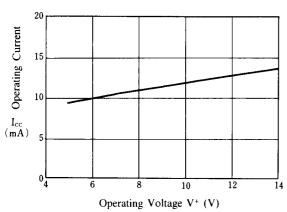
■ TEST CIRCUIT



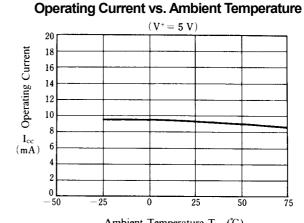
■ PIN FUNCTION

PIN No.	PIN NAME	DC VOLTAGE	INSIDE EQUIVALENT CIRCUIT
16 1	IN 2 A IN 2 B [Input]	2.5V	500 15k 2.5V
8	IN 1A [Input]	1.5V	
9 11	IN 1B IN 1C [Input]		
7 12 2	CTL 1A CTL 1B CTL 2 [Control]		
5	OUT1 [Output]	1.8V	
3	OUT2 [Output]	0.8V	
13	V ⁺	5V	
15 4 10	GND 1 GND 2 GND 3		

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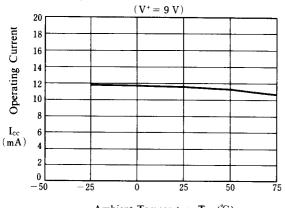


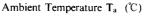
Operating Current vs. Operating Voltage

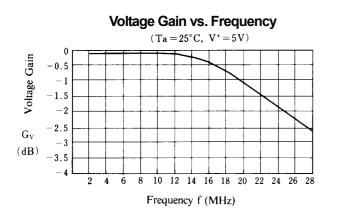


Ambient Temperature T_a (°C)

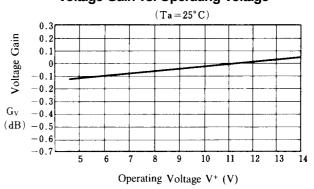
Operating Current vs. Ambient Temperature

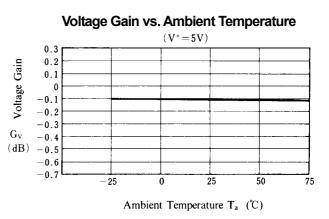


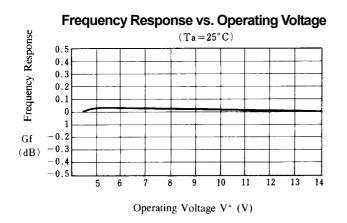


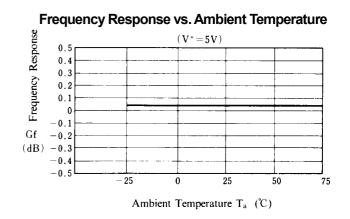


Voltage Gain vs. Operating Voltage

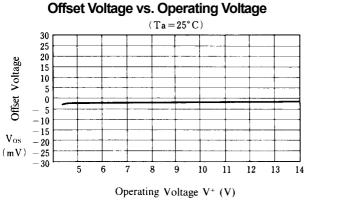


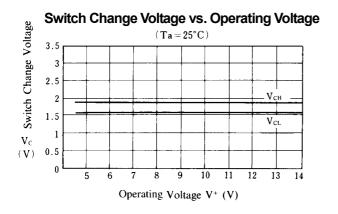






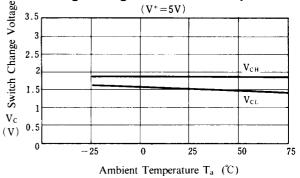
Offset Voltage vs. Ambient Temperature





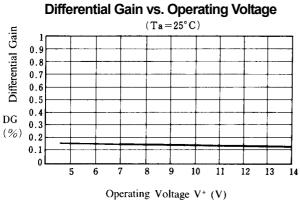
 $(V^{+}=5V)$ 30 25 20 15 Offset Voltage 10 5 0 5 - 10 -15 Vos 20 (\mathbf{mV}) - 25 - 30 - 25 25 0 50 75

Switch Change Voltage vs. Ambient Temperature



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Ambient Temperature T_a (°C)



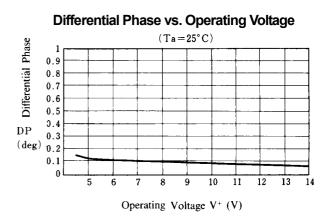
Differential Gain vs. Ambient Temperature

 $(V^+ = 5V)$

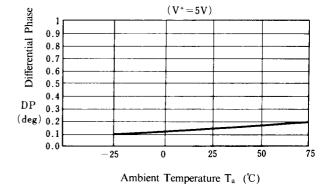
25

Ambient Temperature T_a (°C)

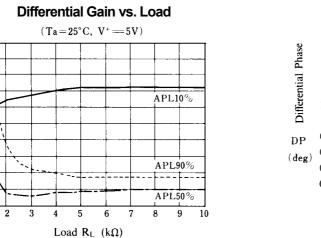
0



Differential Phase vs. Ambient Temperature



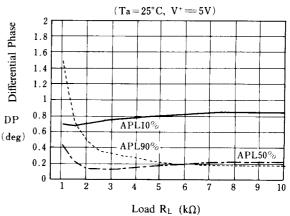




50

75

Differential Phase vs. Load



Differential Gain

DG

(%) 0.2 0.1

1

09

0.8

0.7

0.6

0.5

0.4

0.3

0

2

1.8

1.6 1.4

1.2

0.4

0.2

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1

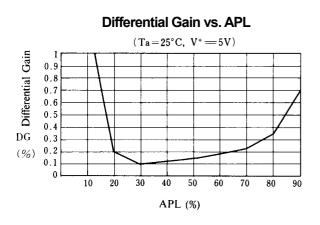
1

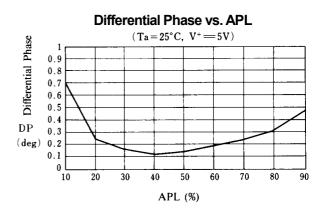
Differential Gain

DG 0.8

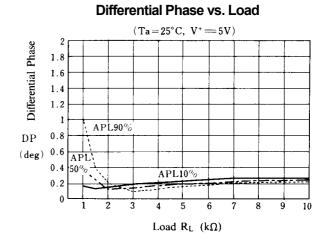
(%) 0.6

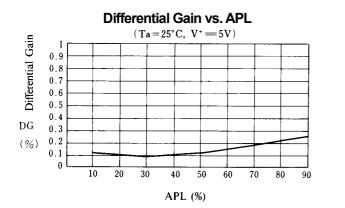
-25

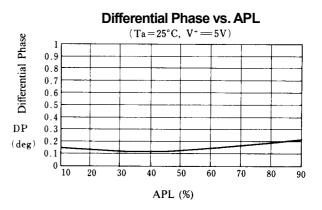


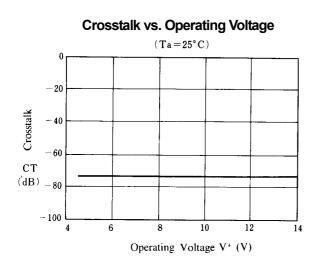


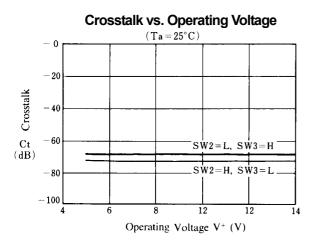
Differential Gain vs. Load $(Ta = 25^{\circ}C, V^{+} = 5V)$ 2 Differential Gain 1.8 1.6 APL90% 1.4 1.2 ARL50% 1 DG 0.8 APL10% (%) 0.6 0.4 0.2 0 2 3 5 6 8 9 10 4 7 Load R_L (k Ω)

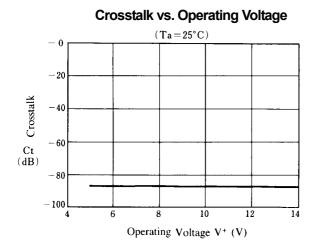


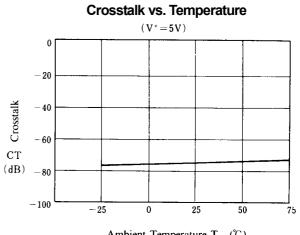






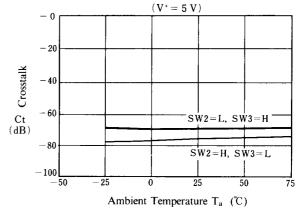


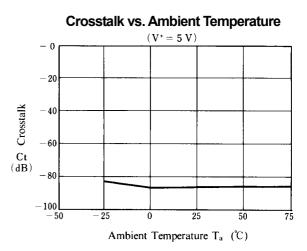




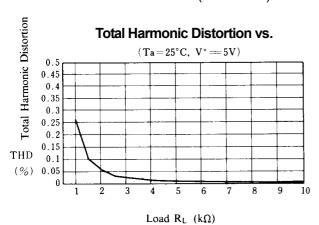
Ambient Temperature T_a (°C)





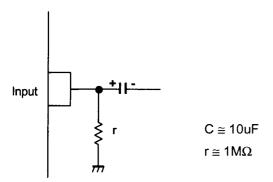


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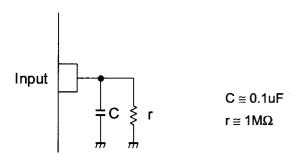


■ APPLICATION

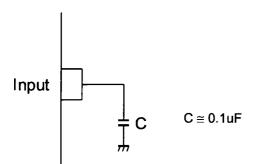
This IC requires 1MΩ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1μF capacitor between INPUT and GND, 1MΩ resistance between INPUT and GND for clamp type input at mute mode.



This IC requires 0.1µF capacitor between INPUT and GND for bias type input at mute mode.



[CAU	TION]
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