

# PC845XJ0000F **Series**

\*1-channel package type is also available. (model No. PC815XJ0000F Series)

# DIP 16pin (4-channel) **Darlington Phototransistor Output, Photocoupler**



#### Description

PC845XJ0000F Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-channel package, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0 kV. CTR is MIN. to 600% at input current of 1.0 mA.

#### ■ Features

- 1. 6pin DIP 4-channnel package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. Darlington phototransistor output (CTR: MIN. 50% at  $I_F=1.0 \text{ mA}, V_{CE}=2 \text{ V}$
- 4. High isolation voltage between input and output  $(V_{iso(rms)}: 5.0 \text{ kV})$
- 5. Lead-free and RoHS directive compliant

## ■ Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC845)
- 2. Approved by VDE, DIN EN60747-5-2(\*) (as an option), file No. 40008087 (as model No. PC845)
- 3. Package resin: UL flammability grade (94V-0))

(\*) DIN EN60747-5-2: successor standard of DIN VDE0884.

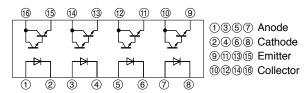
## ■ Applications

- 1. Home appliances
- 2. Programmable controller
- 3. Signal transmission between circuits of different potentials and impedances

1

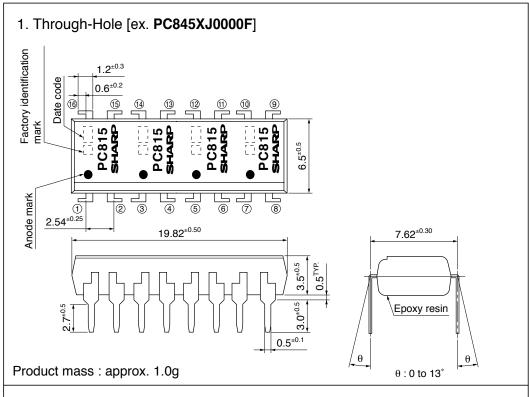


# ■ Internal Connection Diagram

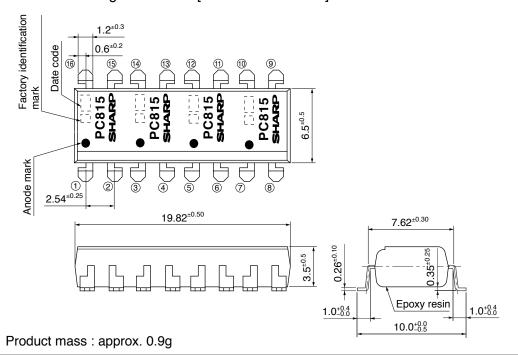


#### **■** Outline Dimensions

(Unit:mm)

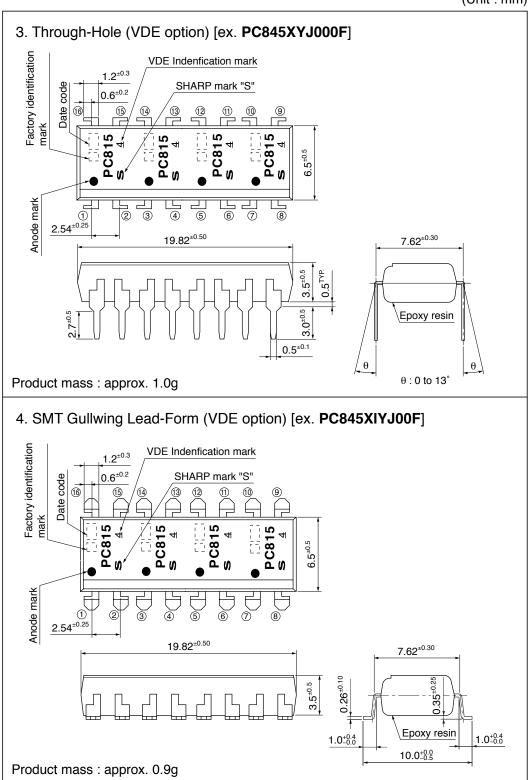


## 2. SMT Gullwing Lead-Form [ex. PC845XIJ000F]





(Unit:mm)



Plating material: SnCu (Cu: TYP. 2%)



Date	code	(2	dia	tir'	١
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	1st	digit		2nd digit				
	Year of production			Month of production				
A.D.	Mark	A.D.	Mark	Month	Mark			
1990	A	2002	P	January	1			
1991	В	2003	R	February	2			
1992	С	2004	S	March	3			
1993	D	2005	T	April	4			
1994	Е	2006	U	May	5			
1995	F	2007	V	June	6			
1996	Н	2008	W	July	7			
1997	J	2009	X	August	8			
1998	K	2010	A	September	9			
1999	L	2011	В	October	0			
2000	M	2012	С	November	N			
2001	N	:	:	December	D			

repeats in a 20 year cycle

# Factory identification mark

Factory identification Mark	Country of origin		
no mark	Lomon		
	Japan		
	Indonesia		
	China		

<sup>\*</sup> This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actural status of the production.

#### Rank mark

There is no rank mark indicator.



<b>Absolute Maximum Ratings</b> $(T_a=25^{\circ}C)$							
	Parameter	Symbol	Rating	Unit			
	Forward current	$I_F$	50	mA			
Lament	*1 Peak forward current	$I_{FM}$	1	A			
Input	Reverse voltage	V <sub>R</sub>	6	V			
	Power dissipation	P	70	mW			
	Collector-emitter voltage	$V_{CEO}$	35	V			
Output	Emitter-collector voltage	V <sub>ECO</sub>	6	V			
	Collector current	$I_{\rm C}$	80	mA			
	Collector power dissipation	P <sub>C</sub>	150	mW			
Total	power dissipation	P <sub>tot</sub>	200	mW			
Opera	ating temperature	Topr	-30 to +100	°C			
Stora	ge temperature	T <sub>stg</sub>	-55 to +125	°C			
*2 Isolat	ion voltage	V <sub>iso(rms)</sub>	5	kV			
*3 Solde	ering temperature	T <sub>sol</sub>	270	°C			

<sup>\*1</sup> Pulse width≤100ms, Duty ratio : 0.001

# **■** Electro-optical Characteristics

 $(T_a=25^{\circ}C)$ 

	(18)						ra-25 C)	
Parameter			Symbol	Condition	MIN.	TYP.	MAX.	Unit
	Forward voltage		$V_F$	I <sub>F</sub> =20mA	_	1.2	1.4	V
Immust	Peak forward voltage		$V_{FM}$	$I_{FM}=0.5V$	_	_	3.0	V
Input	Reverse current		$I_R$	$V_R=4V$	_	_	10	μA
	Terminal capacitance		$C_{t}$	V=0, f=1kHz	_	30	250	pF
	Collector dark current		$I_{CEO}$	$V_{CE}=10V, I_{F}=0$	_	_	1 000	nA
Output	Output Collector-emitter breakdown voltage		$BV_{CEO}$	I <sub>C</sub> =0.1mA, I <sub>F</sub> =0	35	_	_	V
Emitter-collector breakdown voltage		$\mathrm{BV}_{\mathrm{ECO}}$	$I_{E}=10\mu A, I_{F}=0$	6	_	_	V	
	Current transfer ratio		$I_{C}$	$I_F=1 \text{ mA}, V_{CE}=2V$	6.0	16.0	75.0	mA
	Collector-emitter saturation voltage		V <sub>CE(sat)</sub>	$I_F=20\text{mA}, I_C=5\text{mA}$	_	0.8	1.0	V
Transfer	Isolation resistance		R <sub>ISO</sub>	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	1×10 <sup>11</sup>	_	Ω
charac-	Floating capacitance		$C_{\rm f}$	V=0, f=1MHz	_	0.6	1.0	pF
teristics	Cut-off frequency		$f_C$	$V_{CE}=2V$ , $I_{C}=2mA$ , $R_{L}=100\Omega$ , $-3dB$	1	6	_	kHz
	Rise time		$t_r$	$V_{CF}=2V, I_{C}=10mA, R_{I}=100\Omega$	_	60	300	μs
Response time		Fall time	$t_{\mathrm{f}}$	v CE=2 v, 1C=10IIIA, KL=100\$2	_	53	250	μs

<sup>\*2 40</sup> to 60%RH, AC for 1minute, f=60Hz

<sup>\*3</sup> For 10s



# ■ Model Line-up

Lead Form	Throug	gh-Hole	SMT Gullwing Form			
Package	Sleeve					
rackage	25pcs/sleeve					
DIN EN60747-5-2		Approved — Approve				
Model No.	PC845XJ0000F	PC845XYJ000F	PC845XIJ000F	PC845XIYJ00F		

Please contact a local SHARP sales representative to inquire about production status.



Fig.1 Forward Current vs.

Ambient Temperature

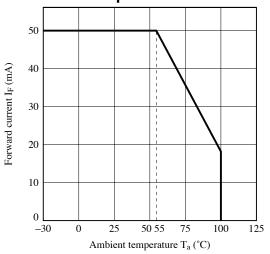


Fig.3 Collector Power Dissipation vs. Ambient Temperature

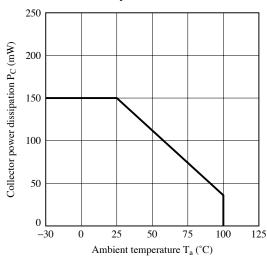


Fig.5 Peak Forward Current vs. Duty Ratio

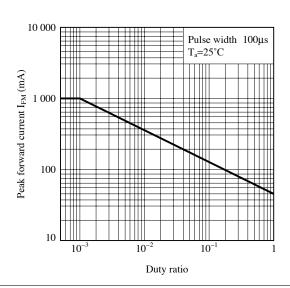


Fig.2 Diode Power Dissipation vs.
Ambient Temperature

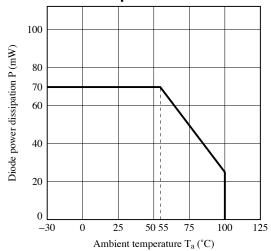


Fig.4 Total Power Dissipation vs.
Ambient Temperature

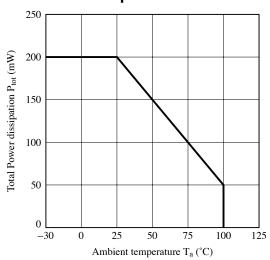


Fig.6 Current Transfer Ratio vs. Forward Current

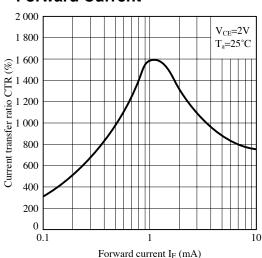




Fig.7 Forward Current vs. Forward Voltage

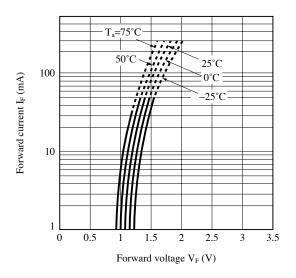


Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

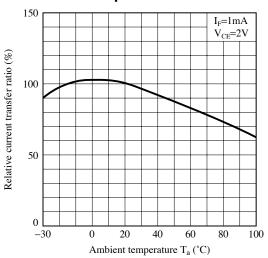


Fig.11 Collector Dark Current vs.
Ambient Temperature

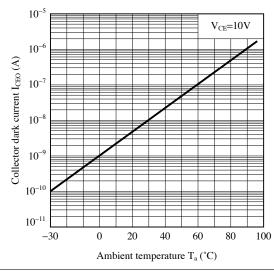


Fig.8 Collector Current vs.
Collector-emitter Voltage

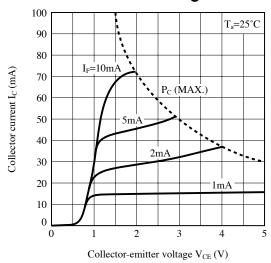


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

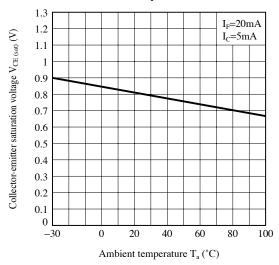


Fig.12 Collector-emitter Saturation Voltage vs. Forward Current

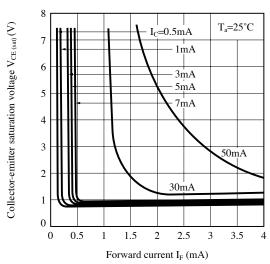
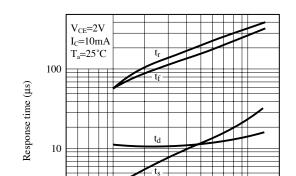


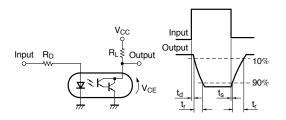


Fig.11 Response Time vs. Load Resistance



Load resistance  $R_L\left(k\Omega\right)$ 

Fig.12 Test Circuit for Response Time



Please refer to the conditions in Fig.13

Fig.13 Frequency Response

0.1

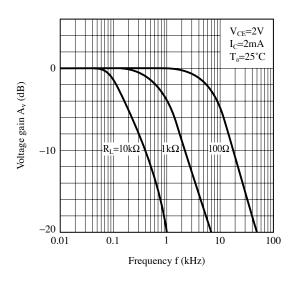
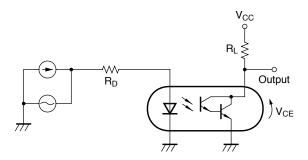


Fig.14 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



# **■** Design Considerations

## Design guide

While operating at I<sub>F</sub><1.0mA, CTR variation may increase.

Please make design considering this fact.

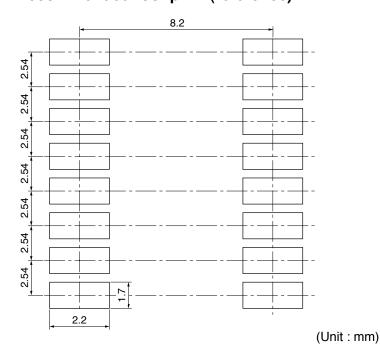
This product is not designed against irradiation and incorporates non-coherent IRED.

# Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

## Recommended foot print (reference)



☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.



## ■ Manufacturing Guidelines

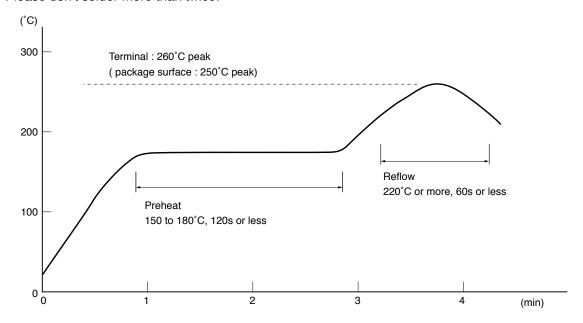
## Soldering Method

#### Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



## Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270 °C and within 10s.

Preheating is within the bounds of 100 to 150 °C and 30 to 80s.

Please don't solder more than twice.

#### Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400 °C.

Please don't solder more than twice.

#### Other notice

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



## Cleaning instructions

#### Solvent cleaning:

Solvent temperature should be 45°C or below. Immersion time should be 3 minutes or less.

#### Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



# ■ Package specification

# ● Sleeve package

# Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

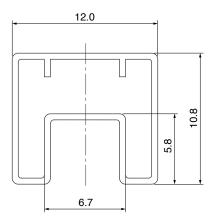
#### Package method

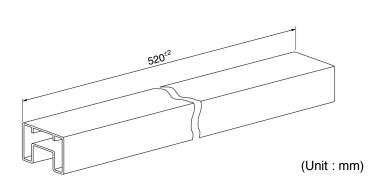
MAX. 25 pcs. of products shall be packaged in a sleeve. Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

#### Sleeve outline dimensions







#### ■ Important Notices

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  - --- Office automation equipment
  - --- Telecommunication equipment [terminal]
  - --- Test and measurement equipment
  - --- Industrial control
  - --- Audio visual equipment
  - --- Consumer electronics
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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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