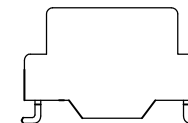
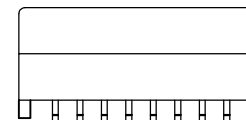
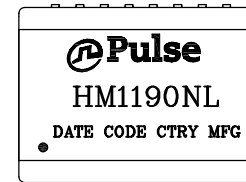


NOTES:

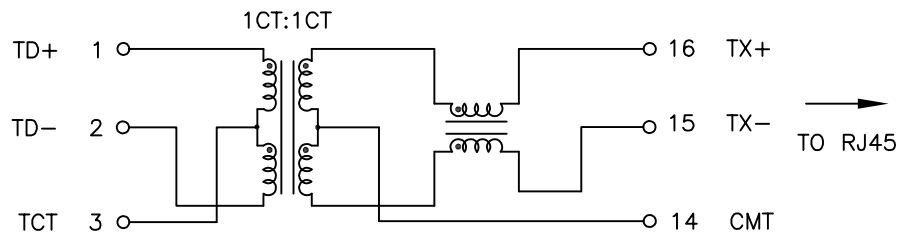
1. ROHS COMPLIANT
2. STORAGE TEMPERATURE: -50°C TO +125°C
3. COMPLIANCE TO J-STD:
  - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
  - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
  - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
4. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e HXXXXXXXXNL BECOMES HXXXXXXXXNLT).



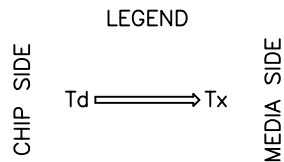
FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-P1	PS-2005.002-D	1 OF 3	HM1190NL	D



SCHEMATIC



ALL CHANNELS IN  
PHASE INPUT TO  
OUTPUT

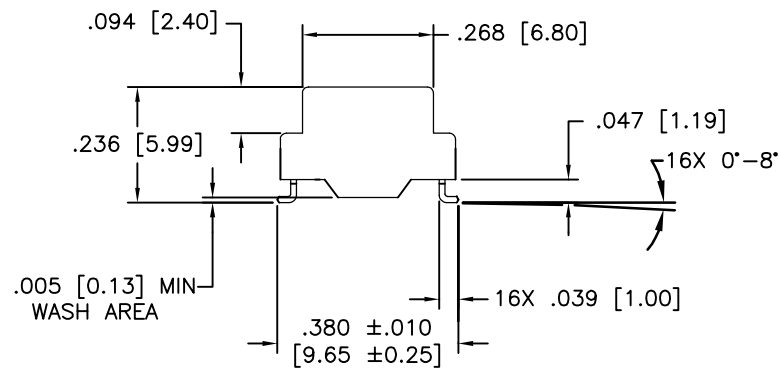
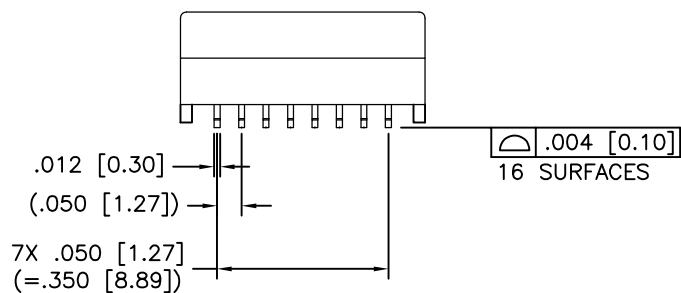
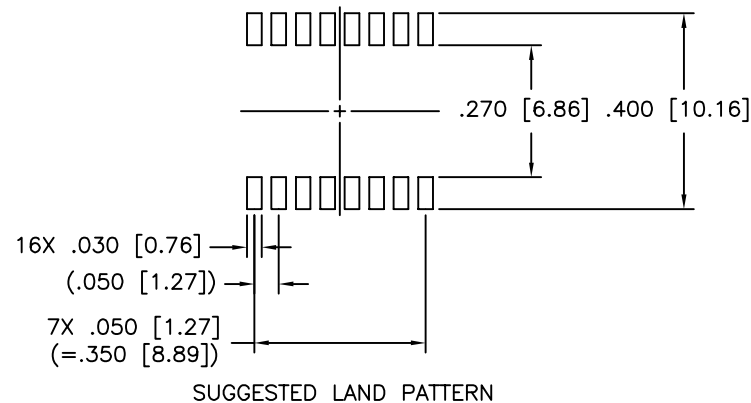
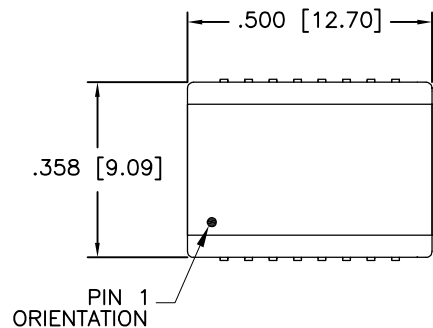
ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED  
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS	
OPERATING TEMP	-40°C TO +125°C	
TURNS RATIO	1 : 1 ±2%	
POLARITY	PER SCHEMATIC	
INDUCTANCE (OCL) (MEDIA SIDE, -40°C~+125°C)	350 uH MIN (MEASURED AT 100 KHz, 100 mVRMS AND WITH 8 mA DC BIAS)	
INSERTION LOSS	1-100 MHz	125 MHz
	-1.0 dB MAX	-3.0 dB MAX
RETURN LOSS Z <sub>OUT</sub> = 100 OHMS	5 MHz	30-80 MHz
	-18 dB MIN	-16 + 20LOG <sub>10</sub> (f/30 MHz) dB MIN
DIFFERENTIAL TO COMMON MODE REJECTION	5-200 MHz	
	-50 + 17LOG <sub>10</sub> (f/5 MHz) dB MIN	
INPUT - OUTPUT ISOLATION	2500 VRMS FOR 60 SECONDS	

NOTE: f IS FREQUENCY IN MHz.

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-P1	PS-2005.002-D	2 OF 3	HM1190NL	D



DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY.  
.XX= ±.01 [±0.25]  
.XXX= ±.005 [±0.13]

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1190NL-P1	PS-2005.002-D	3 OF 3	HM1190NL	D