

# CPH6337

## Power MOSFET –12V, 70mΩ, –3.5A, Single P-Channel

This Power MOSFET is produced using ON Semiconductor’s trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

### Features

- Low On-Resistance
- High Speed Switching
- 1.8V drive
- Pb-Free and RoHS compliance
- Halogen Free compliance : CPH6337-TL-W

### Typical Applications

- Load Switch

### SPECIFICATIONS

**ABSOLUTE MAXIMUM RATING** at Ta = 25°C (Note 1, 2)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V <sub>DSS</sub>	–12	V
Gate to Source Voltage	V <sub>GSS</sub>	±10	V
Drain Current (DC)	I <sub>D</sub>	–3.5	A
Drain Current (Pulse) PW ≤ 10μs, duty cycle ≤ 1%	I <sub>DP</sub>	–14	A
Power Dissipation When mounted on ceramic substrate (1200mm <sup>2</sup> × 0.8mm)	P <sub>D</sub>	1.6	W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : This product is designed to “ESD immunity<200V\*”, so please take care when handling.

\*Machine Model

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (1200mm <sup>2</sup> × 0.8mm)	R <sub>θJA</sub>	78.1	°C/W

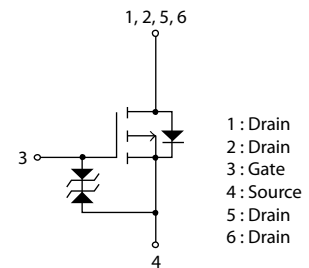


**ON Semiconductor**®

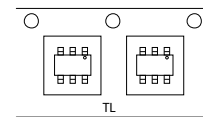
www.onsemi.com

V <sub>DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max
–12V	70mΩ@ –4.5V	–3.5A
	115mΩ@ –2.5V	
	215mΩ@ –1.8V	

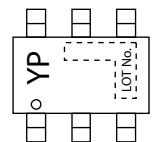
### ELECTRICAL CONNECTION P-Channel



### PACKING TYPE : TL



### MARKING



### ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

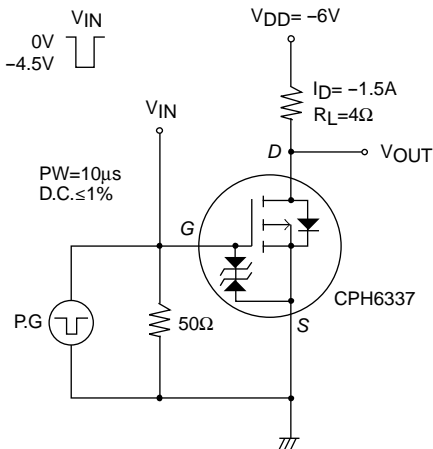
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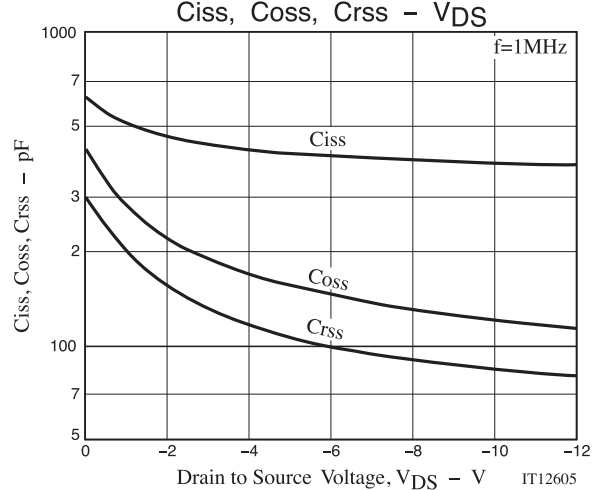
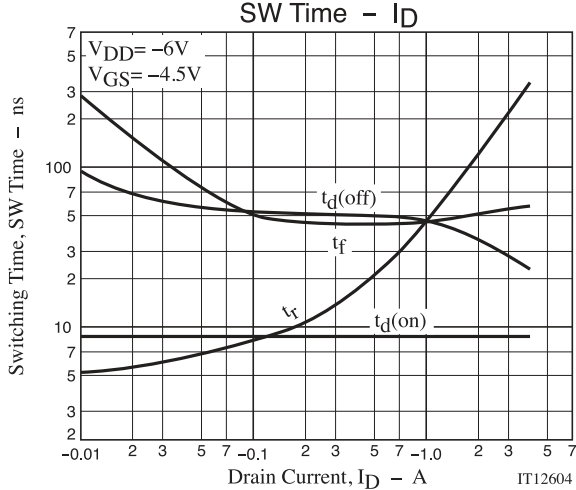
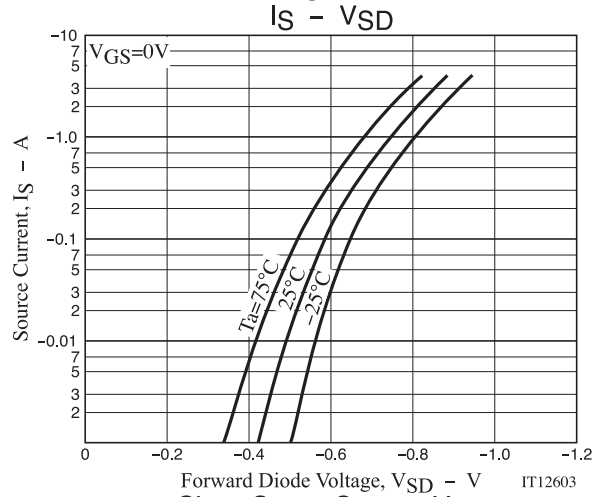
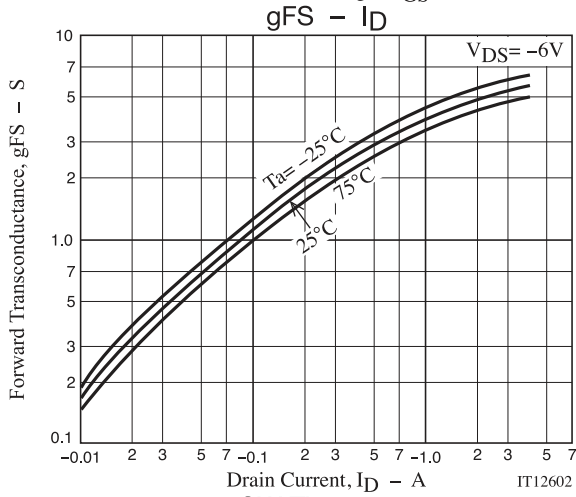
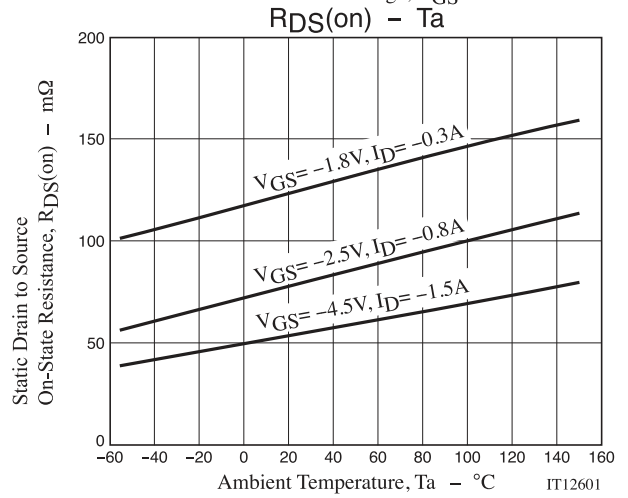
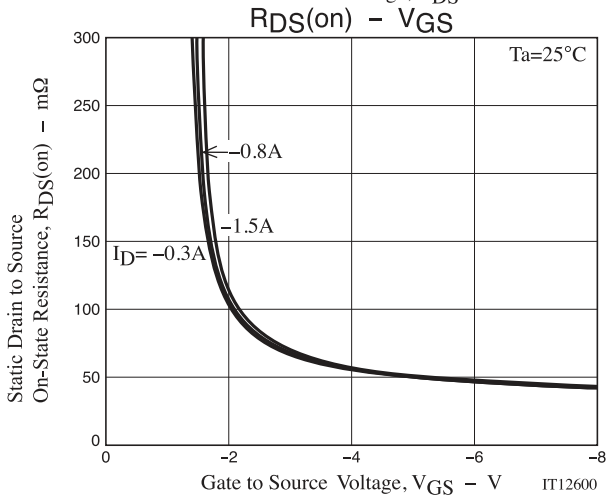
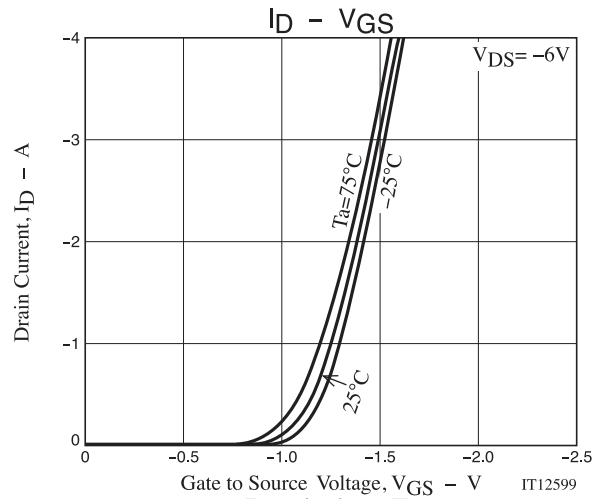
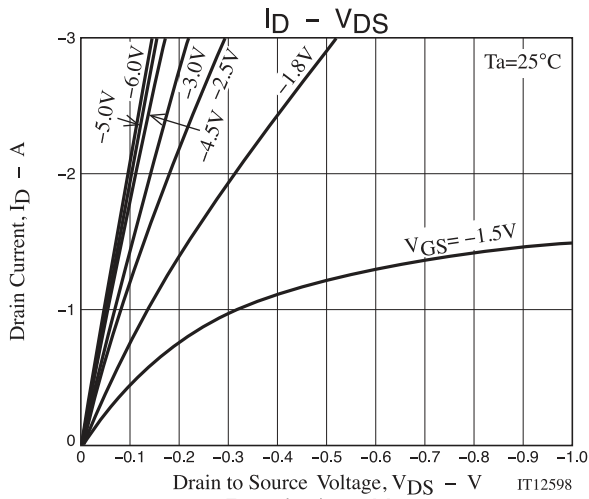
## ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 3)

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-12			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-12V, V <sub>GS</sub> =0V			-10	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =-6V, I <sub>D</sub> =-1mA	-0.4		-1.4	V
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-6V, I <sub>D</sub> =-1.5A	2.7	4.5		S
Static Drain to Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =-1.5A, V <sub>GS</sub> =-4.5V		54	70	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =-0.8A, V <sub>GS</sub> =-2.5V		80	115	mΩ
	R <sub>DS(on)3</sub>	I <sub>D</sub> =-0.3A, V <sub>GS</sub> =-1.8V		125	215	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-6V, f=1MHz		405		pF
Output Capacitance	C <sub>oss</sub>			145		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			100		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit		8.8		ns
Rise Time	t <sub>r</sub>			80		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			41		ns
Fall Time	t <sub>f</sub>			50		ns
Total Gate Charge	Q <sub>g</sub>			5.6		nC
Gate to Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-6V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.5A		0.7		nC
Gate to Drain "Miller" Charge	Q <sub>gd</sub>			1.6		nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> =-3.5A, V <sub>GS</sub> =0V		-0.86	-1.5	V

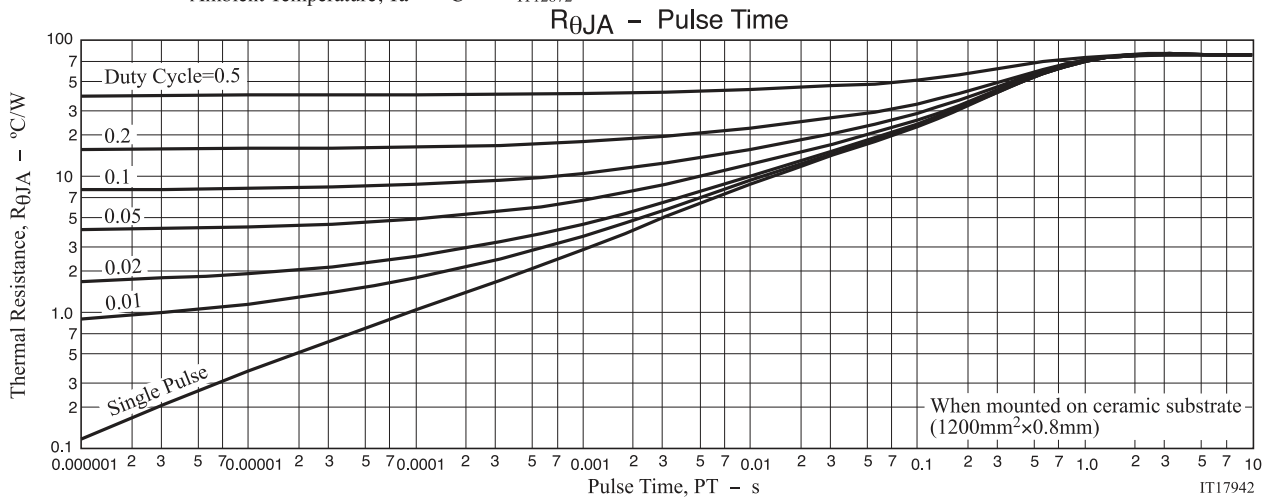
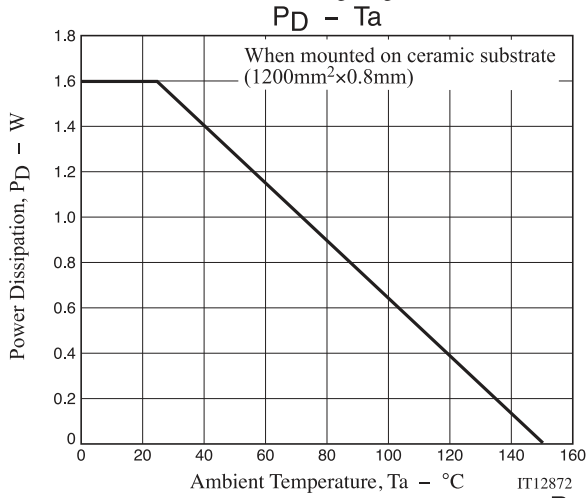
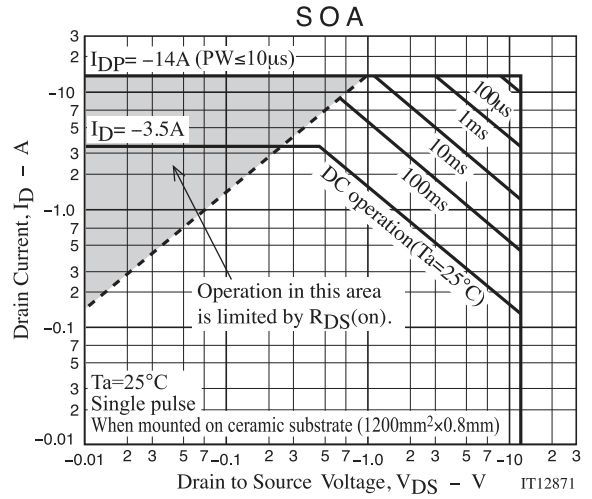
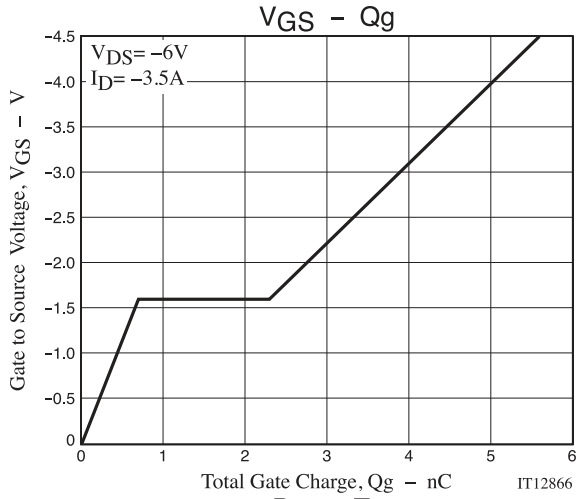
Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### Switching Time Test Circuit





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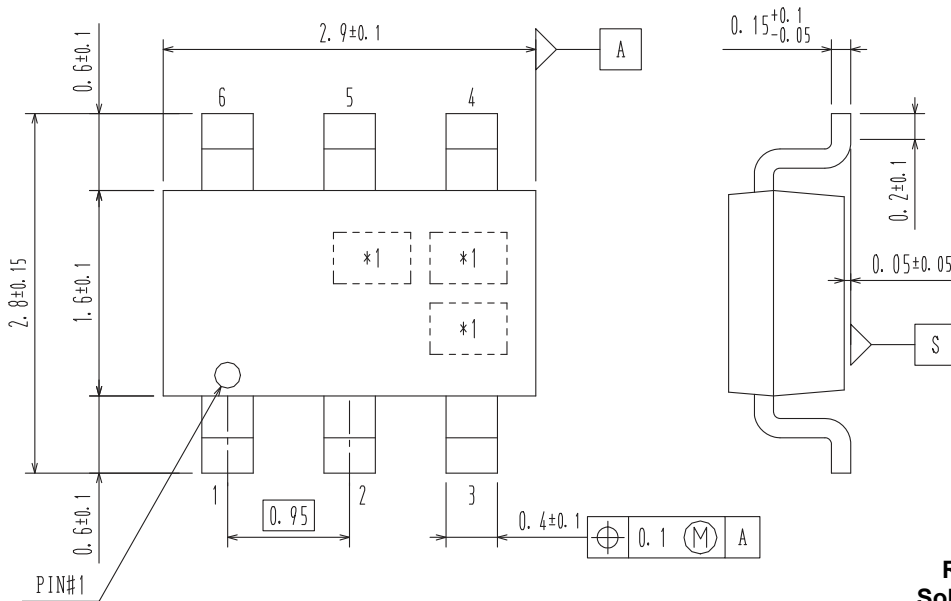


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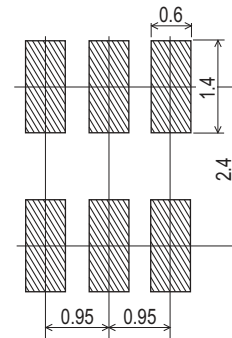
## PACKAGE DIMENSIONS

unit : mm

CPH6  
CASE 318BD  
ISSUE O

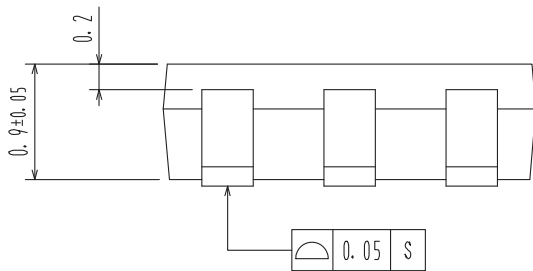


### Recommended Soldering Footprint



\*1:Lot indication

- 1 : Drain
- 2 : Drain
- 3 : Gate
- 4 : Source
- 5 : Drain
- 6 : Drain



## ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
CPH6337-TL-E	YP	CPH6 (Pb-Free)	3,000 / Tape & Reel
CPH6337-TL-W		CPH6 (Pb-Free / Halogen Free)	

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. [http://www.onsemi.com/pub\\_link/Collateral/BRD8011-D.PDF](http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF)

Note on usage : Since the CPH6337 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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