#### TOSHIBA Diodes For Protecting Against ESD

# DF3A6.2F

# Product for Use Only as Protection against Electrostatic Discharge (ESD)

\*This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.

• The mounting of two devices on an ultra-compact package allows the number of parts and the mounting cost to be reduced.

#### Abusolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power dissipation	Р	150*	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	–55 to 150	°C

\*: Mounted on a glass epoxy circuit board of 20 mm × 20 mm, pad dimension of 4 mm × 4 mm.

+0.5 15 +0.1 0.95 2.9±0.2 <u>\_</u> .95 0~0.1 1. Cathode1 2.Cathode2 S-MINI 3. Anode JEDEC JEITA \_\_\_\_ 1-3G1E TOSHIBA

Weight: 12 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Zener voltage	VZ	I <sub>Z</sub> = 5 mA	5.8	6.2	6.6	V
Dynamic impedance	ZZ	I <sub>Z</sub> = 5 mA	_	_	30	Ω
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 3 V	_	_	1.0	μA
Terminal capacitance (between Cathode and Anode)	CT	V <sub>R</sub> = 0 V, f = 1 MHz		55	_	pF

Unit: mm

# **Guaranteed Level of ESD Immunity**

Test Condition	ESD Immunity Level
IEC61000-4-2 (Contact discharge)	$\pm$ 30 kV

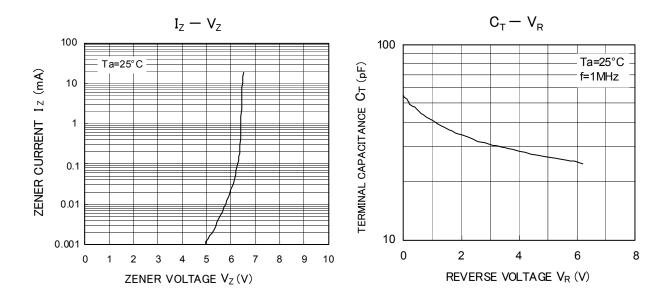
Criterion: No damage to device elements

# Marking

# Equivalent Circuit (Top View)







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