

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL announced Product

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## 2SK2737

Silicon N Channel MOS FET  
High Speed Power Switching

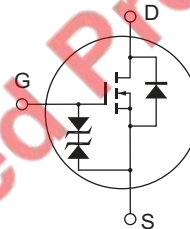
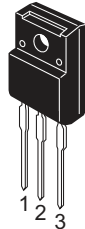
REJ03G1031-0400  
(Previous: ADE-208-533B)  
Rev.4.00  
Sep 07, 2005

### Features

- Low on-resistance  
 $R_{DS(on)} = 10 \text{ m}\Omega$  typ.
- 4 V gate drive devices.
- High speed switching

### Outline

RENESAS Package code: PRSS0003AE-A  
(Package name: TO-220C•FM)



1. Gate
2. Drain
3. Source

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	45	A
Drain peak current	I <sub>D(pulse)</sub> <sup>Note1</sup>	180	A
Body-drain diode reverse drain current	I <sub>DR</sub>	45	A
Channel dissipation	P <sub>ch</sub> <sup>Note2</sup>	30	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10μs, duty cycle ≤ 1 %

2. Value at Tc = 25°C

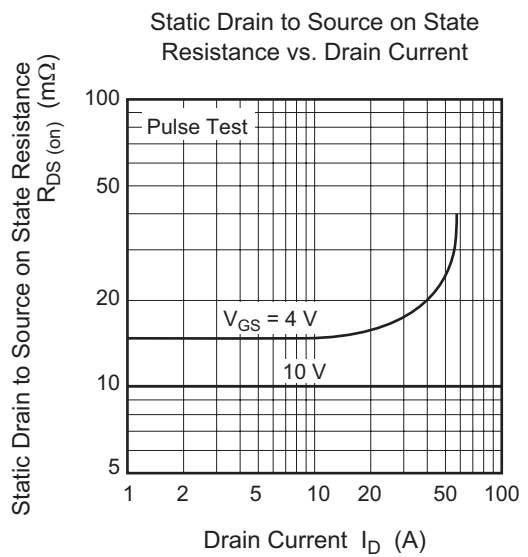
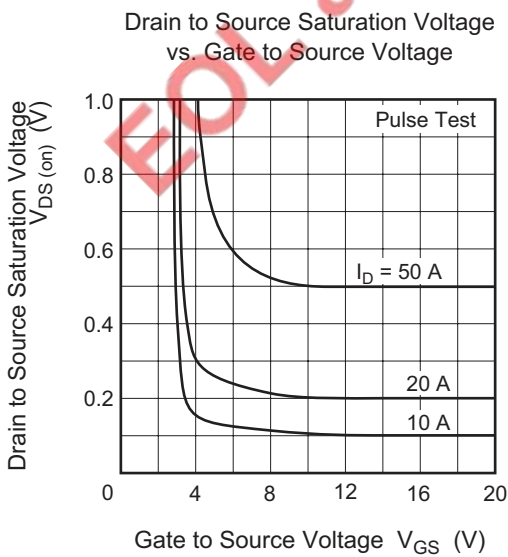
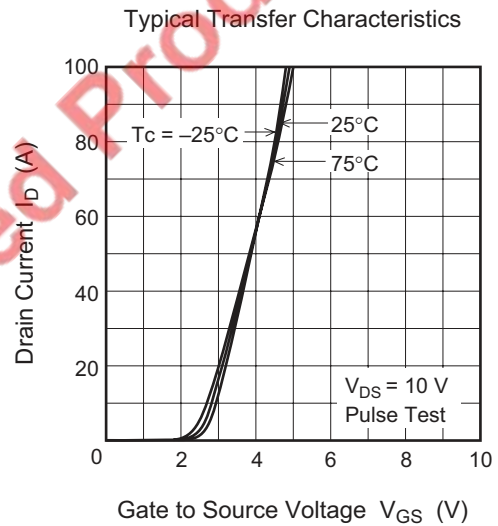
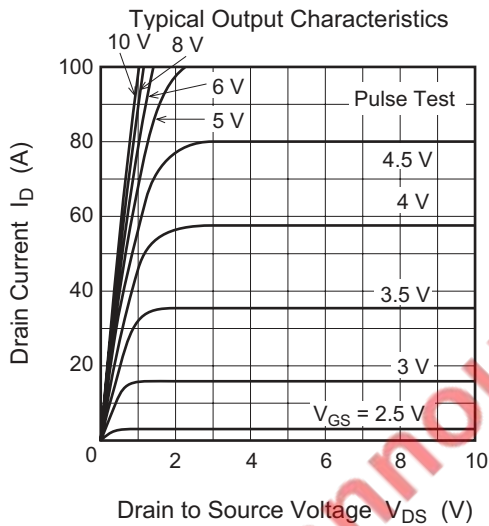
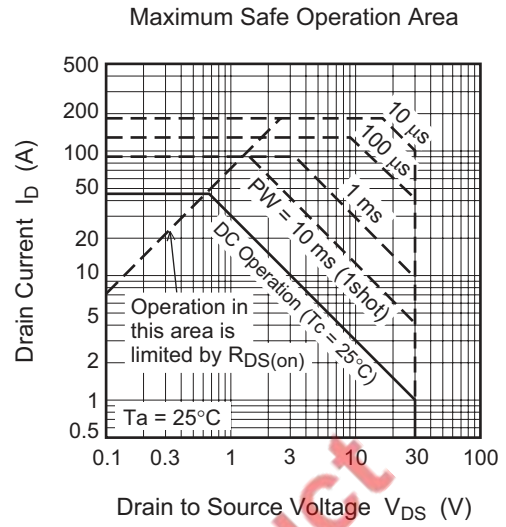
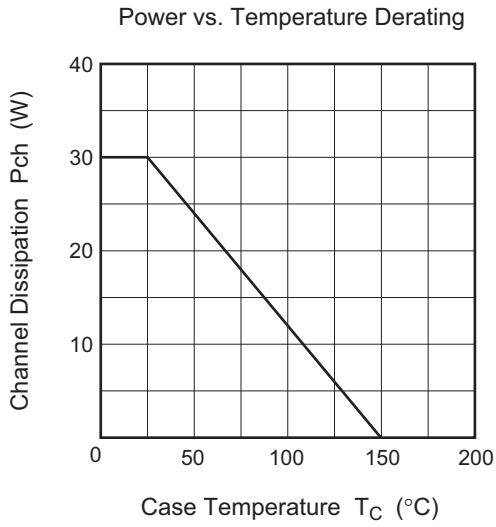
## Electrical Characteristics

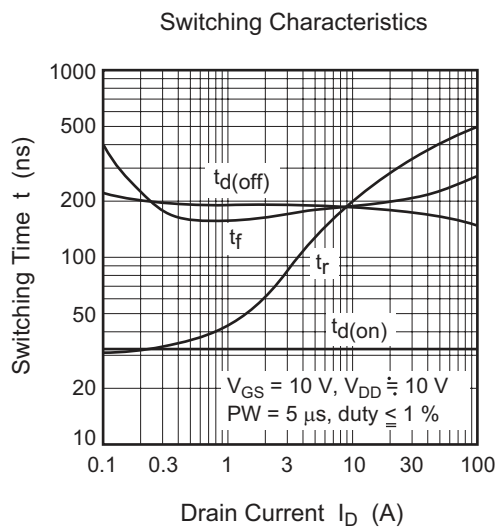
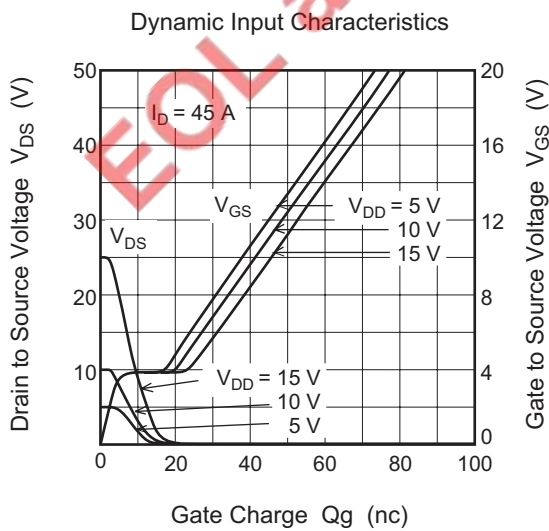
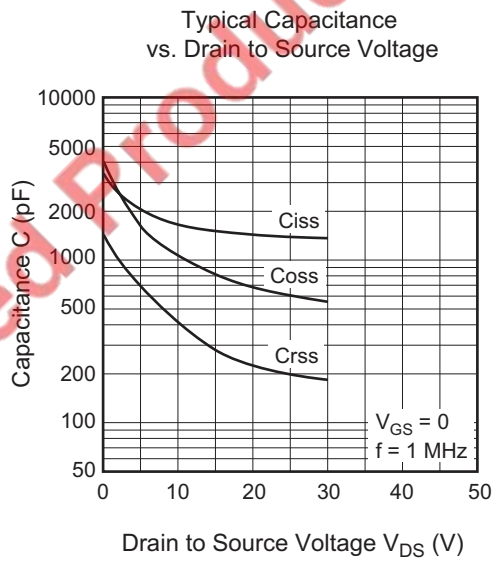
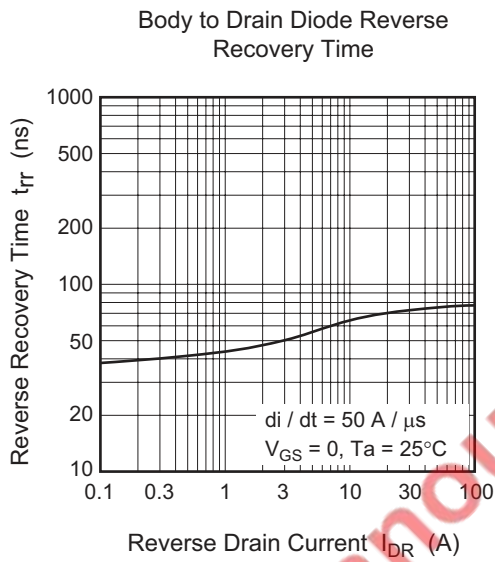
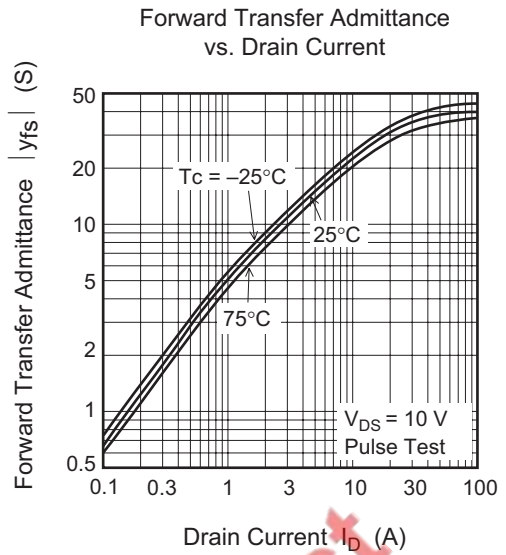
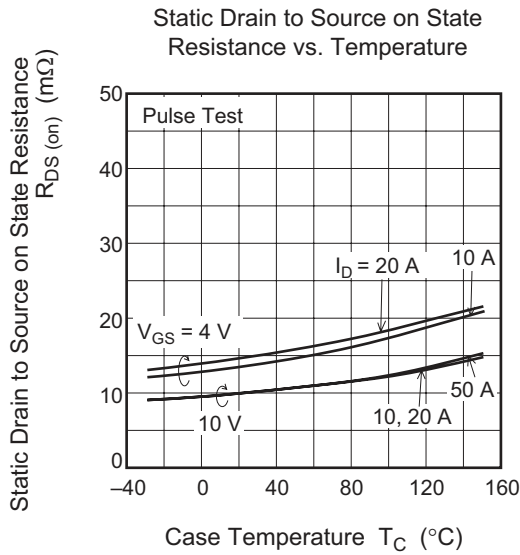
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	—	2.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V <sup>Note3</sup>
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	10	14	mΩ	I <sub>D</sub> = 20 A, V <sub>GS</sub> = 10 V <sup>Note3</sup>
	R <sub>DS(on)</sub>	—	15	25	mΩ	I <sub>D</sub> = 20 A, V <sub>GS</sub> = 4 V <sup>Note3</sup>
Forward transfer admittance	y <sub>fs</sub>	20	30	—	S	I <sub>D</sub> = 20 A, V <sub>DS</sub> = 10 V <sup>Note3</sup>
Input capacitance	C <sub>iss</sub>	—	1570	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1MHz
Output capacitance	C <sub>oss</sub>	—	1100	—	pF	
Reverse transfer capacitance	C <sub>rss</sub>	—	410	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	32	—	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, R <sub>L</sub> = 0.5 Ω
Rise time	t <sub>r</sub>	—	300	—	ns	
Turn-off delay time	t <sub>d(off)</sub>	—	180	—	ns	
Fall time	t <sub>f</sub>	—	200	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>	—	1.0	—	V	I <sub>F</sub> = 45 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	75	—	ns	I <sub>F</sub> = 45 A, V <sub>GS</sub> = 0 di <sub>F</sub> /dt = 50A/μs

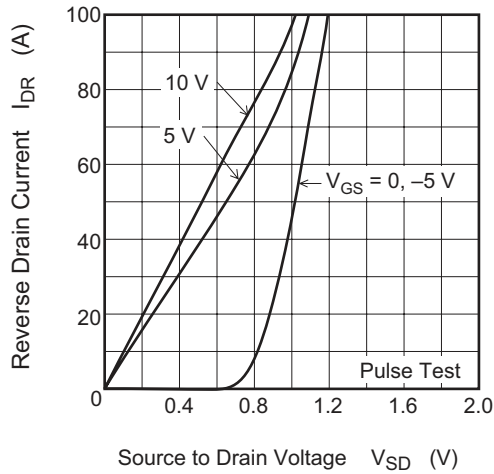
Note: 3. Pulse test

Main Characteristics

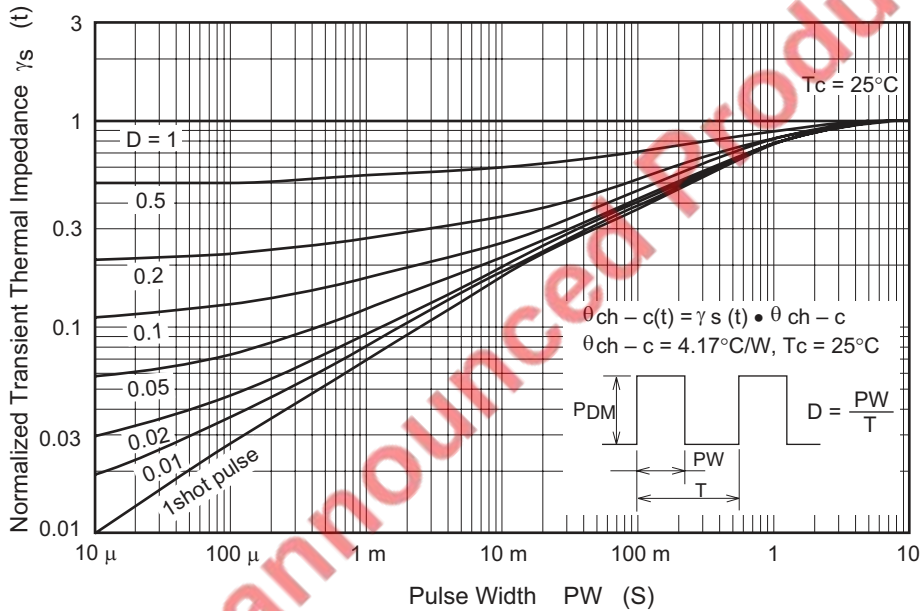




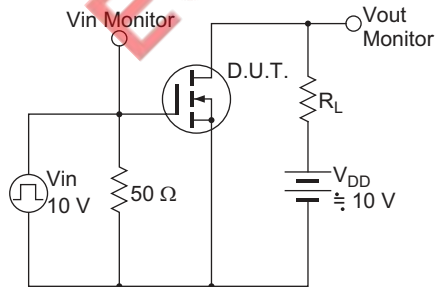
Reverse Drain Current vs. Source to Drain Voltage



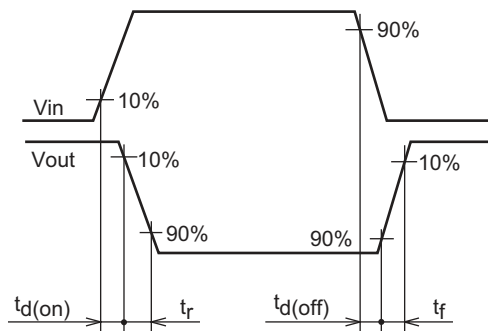
Normalized Transient Thermal Impedance vs. Pulse Width



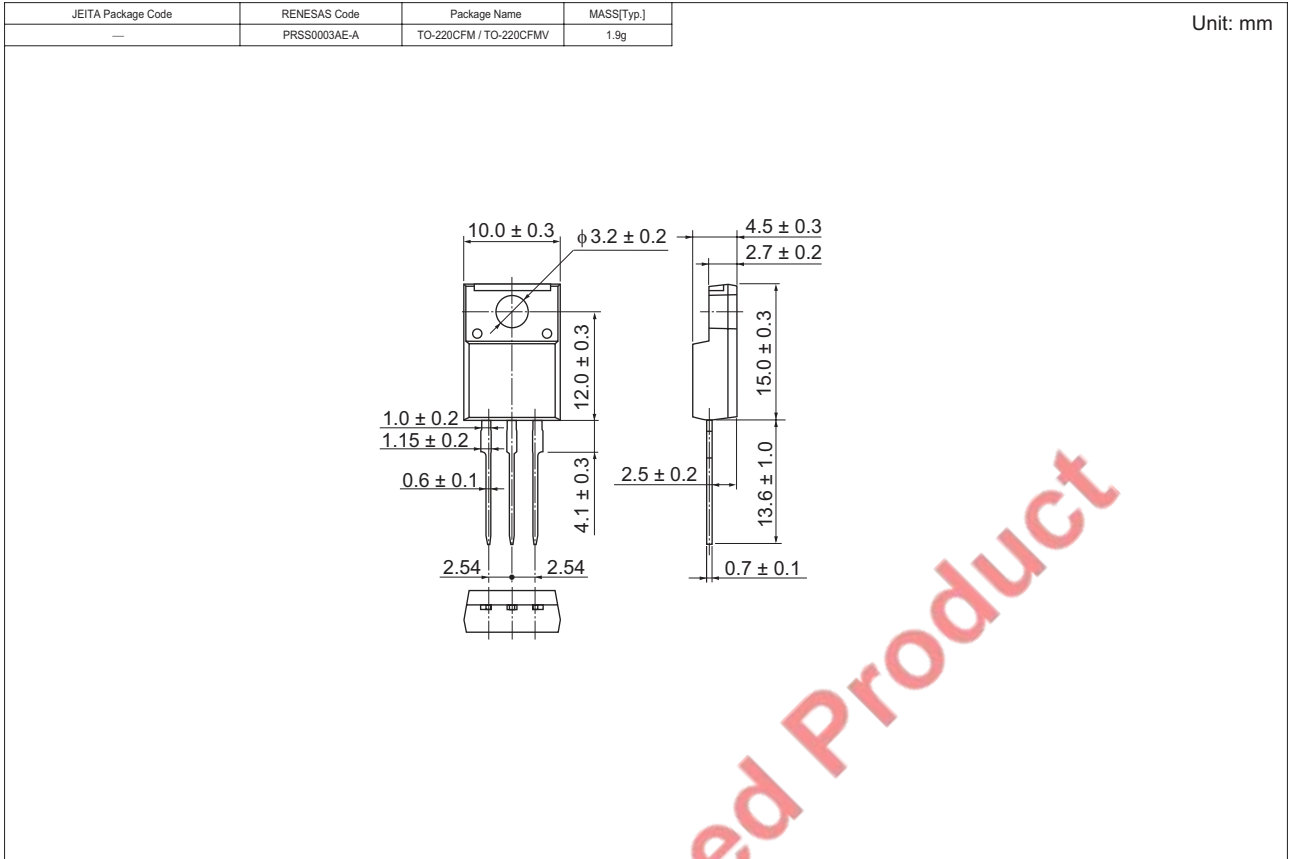
Switching Time Test Circuit



Waveform



### Package Dimensions



### Ordering Information

Part Name	Quantity	Shipping Container
2SK2737-E	50 pcs	Plastic magazine

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Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

#### Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.  
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

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7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong  
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#### Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan  
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

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1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
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#### Renesas Technology Malaysia Sdn. Bhd.

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