



Product Summary

BV _{DSS}	R _{DS(ON) Max}	Ι _D T _A = +25°C
-60V	250mΩ @ V_{GS} = -10V	-2.1A
-00 V	$300m\Omega @ V_{GS} = -4.5V$	-1.9A

Description

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- **DC-DC Converters**
- **Power Management Functions**
- Uninterrupted Power Supply

Features and Benefits

- Low Gate Drive
- Low Input Capacitance
- Fast Switching Speed •
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

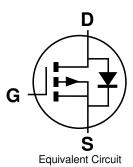
- Case: SOT223 •
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.112 grams (Approximate)



SOT223

S D D G

Pin Out - Top View



Ordering Information (Note 4)

Part Number Qualification		Qualification	Case	Packaging	
	DMP6250SE-13	Standard	SOT223	2,500 / Tape & Reel	
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.					

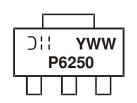
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



∃ = Manufacturer's Marking P6250 = Marking Code YWW = Date Code Marking \overline{Y} or Y = Year (ex: 7 = 2017) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	-60	V	
Gate-Source Voltage (Note 5)		V _{GS}	±20	V
	T _A = +25°C T _A = +70°C	I _D	-2.1 -1.7 -6.1 -4.9	A
Continuous Drain Current (Note 6) $V_{GS} = -10V$	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	I _D		
Maximum Body Diode Continuous Current	ls	-1.8	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-11	А	
Single Pulsed Avalanche Current (Note 7) L = 0.1mH	I _{AS}	-12	A	
Single Pulsed Avalanche Energy (Note 7) L = 0.1mH		E _{AS}	8	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Dower Dissinction (Note 6)	$T_A = +25^{\circ}C$	D	1.8	W
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.1	
Thermal Resistance, Junction to Ambient (Note 6)		R _{θJA}	69	°C/W
Total Power Dissipation (Note 6)	$T_{C} = +25^{\circ}C$	PD	14	W
Thermal Resistance, Junction to Case (Note 6)		R _{eJC}	8.7	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Symbol		·yp	max			
Drain-Source Breakdown Voltage	BV _{DSS}	-60			V	$I_D = -250 \mu A$, $V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	IDSS	_		-1	μA	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			128	250		$V_{GS} = -10V, I_D = -1.0A$	
Static Drain-Source On-Resistance			128	250	mΩ	$V_{GS} = -10V, I_D = -1.9A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	156	300	mΩ	$V_{GS} = -4.5V, I_D = -0.5A$	
			158	300		V _{GS} = -4.5V, I _D = -1.5A	
Diode Forward Voltage	V _{SD}	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -2.0A$	
DYNAMIC CHARACTERISTICS (Note 9)						·	
Input Capacitance	Ciss	_	551		pF	N 201/ 1/ 01/	
Output Capacitance	Coss	_	25.7		pF	− V _{DS} = -30V, V _{GS} = 0V − f= 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	19.1		pF		
Gate Resistance	Rg	_	12.1		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	4.8		nC		
Total Gate Charge (V _{GS} = -10V)	Qg	_	9.7		nC		
Gate-Source Charge	Q _{gs}	_	1.5		nC	$V_{DS} = -30V, I_D = -2A$	
Gate-Drain Charge	Q _{gd}	_	1.6		nC		
Turn-On Delay Time	t _{D(ON)}	_	6.3		ns		
Turn-On Rise Time	t _R	_	10.3		ns		
Turn-Off Delay Time	t _{D(OFF)}		91.4		ns	$R_G = 50\Omega, I_D = -1A$	
Turn-Off Fall Time	tF		39.8		ns	7	
Reverse Recovery Time	t _{RR}		9.2		ns		
Reverse Recovery Charge	Q _{RR}	_	3.9		nC	- I _S = -1A, di/dt= 100A/μs	

Notes: 5. AEC-Q101 V_{GS} maximum is ±16V.

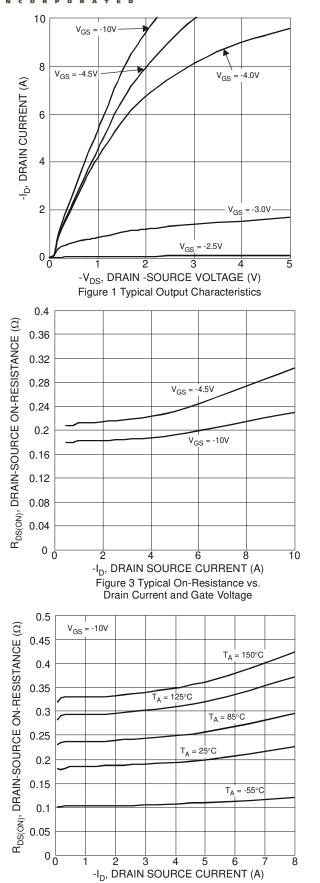
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

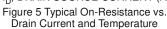
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.

8. Short duration pulse test used to minimize self-heating effect.

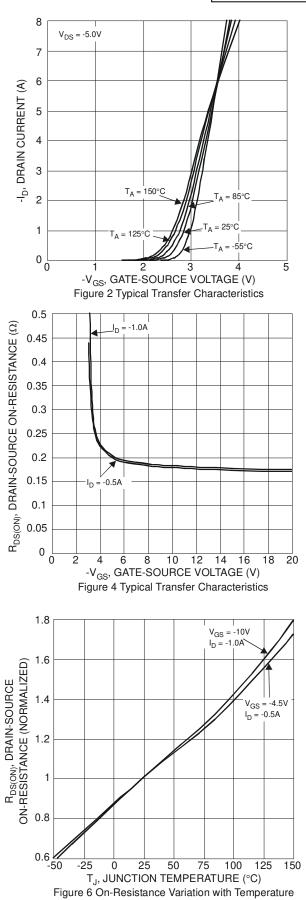
9. For design aid only, not subject to production testing.





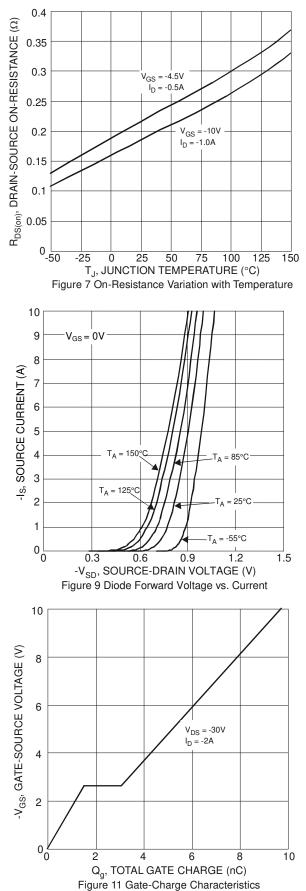


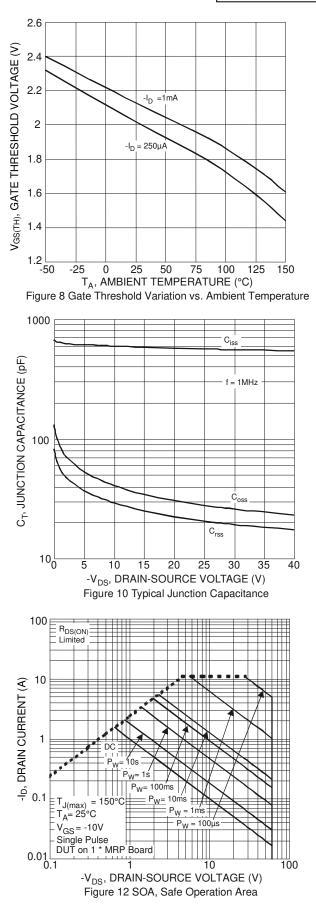




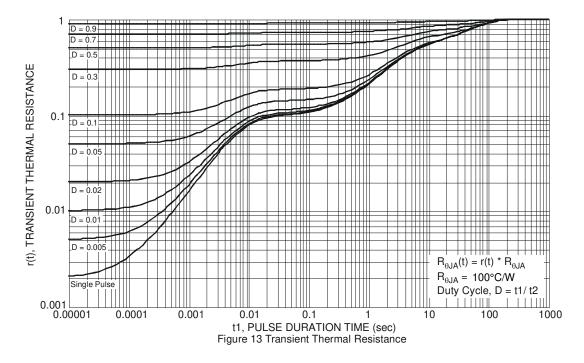








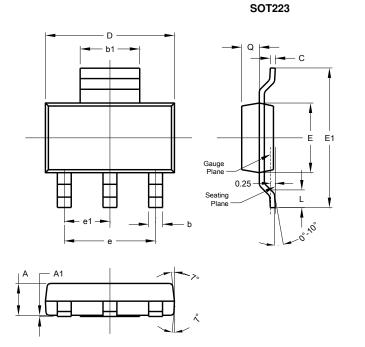






Package Outline Dimensions

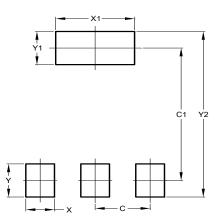
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT223					
Dim Min Max			Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
E	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All I	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

SOT223



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