# **ART1K6PH; ART1K6PHG**

## Power LDMOS transistor

**AMPLEON** 

Rev. 2 — 25 March 2021

Product data sheet

### 1. Product profile

#### 1.1 General description

Based on Advanced Rugged Technology (ART), this 1600 W LDMOS RF power transistor has been designed to cover a wide range of applications for ISM, broadcast and communications. The unmatched transistor has a frequency range of 1 MHz to 450 MHz.

Table 1. Application information

Test signal	f	V <sub>DS</sub>	PL	Gp	η <sub>D</sub>
	(MHz)	(V)	(W)	(dB)	(%)
CW pulsed [1][3]	108	50	1400	27.2	77.7
	108	55	1600	28.1	76.4
CW pulsed [2][3]	325	53	1520	20.2	73.6
CW [2]	325	53	1390	19.5	68.4

<sup>[1]</sup> Production circuit.

#### 1.2 Features and benefits

- High breakdown voltage enables class E operation at V<sub>DS</sub> = 48 V
- Suitable for V<sub>DS</sub> = 50 V and 55 V
- Qualified up to a maximum of V<sub>DS</sub> = 55 V
- Characterized from 30 V to 55 V for extended power range
- Easy power control
- Integrated dual sided ESD protection enables class C operation and complete switch off of the transistor
- Excellent ruggedness with no device degradation
- High efficiency
- Excellent thermal stability
- Designed for broadband operation
- For RoHS compliance see the product details on the Ampleon website

<sup>[2]</sup> Application circuit.

<sup>[3]</sup>  $t_p = 100 \,\mu\text{s}; \, \delta = 10 \,\%.$ 

### 1.3 Applications

- Industrial, scientific and medical applications
  - Plasma generators
  - MRI systems
  - ♦ CO<sub>2</sub> lasers
  - Particle accelerators
- Broadcast
  - FM radio
  - ♦ VHF TV
- Communications
  - ◆ Non cellular communications
  - ◆ UHF radar

### 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
ART1	K6PH (OMP-1230-4F-1)		
1	gate1		
2	gate2	4 3	4 . <u> </u>
3	drain2		1_
4	drain1		5
5	source [1]		
		1 2	' <u></u>
			amp01358
ART1	K6PHG (OMP-1230-4G-1	)	
1	gate1		
2	gate2	4 3	4
3	drain2		1
4	drain1	<u>a</u> e	5
5	source [1]	1 2	2
			' <u></u>
			3 amp01358

[1] Connected to flange.

### 3. Ordering information

Table 3. Ordering information

Package name	Orderable part number	12NC	Packing description	Min. orderable quantity (pieces)
OMP-1230-4F-1	ART1K6PHZ	9349 603 47517	Tray, 20-fold; dry pack	60
	ART1K6PHY	9349 603 47518	TR13; 100-fold; 56 mm; dry pack	100
OMP-1230-4G-1	ART1K6PHGZ	9349 604 28517	Tray, 20-fold; dry pack	60
	ART1K6PHGY	9349 604 28518	TR13; 100-fold; 56 mm; dry pack	100

### 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	177	V
$V_{GS}$	gate-source voltage		-6	+11	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

<sup>[1]</sup> Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

#### 5. Thermal characteristics

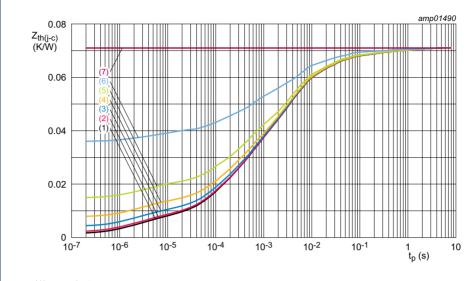
Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Тур	Unit
R <sub>th(j-c)</sub>	thermal resistance from junction to case	T <sub>j</sub> = 150 °C	[1][2]	0.071	K/W
$Z_{\text{th(j-c)}}$	transient thermal impedance from junction to case	$T_j = 150  ^{\circ}\text{C}; t_p = 100  \mu\text{s}; \\ \delta = 10  \%$		0.02	K/W

<sup>[1]</sup>  $T_i$  is the junction temperature.

<sup>[2]</sup>  $R_{th(j-c)}$  is measured under RF conditions.

<sup>[3]</sup> See Figure 1.



- (1)  $\delta = 1 \%$
- (2)  $\delta = 2 \%$
- (3)  $\delta = 5 \%$
- (4)  $\delta = 10 \%$
- (5)  $\delta = 20 \%$
- (6)  $\delta = 50 \%$
- (7)  $\delta = 100 \% (DC)$

Fig 1. Transient thermal impedance from junction to case as a function of pulse duration

### 6. Characteristics

Table 6. DC characteristics

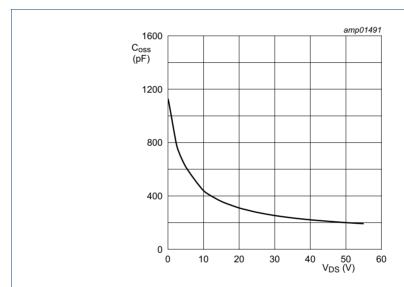
 $T_i = 25$  °C; per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 5.5 \text{ mA}$	177	-	-	٧
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 20 \text{ V}; I_D = 550 \text{ mA}$	1.5	2.1	2.5	٧
I <sub>DSS</sub>	drain leakage current	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 50 V	-	-	0.96	μА
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 20 \text{ V}$	-	76	-	Α
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V	-	-	60	nA
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 19.25 \text{ A}$	-	0.090	-	Ω

Table 7. AC characteristics

 $T_i = 25$  °C; per section unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C <sub>rs</sub>	feedback capacitance	V <sub>GS</sub> = 0 V; f = 1 MHz				
		V <sub>DS</sub> = 50 V	-	3.12	-	pF
		V <sub>DS</sub> = 55 V	-	3.06	-	pF
C <sub>iss</sub>	input capacitance	V <sub>GS</sub> = 0 V; f = 1 MHz				
		V <sub>DS</sub> = 50 V	-	632	-	pF
		V <sub>DS</sub> = 55 V	-	632	-	pF
Coss	output capacitance	$V_{GS} = 0 V; f = 1 MHz$				
		V <sub>DS</sub> = 50 V	-	200	-	pF
		V <sub>DS</sub> = 55 V	-	192	-	pF



 $V_{GS} = 0 V$ ; f = 1 MHz

Fig 2. Output capacitance as a function of drain-source voltage; typical values per section

#### Table 8. RF characteristics

Test signal: pulsed RF;  $t_p$  = 100  $\mu$ s;  $\delta$  = 5 %; f = 108 MHz; RF performance at  $V_{DS}$  = 55 V;  $I_{Dq}$  = 50 mA per section;  $T_{case}$  = 25 °C; unless otherwise specified; in a class-AB production test circuit.

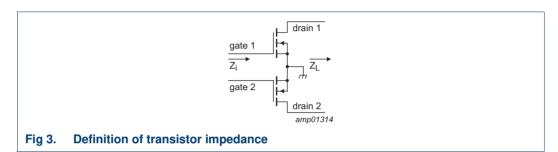
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	P <sub>L</sub> = 1600 W	26	27.4	-	dB
RLin	input return loss	P <sub>L</sub> = 1600 W	-	-16.1	-	dB
$\eta_{D}$	drain efficiency	P <sub>L</sub> = 1600 W	71	74.7	-	%

### 7. Test information

#### 7.1 Ruggedness in class-AB operation

The ART1K6PH and ART1K6PHG are capable of withstanding a load mismatch corresponding to VSWR  $\geq 65$ : 1 through all phases under the following conditions:  $P_L = 1400$  W pulsed at  $V_{DS} = 50$  V and  $P_L = 1600$  W pulsed at  $V_{DS} = 55$  V;  $I_{Dq} = 100$  mA per section;  $t_D = 100~\mu s$ ;  $\delta = 10~\%$ ; f = 108 MHz.

#### 7.2 Impedance information



**Table 9. Typical push-pull impedance** Simulated  $Z_i$  and  $Z_L$  device impedance.

f	Z <sub>i</sub>	Z <sub>L</sub>	$P_L$	
(MHz)	<b>(</b> Ω <b>)</b>	<b>(</b> Ω <b>)</b>	(W)	
$V_{DS} = 50 \text{ V}$				
108	2.4 – j8.5	3.3 + j0.7	1400	
V <sub>DS</sub> = 55 V				
108	2.4 – j8.5	3.5 + j0.8	1600	

#### 7.3 Test circuit

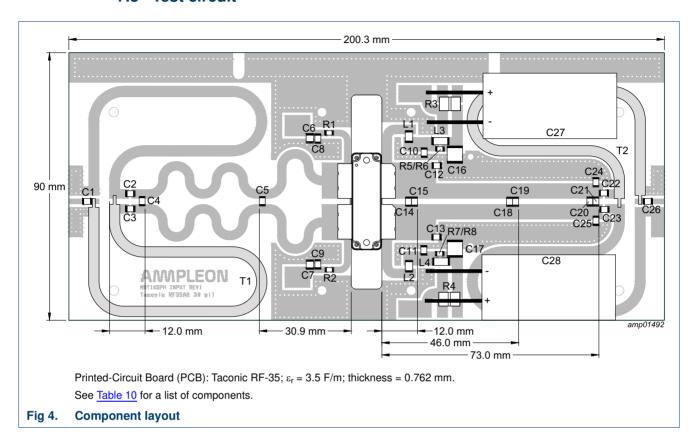


Table 10. List of components

For test circuit see Figure 4.

Component	Description	Value	Remarks
C1, C26	multilayer ceramic chip capacitor	470 pF [1	
C2, C3	multilayer ceramic chip capacitor	68 pF [1	
C4	multilayer ceramic chip capacitor	43 pF [1	
C5	multilayer ceramic chip capacitor	300 pF	
C6, C7	multilayer ceramic chip capacitor	4.7 μF, 50 V	Murata: GRM32ER71H475KA88L
C8, C9, C10, C11	multilayer ceramic chip capacitor	920 pF [1	
C12, C13	multilayer ceramic chip capacitor	180 pF [1	
C14, C15	multilayer ceramic chip capacitor	39 pF [1	
C16, C17	multilayer ceramic chip capacitor	4.7 μF, 100 V	TDK: C5750X7R2A475KT/A
C18, C19	multilayer ceramic chip capacitor	56 pF [1	
C20, C21	multilayer ceramic chip capacitor	51 pF [1	
C22, C23	multilayer ceramic chip capacitor	120 pF [1	
C24, C25	multilayer ceramic chip capacitor	20 pF [1	
C27, C28	electrolytic capacitor	2200 μF, 100 V	
L1, L2	air inductor	47 nH	Coilcraft: 1515SQ-47N
L3, L4	air inductor	82 nH	Coilcraft: 1515SQ-82N
R1, R2	resistor	4.7 kΩ	SMD 1206

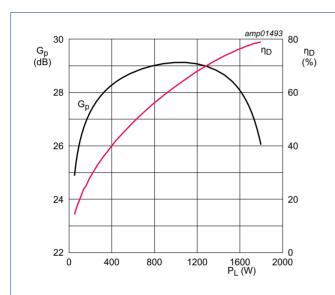
Table 10. List of components ... continued

For test circuit see Figure 4.

Component	Description	Value	Remarks
R3, R4	resistor	0.01 Ω	Vishay: WSHP2818
R5, R6, R7, R8	resistor	9.1 Ω	SMD 1206
T1, T2	semi rigid coax	50 Ω, 160 mm	EZ141-AL-TP/M17

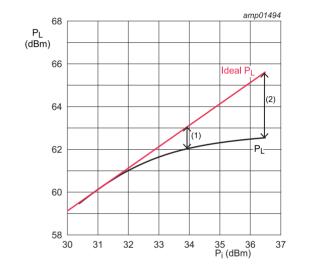
[1] American Technical Ceramics type 100B or capacitor of same quality.

#### 7.4 Graphical data



 $V_{DS}$  = 55 V;  $I_{Dq}$  = 100 mA per section; f = 108 MHz;  $t_p$  = 100  $\mu s;$   $\delta$  = 10 %.

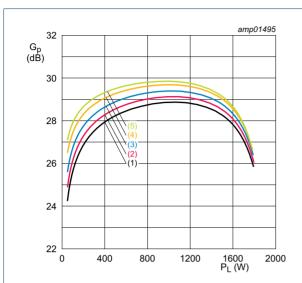
Fig 5. Power gain and drain efficiency as function of output power; typical values



 $V_{DS}$  = 55 V;  $I_{Dq}$  = 100 mA per section; f = 108 MHz;  $t_p$  = 100  $\mu s;$   $\delta$  = 10 %.

- (1)  $P_{L(1dB)} = 62.03 dBm (1600 W)$
- (2)  $P_{L(3dB)} = 62.54 \text{ dBm } (1800 \text{ W})$

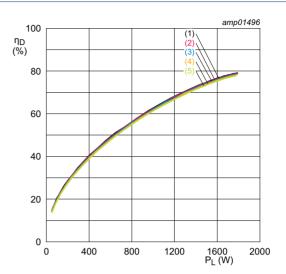
Fig 6. Output power as a function of input power; typical values



 $V_{DS} = 55 \text{ V; } f = 108 \text{ MHz; } t_p = 100 \text{ } \mu\text{s; } \delta = 10 \text{ } \%.$ 

- (1)  $I_{Dq} = 50 \text{ mA per section}$
- (2)  $I_{Da} = 100 \text{ mA per section}$
- (3)  $I_{Dq} = 200 \text{ mA per section}$
- (4)  $I_{Dq} = 400 \text{ mA per section}$
- (5)  $I_{Dq} = 600 \text{ mA per section}$

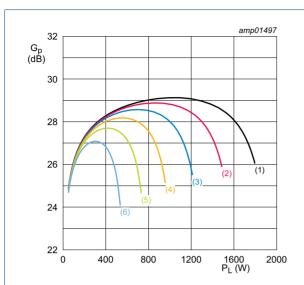
Fig 7. Power gain as a function of output power; typical values



 $V_{DS}$  = 55 V; f = 108 MHz;  $t_p$  = 100  $\mu s;$   $\delta$  = 10 %.

- (1)  $I_{Dq} = 50 \text{ mA per section}$
- (2)  $I_{Dq} = 100 \text{ mA per section}$
- (3)  $I_{Dq} = 200 \text{ mA per section}$
- (4)  $I_{Dq} = 400 \text{ mA per section}$
- (5)  $I_{Dq} = 600 \text{ mA per section}$

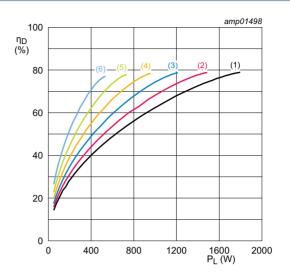
Fig 8. Drain efficiency as a function of output power; typical values



 $I_{Dq}$  = 100 mA per section; f = 108 MHz;  $t_p$  = 100  $\mu s;$   $\delta$  = 10 %.

- (1)  $V_{DS} = 55 \text{ V}$
- (2)  $V_{DS} = 50 \text{ V}$
- (3)  $V_{DS} = 45 \text{ V}$
- (4)  $V_{DS} = 40 \text{ V}$
- (5)  $V_{DS} = 35 \text{ V}$
- (6)  $V_{DS} = 30 \text{ V}$

Fig 9. Power gain as a function of output power; typical values



 $I_{Dq}$  = 100 mA per section; f = 108 MHz;  $t_p$  = 100  $\mu s$ ;  $\delta$  = 10 %.

- (1)  $V_{DS} = 55 \text{ V}$
- (2)  $V_{DS} = 50 \text{ V}$
- (3)  $V_{DS} = 45 \text{ V}$
- $(4) \quad V_{DS} = 40 \ V$
- (5)  $V_{DS} = 35 \text{ V}$
- (6)  $V_{DS} = 30 \text{ V}$

Fig 10. Drain efficiency as a function of output power; typical values

### 8. Package outline

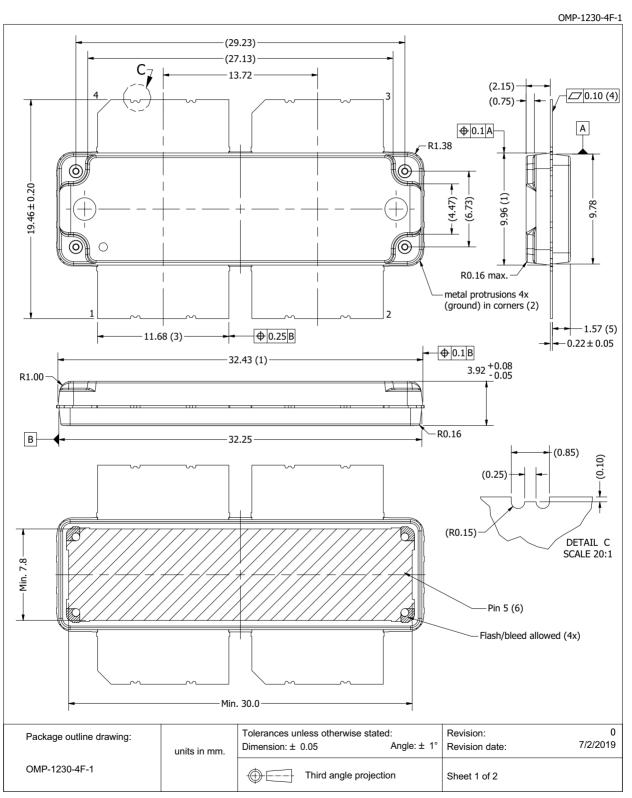
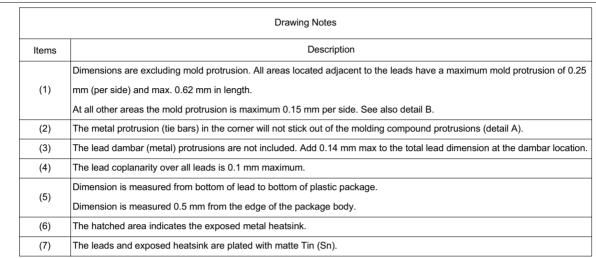


Fig 11. Package outline OMP-1230-4F-1 (sheet 1 of 2)

OMP-1230-4F-1



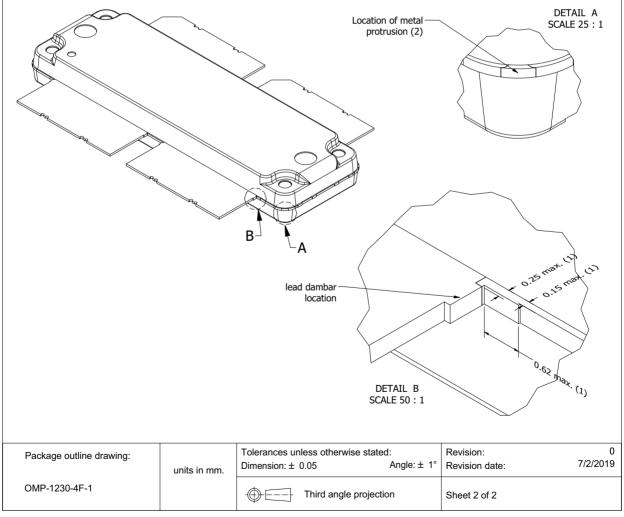


Fig 12. Package outline OMP-1230-4F-1 (sheet 2 of 2)

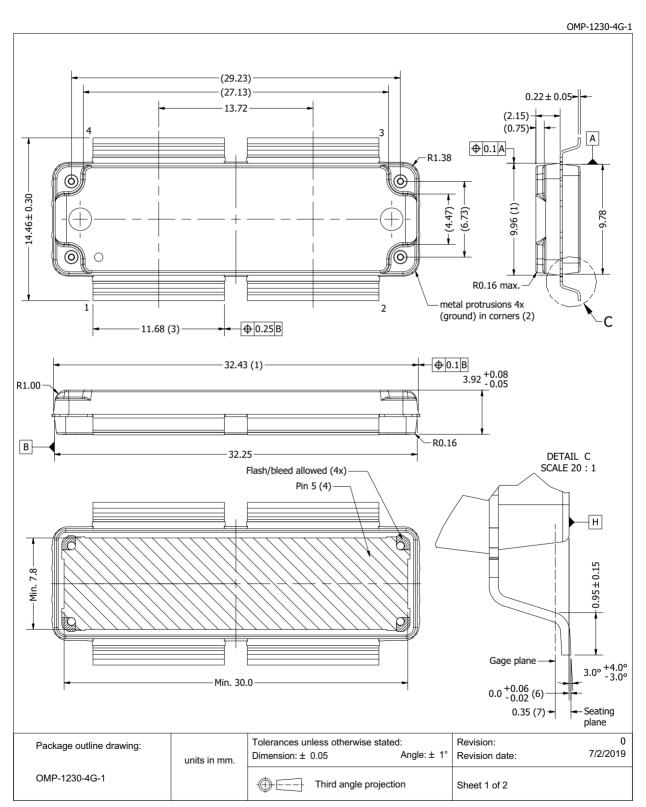
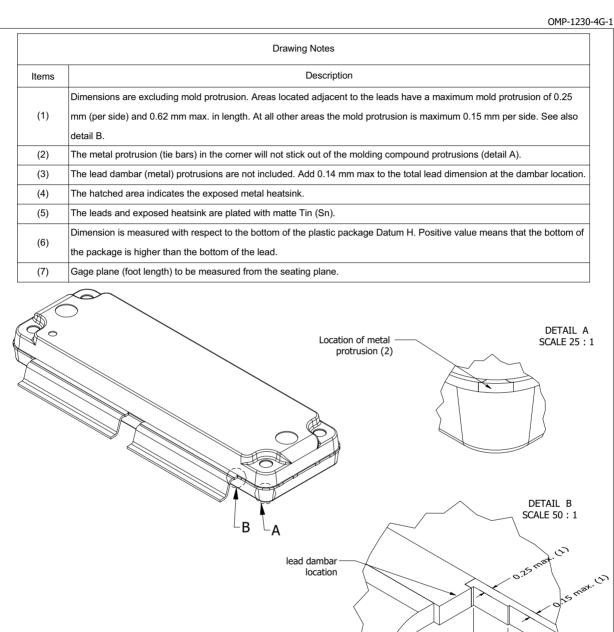


Fig 13. Package outline OMP-1230-4G-1 (sheet 1 of 2)



			0.65 Wax (1)
Package outline drawing:	units in mm.	Tolerances unless otherwise stated: Dimension: ± 0.05 Angle: ± 1°	Revision: 0 Revision date: 7/2/2019
OMP-1230-4G-1		Third angle projection	Sheet 2 of 2

Fig 14. Package outline OMP-1230-4G-1 (sheet 2 of 2)

### 9. Handling information

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 11. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

- [1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.
- [2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

#### 10. Abbreviations

Table 12. Abbreviations

Acronym	Description		
CW	Continuous Wave		
ESD	ElectroStatic Discharge		
FM	Frequency Modulation		
ISM	Industrial, Scientific and Medical		
LDMOS	Laterally Diffused Metal-Oxide Semiconductor		
MRI	Magnetic Resonance Imaging		
MTF	Median Time to Failure		
RoHS	Restriction of Hazardous Substances		
SMD	Surface Mounted Device		
UHF	Ultra High Frequency		
VHF	Very High Frequency		
VSWR	Voltage Standing Wave Ratio		

### 11. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
ART1K6PH_ART1K6PHG v.1	20210325	Product data sheet	-	ART1K6PH v.1
Modifications:	The document now describes the straight lead and gull wing version of this product			
	Section 2 on page 2: added ART1K6PHG data			
	Section 3 on page 3: added ART1K6PHG data			
	Section 7.1 on page 6: added ART1K6PHG to text			
	Section 8 on page 11: added package outline version OMP-1230-4G-1			-4G-1
ART1K6PH v.1	20201019	Product data sheet	-	-

ART1K6PH\_ART1K6PHG

## ART1K6PH; ART1K6PHG

**Power LDMOS transistor** 

### 12. Legal information

#### 12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.ampleon.com">http://www.ampleon.com</a>.

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## ART1K6PH; ART1K6PHG

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### **AMPLEON**

# ART1K6PH; ART1K6PHG

**Power LDMOS transistor** 

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