

PSMN022-30BL

N-channel 30 V 22.6 mΩ logic level MOSFET in D2PAK Rev. 1 — 21 March 2012 Product de

Product data sheet

1. **Product profile**

1.1 General description

Logic level N-channel MOSFET in D2PAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Suitable for logic level gate drive sources

1.3 Applications

- DC-to-DC converters
- Load switiching

- Motor control
- Server power supplies

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	30	V
I _D	drain current	T_{mb} = 25 °C; V_{GS} = 10 V; see <u>Figure 1</u>	-	-	30	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	41	W
Tj	junction temperature		-55	-	175	°C
Static characte	eristics					
R _{DSon} drain-source on-state resistance		$V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 100 \text{ °C};$ see Figure 13	-	26.84	31.6	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 25 \text{ °C};$ see Figure 12	-	19.17	22.6	mΩ
Dynamic chara	acteristics					
Q_{GD}	gate-drain charge	$V_{GS} = 4.5 \text{ V}; I_D = 5 \text{ A}; V_{DS} = 15 \text{ V};$	-	1.4	-	nC
Q _{G(tot)}	total gate charge	see Figure 14; see Figure 15	-	4.4	-	nC
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 30 A; $V_{sup} \le$ 30 V; R_{GS} = 50 Ω ; unclamped	-	-	7	mJ



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain[1]	mb	D
3	S	source		
mb	D	mounting base; connected to drain		mbb076 S
			SOT404 (D2PAK)	

^[1] It is not possible to make connection to pin 2.

3. Ordering information

Table 3. Ordering information

Type number Package			
	Name	Description	Version
PSMN022-30BL	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

4. Marking

Table 4. Marking codes

Type number	Marking code
PSMN022-30BL	PSMN022-30BL

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	30	٧
V_{DGR}	drain-gate voltage	$T_j \ge 25$ °C; $T_j \le 175$ °C; $R_{GS} = 20$ kΩ	-	30	٧
V_{GS}	gate-source voltage		-20	20	V
I _D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 100 \text{ °C}; \text{see } \frac{\text{Figure 1}}{\text{Model}}$	-	22	Α
		V_{GS} = 10 V; T_{mb} = 25 °C; see Figure 1	-	30	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 ^{\circ}C$; see Figure 3	-	125	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	41	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
T _{sld(M)}	peak soldering temperature		-	260	°C
Source-drain	n diode				
Is	source current	$T_{mb} = 25 ^{\circ}C$	-	30	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	125	Α
Avalanche ru	uggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 30 A; V_{sup} ≤ 30 V; R_{GS} = 50 Ω ; unclamped	-	7	mJ

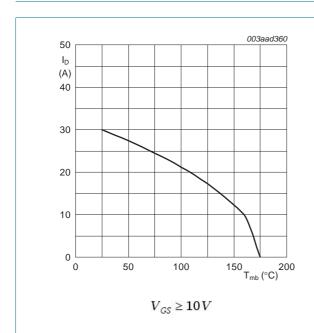


Fig 1. Continuous drain current as a function of mounting base temperature

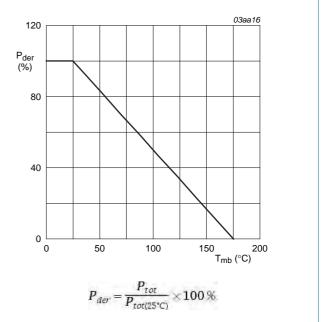
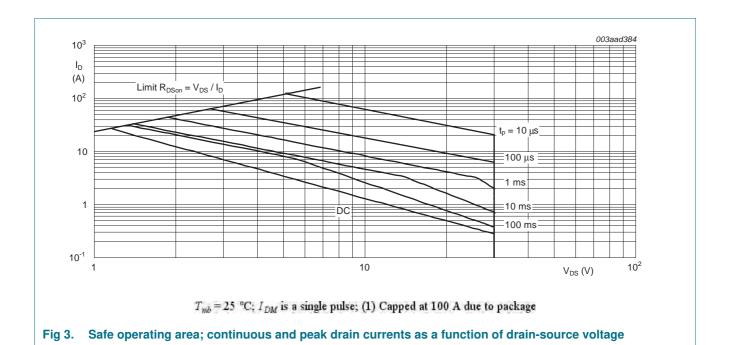


Fig 2. Normalized total power dissipation as a function of mounting base temperature

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6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	3.1	3.6	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	minimum footprint; mounted on a printed circuit board	-	50	-	K/W

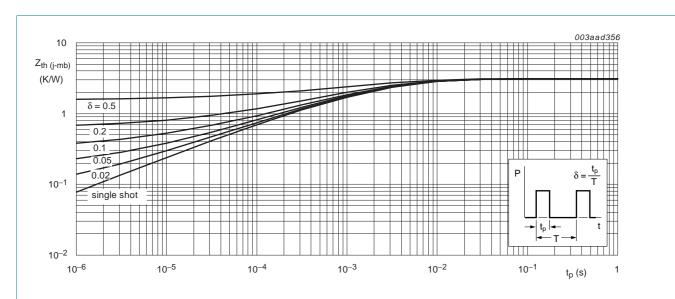


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration; typical values

PSMN022-30BL

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7. Characteristics

Table 7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
$V_{(BR)DSS}$	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	30	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	27	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 25$ °C; see <u>Figure 10</u> ; see <u>Figure 11</u>	1.3	1.7	2.15	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = 175 °C; see Figure 11	0.5	-	-	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = -55 °C; see <u>Figure 11</u>	-	-	2.45	V
I _{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.3	1	μΑ
		$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 125 \text{ °C}$	-	-	50	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 16 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nA
		$V_{GS} = -16 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nA
D0011	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 5 \text{ A}; T_j = 25 \text{ °C};$ see <u>Figure 12</u>	-	25.17	29.6	mΩ
		$V_{GS} = 10 \text{ V}$; $I_D = 5 \text{ A}$; $T_j = 175 ^{\circ}\text{C}$; see Figure 13; see Figure 12	-	50.99	60	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 100 \text{ °C};$ see <u>Figure 13</u>	-	26.84	31.6	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 12</u>	-	19.17	22.6	mΩ
R _G	gate resistance	f = 1 MHz	-	2	-	Ω
Dynamic	characteristics					
$Q_{G(tot)} \qquad \qquad total \ gate \ charge$		$I_D = 5 \text{ A}$; $V_{DS} = 15 \text{ V}$; $V_{GS} = 10 \text{ V}$; see Figure 14; see Figure 15	-	9	-	nC
		$I_D = 0 A; V_{DS} = 0 V; V_{GS} = 10 V$	-	8	-	nC
		$I_D = 5 \text{ A}; V_{DS} = 15 \text{ V}; V_{GS} = 4.5 \text{ V};$	-	4.4	-	nC
Q _{GS}	gate-source charge	see Figure 14; see Figure 15	-	1.6	-	nC
Q _{GS(th)}	pre-threshold gate-source charge		-	8.0	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	0.8	-	nC
Q_{GD}	gate-drain charge		-	1.4	-	nC
V _{GS(pl)}	gate-source plateau voltage	I _D = 5 A; V _{DS} = 15 V; see <u>Figure 14</u> ; see <u>Figure 15</u>	-	3	-	V
C _{iss}	input capacitance	$V_{DS} = 15 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	447	-	pF
Coss	output capacitance	T _j = 25 °C; see <u>Figure 16</u>	-	96	-	pF
C _{rss}	reverse transfer capacitance		-	61	-	pF

Table 7. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$t_{d(on)}$	turn-on delay time	$V_{DS} = 15 \text{ V}; R_L = 1.5 \Omega; V_{GS} = 4.5 \text{ V};$	-	12	-	ns
t _r	rise time	$R_{G(ext)} = 4.7 \Omega$	-	29	-	ns
$t_{d(off)}$	turn-off delay time		-	17	-	ns
t _f	fall time		-	7	-	ns
Source-drai	in diode					
V_{SD}	source-drain voltage	$I_S = 5 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 17	-	0.7	1.2	V
t _{rr}	reverse recovery time	$I_S = 5 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V}; V_{DS} = 15 \text{ V}$	-	22	-	ns
Q _r	recovered charge		-	10	-	nC

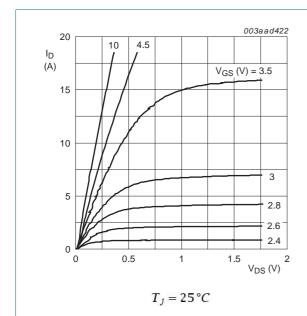


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

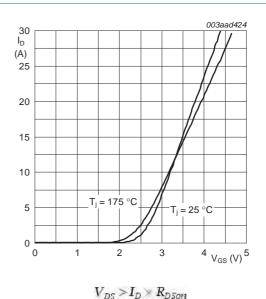


Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values

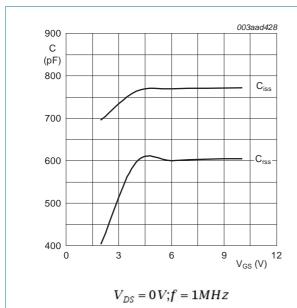


Fig 7. Input and reverse transfer capacitances as a function of gate-source voltage; typical values

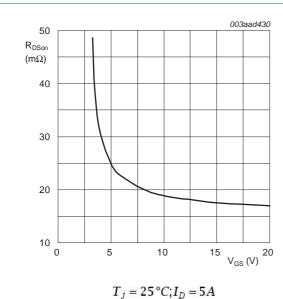
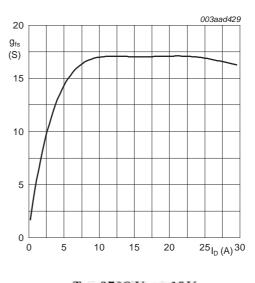
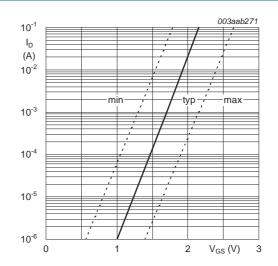


Fig 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



 $T_j = 25 \,^{\circ}C; V_{DS} = 10 \, V$

Fig 8. Forward transconductance as a function of drain current; typical values



$$T_j = 25 \,{}^{\circ}C; V_{DS} = 5V$$

Fig 10. Sub-threshold drain current as a function of gate-source voltage

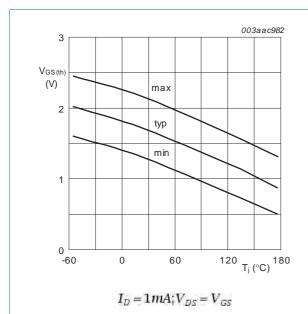


Fig 11. Gate-source threshold voltage as a function of junction temperature

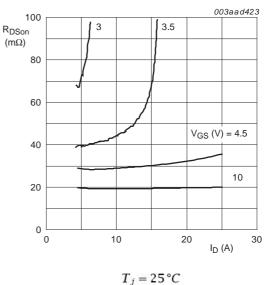


Fig 12. Drain-source on-state resistance as a function of drain current; typical values

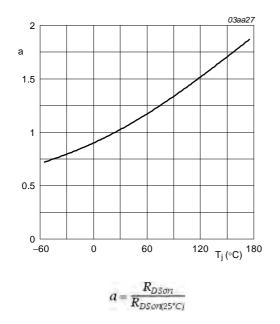


Fig 13. Normalized drain-source on-state resistance factor as a function of junction temperature

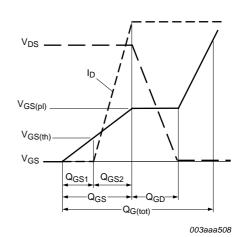


Fig 14. Gate charge waveform definitions

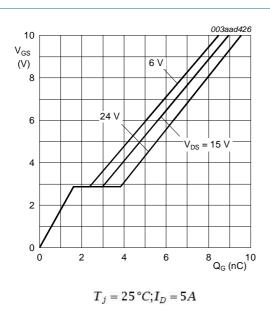
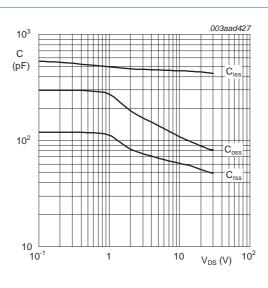
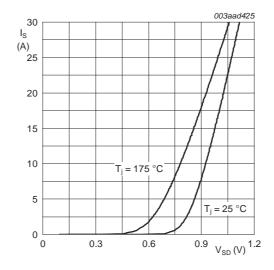


Fig 15. Gate-source voltage as a function of gate charge; typical values



 $V_{GS} = 0V; f = 1MHz$

Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



 $V_{GS} = 0 V$

Fig 17. Source current as a function of source-drain voltage;

8. Package outline

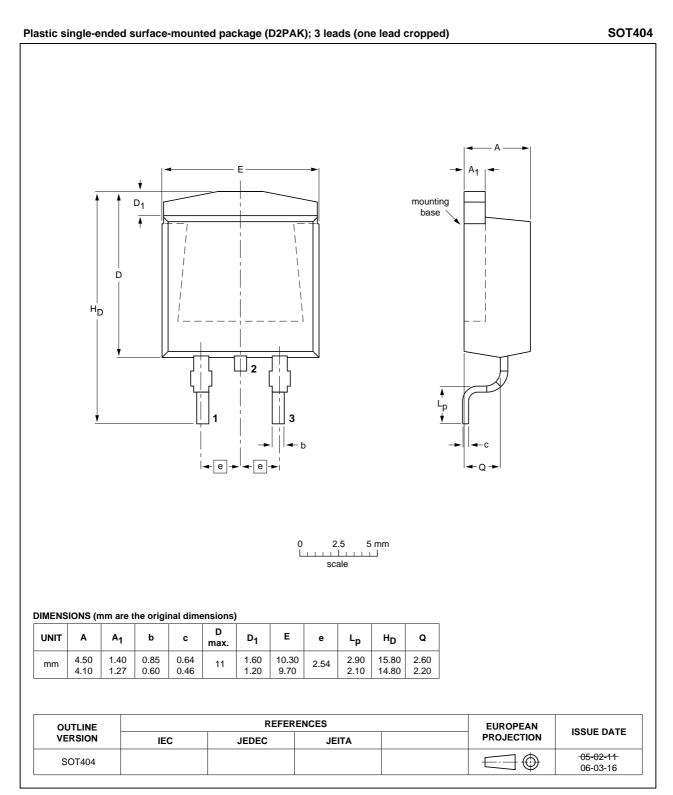


Fig 18. Package outline SOT404 (D2PAK)

9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN022-30BL v.1	20120321	Product data sheet	-	-

10. Legal information

10.1 Data sheet status

Document status[1] [2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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