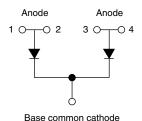


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Vishay Semiconductors

Not Insulated SOT-227 Power Module Ultrafast Rectifier, 310 A





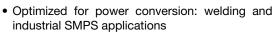
PRIMARY CHARACTERISTICS						
V _R	400 V					
I _{F(AV)} at T _C = 119 °C per module ⁽¹⁾	310 A					
t _{rr}	39 ns					
at T _C	135 °C					
Туре	Modules - diode, FRED Pt®					
Package	SOT-227					

Note

(1) All 4 anode terminals connected

FEATURES

- · Not insulated package
- · Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape



- Plug-in compatible with other SOT-227 packages
- Easy to assemble
- · Direct mounting to heatsink
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB310CB40 not insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V_{R}		400	V		
Continuous forward current per diode	I _F	T _C = 135 °C	155	^		
Single pulse forward current per diode	I _{FSM} ⁽¹⁾	T _C = 25 °C	1300	A		
Maximum power dissipation per module	P_{D}	T _C = 135 °C	421	W		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

Note

(1) 10 ms sine or 6 ms rectangular pulse



ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	400	-	-	
		I _F = 100 A	-	1.11	1.34	
		I _F = 100 A, T _J = 125 °C	-	0.99	1.1	
Forward voltage, per leg V _{FM}	W	I _F = 100 A, T _J = 175 °C	-	0.97	-	V
	VFM	I _F = 200 A	-	1.3	1.6	
		I _F = 200 A, T _J = 125 °C	-	1.22	1.4	
		I _F = 200 A, T _J = 175 °C	-	1.25	-	
Reverse leakage current, per leg	I _{RM}	V _R = V _R rated	-	1.3	50	μΑ
		V _R = V _R rated, T _J = 125 °C	-	100	-	
		V _R = V _R rated, T _J = 175 °C	-	1	4	mA
Junction capacitance, per leg	C _T	V _R = 400 V	-	100	_	pF

DYNAMIC RECOVERY CHARACTERISTICS PER DIODE (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 400 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	39	-	
Reverse recovery time, per leg	t _{rr}	T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/µs V _R = 200 V	-	89	-	ns
		T _J = 125 °C		-	184	-	
Peak recovery current, per leg I _{RRM}		T _J = 25 °C		-	9	-	A
	IRRM	T _J = 125 °C		-	20	-	
Reverse recovery charge, per leg	Q _{rr}	T _J = 25 °C		-	400	-	nC
		T _J = 125 °C		-	1840	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Junction to case, single leg conducting	D		-	-	0.19	
Junction to case, both leg conducting	R_{thJC}		-	-	0.095	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.07	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT-227	not insulate	d



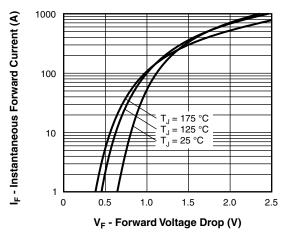


Fig. 1 - Typical Forward Voltage Drop Characteristics, Per Leg

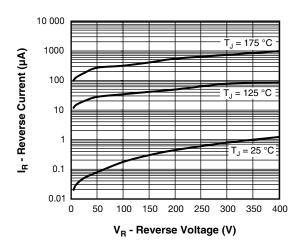


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage, Per Leg

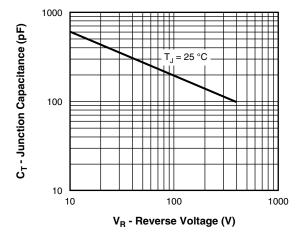


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage, Per Leg

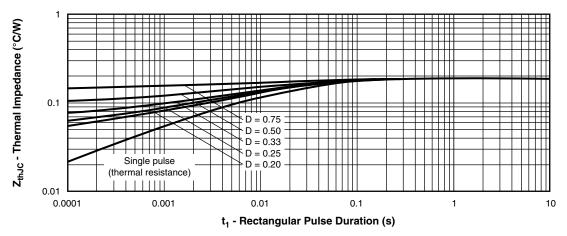


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics, Per Leg



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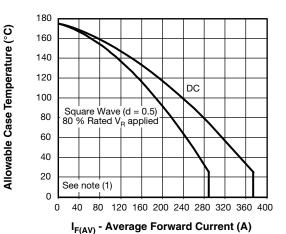


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current, Per Leg

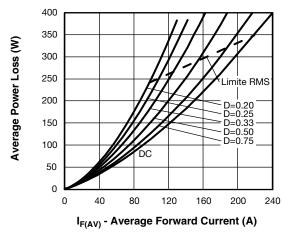


Fig. 6 - Forward Power Loss Characteristics, Per Leg

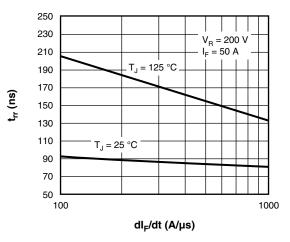


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt , Per Leg

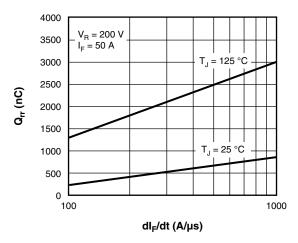


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt, Per Leq

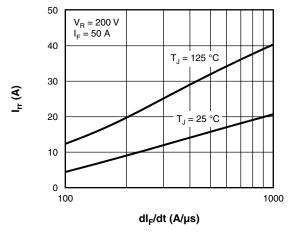


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt, Per Leg

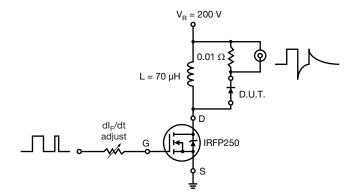
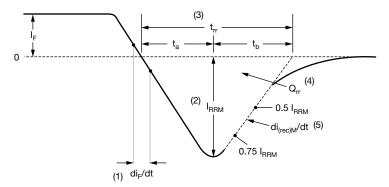


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

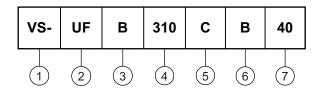
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- 2 Ultrafast rectifier
- Ultrafast Pt diffused
- 4 Current rating (310 = 310 A)
- 5 Circuit configuration (two diodes common cathode)
- 6 Package indicator (SOT-227 standard not insulated)
- 7 Voltage rating (40 = 400 V)

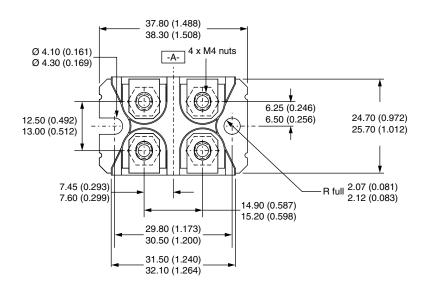
Quantity per tube is 10 pcs, M4 screw and washer included

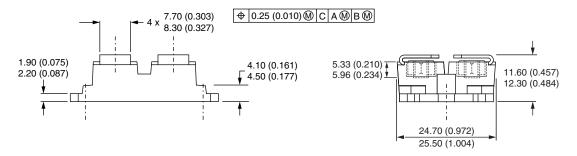
CIRCUIT CONFI	CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two diodes common cathode	С	Lead Assignment 40 Base 10 2				

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95423</u>				
Packaging Information	www.vishay.com/doc?95425			

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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