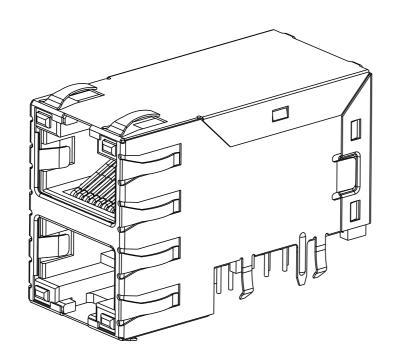
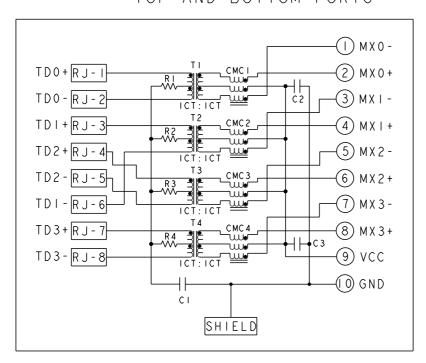
Р	LTR	DESCRIPTION	DATE	DWN	APVD
	1	INITIAL RELEASE	17APR2018	QL	TY
	Α	EC-2009010	21SEP2020	QL	TY



S8G56 GIGABIT CIRCUIT &A

TOP AND BOTTOM PORTS



CI = 1000pF, 2kV CAPACITOR

C2 - C3 = IOnF, 50V CAPACITORS

RI - R4 = 75 Ohms, I/I6W, RESISTORS

1 MATERIALS:

- HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
- SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
- POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
- CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE
- AND 4.00 um MIN MATTE TIN ON SOLDER TAILS.

- -APPLICATION: 10/100/1000 BASE-T
- -IMPEDANCE: 100 OHMS
- -TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS
- -OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
- -ALL FOUR PAIRS BI-DIRECTIONAL
- -PERFORMANCE @ 25 °C:

INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz

12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz

33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz

COMMON MODE REJECTION RATIO (CMRR):

30dB MIN FROM 0.5MHz TO 100MHz

- -ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM b.
- APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK. WITH SUNDAY=1
- 1 TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
- RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.
- 6 LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20ma

DOMINANT WAVELENGTH (\(\lambda\)D)

GREEN, 568nm TYP AT IF=20mA

YELLOW, 588nm TYP AT IF=20mA

FORWARD VOLTAGE(VF)

GREEN, 2.2V TYP AT IF=20mA

YELLOW, 2.1V TYP AT IF=20mA

⚠ INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

- 8 OPERATING TEMPERATURE: FROM 0 °C TO 70 °C
- 9 THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

			PLIANT TO A	ASME Y14.5M	CHE TOWER YU 17APR2018 TOWER YU 17APR2018 TONY YUAN		trp.	CONNECTO		ONGGUAN CHINA
DIM A	BOTTOM LED				TOP LED 2(LEF	T) TOP	LED I(RI	GHT) P	ART 1	NUMBER
3.30	GREEN/Y	ELLOW	GREE	N/YELLOW	GREEN/YELLOV	V GI	REEN/YELL	OW	2250	577-1
3.30	GREEN		Y E	ELLOW	GREEN		YELLOW		2250	577-2
2.10	GREEN/Y	ELLOW	GREE	N/YELLOW	GREEN/YELLOV	/ GI	REEN/YELL(OW 2	-2250	0577-1

2XI S8G56 GIGABIT CIRCUIT, OFFSET, WITH LED $\oplus \ominus$ MAGJACK STACK NON-PoE RODUCT SPEC C-2250577 108-104004 CUSTOMER DRAWING

