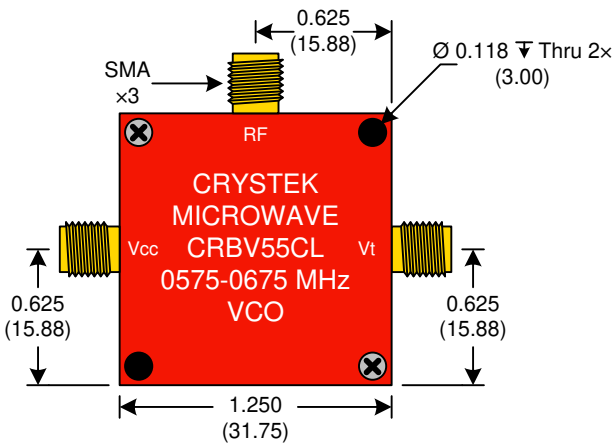


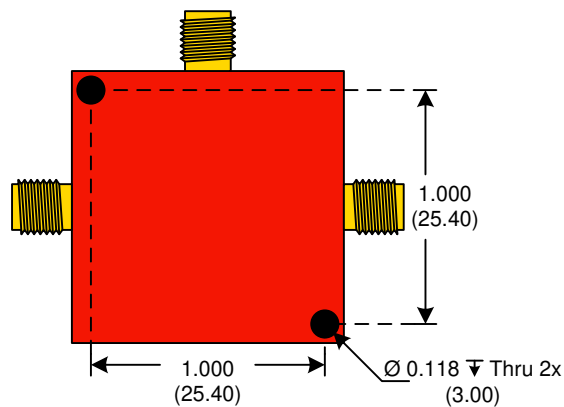


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			575	MHz
Upper Frequency:	675			MHz
Tuning Voltage:	0.3		4.7	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+3.0	+6.0	dBm
Supply Current:		15	24	mA
Harmonic Suppression (2 nd Harmonic)			-15	dBc
Pushing:			3.0	MHz/V
Pulling, all Phases:			1.0	MHz pk-pk
Tuning Sensitivity:		30		MHz/V
Phase Noise @ 10kHz offset:		-110		dBc/Hz
Phase Noise @ 100kHz offset:		-133		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			68	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

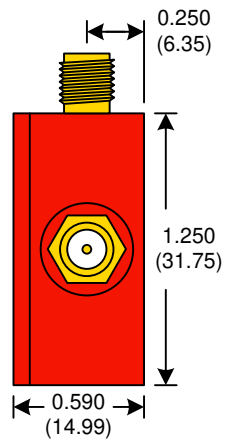
FRONT



BACK



SIDE



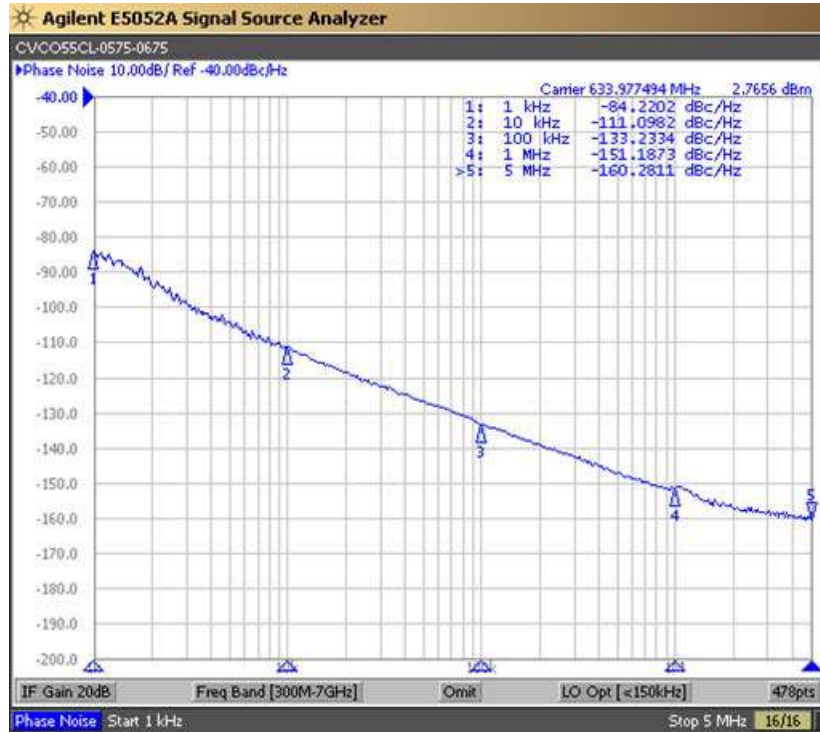
● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$

Product Control:

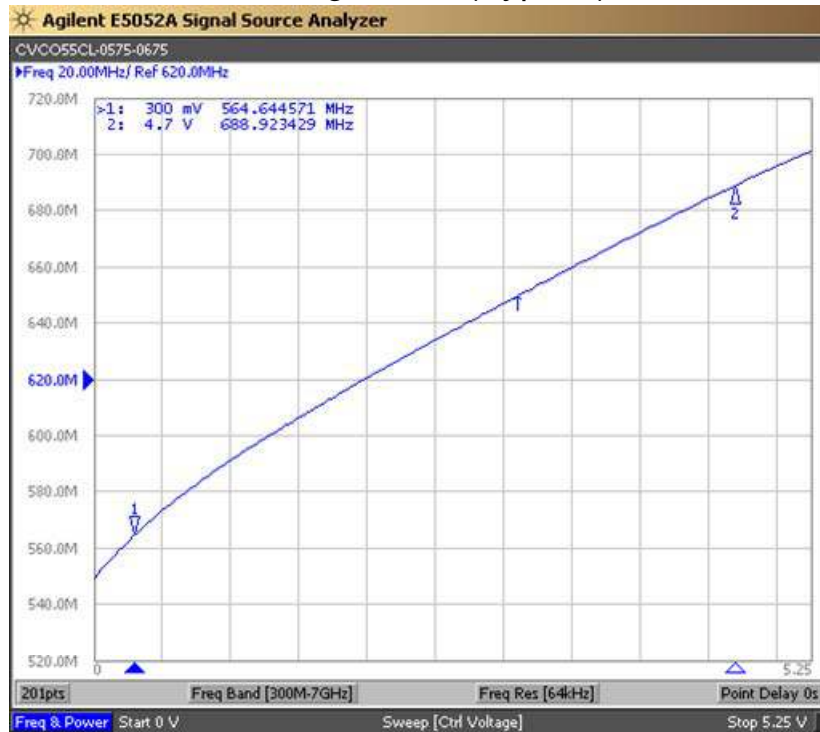
Crystek Part Number:	CRBV55CL-0575-0675	Release Date:	12-Oct-2015
Revision Level:	E	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0575-0675	Release Date:	12-Oct-2015
Revision Level:	E	Responsible:	C. Vales

Specification is subject to change without notice