



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

Device	BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
PMOS	-20V	1.9Ω @ V _{GS} = -4.5V	-350mA
		2.4Ω @ V _{GS} = -2.5V	-300mA
		3.4Ω @ V _{GS} = -1.8V	-260mA
		5Ω @ V _{GS} = -1.5V	-210mA

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage, -1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surface Mount Package 0.8mm × 0.6mm
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Description and Applications

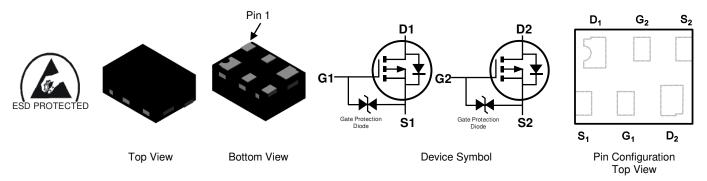
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Mechanical Data

- Case: X2-DFN0806-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.001 grams (Approximate)

X2-DFN0806-6



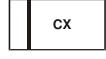
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP22D5UDA-7B	X2-DFN0806-6	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



CX = Product Type Marking Code

Top View



Maximum Ratings P-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		ID	-350 -280	mA	
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-0.4	Α
Pulsed Drain Current (Note 6)			I _{DM}	-1.1	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.36	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	346	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

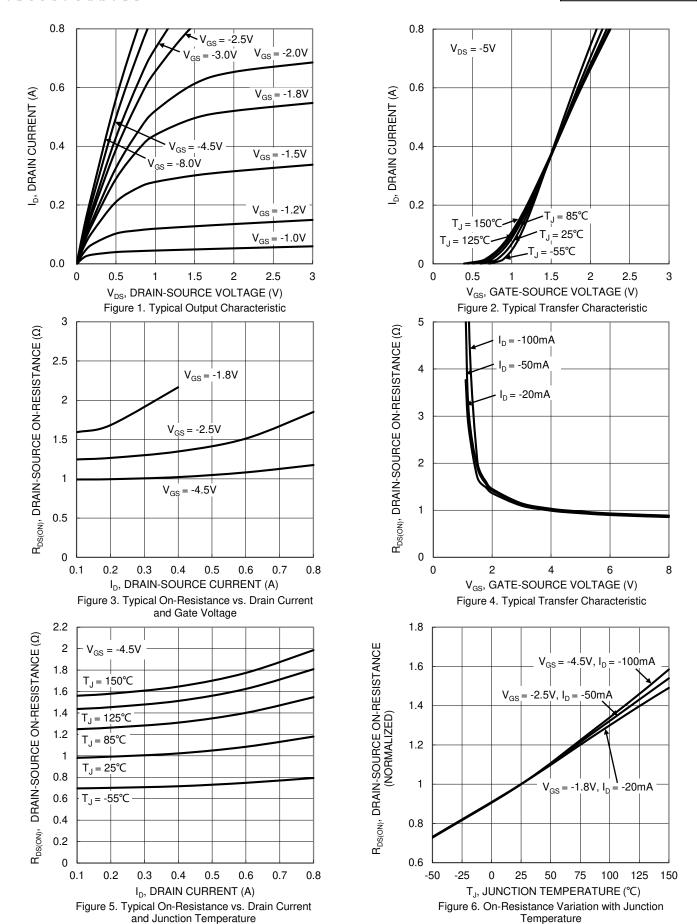
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	V _G S = 0V, I _D = -250μA	
Zero Gate Voltage Drain Current		_	_	-1	μA	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage			_	±10	μA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
			1.0	1.9	$V_{GS} = -4.5V$, $I_{D} = -100mA$		
Static Drain-Source On-Resistance	Process	_	1.2	2.4	Ω	$V_{GS} = -2.5V$, $I_{D} = -50mA$	
Static Drain-Source On-Nesistance	RDS(ON)		1.6	3.4		$V_{GS} = -1.8V, I_D = -20mA$	
		_	1.7	5		$V_{GS} = -1.5V$, $I_{D} = -10mA$	
Diode Forward Voltage			-0.5	-1.1	V	$V_{GS} = 0V$, $I_{S} = -10mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	17	_	pF	45)/ 1/ 0)/	
Output Capacitance	Coss	_	4.1	_	pF	V _{DS} = -15V, V _{GS} = 0V, -1f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	2.7	_	pF		
Total Gate Charge	Qg	_	0.3	_	nC	V 45V V 10V	
Gate-Source Charge	Qgs		0.04		nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -250 \text{mA}$	
Gate-Drain Charge	Qgd		0.1		nC	ID = -20IIIA	
Turn-On Delay Time	tD(ON)	_	7.3	_	ns		
Turn-On Rise Time	tr	_	20.7	-	ns	$V_{DD} = -15V, V_{GS} = -4.5V,$	
Turn-Off Delay Time t _{D(OFF)} Turn-Off Fall Time t _F		_	185	_	ns	$R_G = 2\Omega$, $I_D = -200mA$	
		_	97	_	ns		

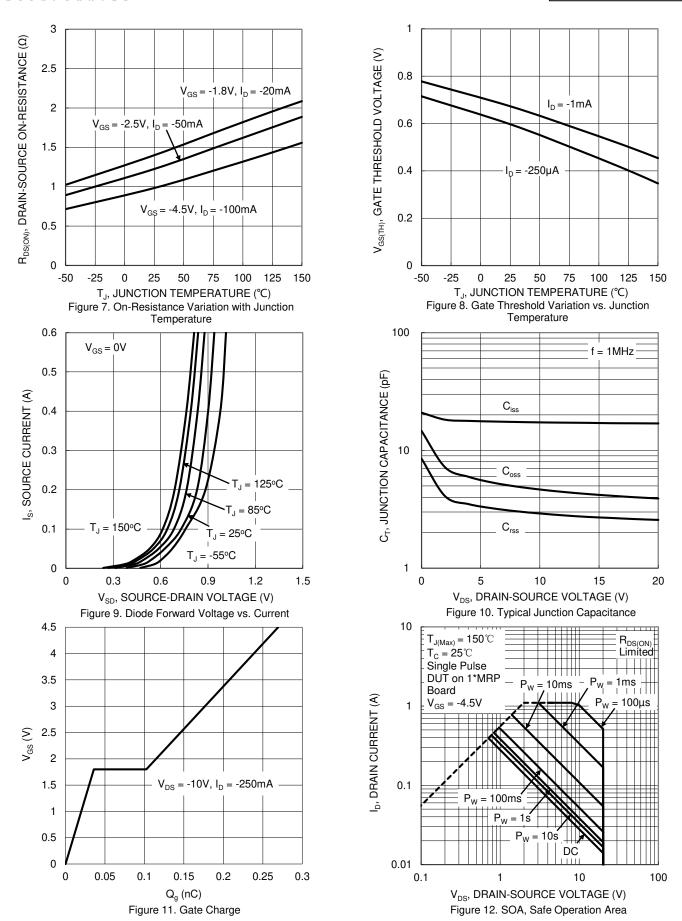
Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.











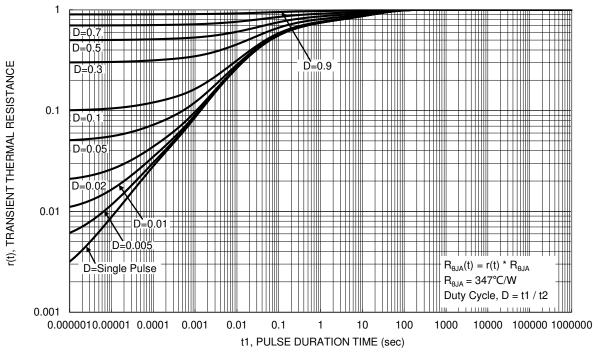


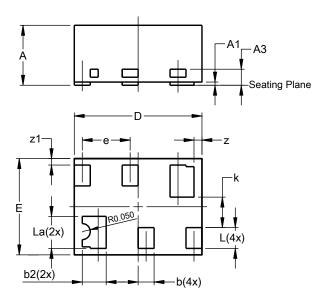
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-6

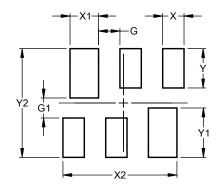


X2-DFN0806-6					
Dim	Min	Max	Тур		
Α		0.40	0.36		
A 1	0.00	0.03	0.02		
А3	-	-	0.10		
b	0.07	0.15	0.10		
b2	0.10	0.20	0.15		
D	0.75	0.85	0.80		
Е	0.55	0.65	0.60		
е			0.30		
k			0.19		
L	0.10	0.18	0.13		
La	0.17	0.25	0.20		
Z			0.05		
z1			0.04		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-6



Dimensions	Value (in mm)			
G	0.150			
G1	0.140			
Χ	0.150			
X1	0.200			
X2	0.800			
Υ	0.275			
Y1	0.345			
Y2	0.760			

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