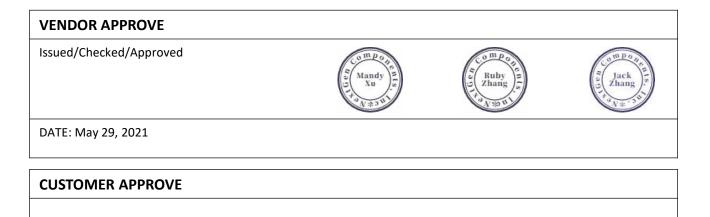


SPECIFICATION SHEET

SPECIFICATION SHEET NO.	N0529-DC455K0000S140
DATE	May 29, 2021
REVISION	A0
DESCRIPITION	KHz SMD Discriminators 6260 Type L6.2*W6.0*H3.1mm 2 Pads CDBC Series 455.0KHz, Demodulated Bandwidth(3dB): ±4.0KHz Min from 455KHz Operating Temp. Range -20°C ~+80°C Reflow Profile Condition 260 °C Max. In Tape/Reel, 2000pcs/Reel RoHS III Complaint
CUSTOMER	
CUSTOMER PART NUMBER	
CROSS REF. PART NUMBER	
ORIGINAL PART NUMBER	TGS CDBC 455C40 TLF
PART CODE	DC455K0000S140



DATE:

KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

MAIN FEATURE

- KHz SMD Ceramic Discriminator 6260 Type 2 pads
- White case, L6.2*W6.0*H3.1mm

NextGen

Components,Inc.

- Low cost and short shipment
- Reflow Profile Condition 260 °C Max.
- Cross main competitors parts CDBC and JTC series
- RoHS/RoHS III compliant
- For quadrature detection with IC: TOSHIBA/TA31145

APPLICATION

Communication Electronics

PART CODE GUIDE

DC	455K0000	S	140
1	2	3	4

1) DC: Part family Code for KHz SMD Ceramic Discriminator 6260 Type L6.2*W6.0*H3.1mm 2 Pads

2) 455K0000: Frequency range code for 455.0000KHz

3) S: SMD type, Package Tape/Reel, 2000pcs/Reel

4) 140: Specification code for original part No.: TGS CDBC 455C40 TLF





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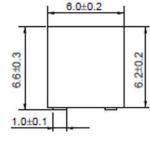


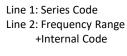
KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

DIMENSION (Unit: mm, Tol. +/-0.15mm)

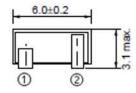
Image for reference







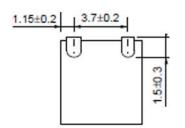
CDBC

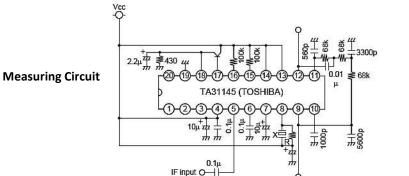


Connection

Marking

Pin 1: Input Pin 2: Output

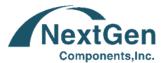




Note X: DC455K0000S140 R: 3.3kohm

Unit C : F R : Ω

AF output



KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

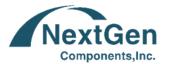
ELECTRICAL PARAMETERS

Parameter		Part No.		Value		Condition	
	Symbol	Symbol		Min.	Typical	Max.	
Original	Manufacturer	TGS	TGS Crystals				
Holder 1	Гуре	CDBC	KHz SMD Discriminator				
			6260	Type L6.2*W6	.0*H3.1mm 2 F	Pads	
Frequen	ncy Range (f0)	455	KHz		455.000		@ ± 1.0KHz
Bandwi	dth	C40	KHz	±4.0			@3dB Bandwidth, f0
Operation Temperation			°C	-20		+80	
Storage	Temperance		°C	-40		+85	
Demodulated Output			mV		40±20		@f0
Demodu Distortio	ulated on Factor		%	% 3.5		@ 455KHz	
Temper	ature Stability		%	0.3		@-20 ~+80 °C	
IC Mode	el No.			TOSHIBA/TA31145			
Withsta	nd Voltage			DC 5.0V 1 minute			
	Package	т	Tape/Reel, 2000pcs/Reel				
	RoHS Status	LF	RoHS III compliant				
Other	Add Value		N/A				
	Special Code		For Internal Control, Blank: N/A				

Note: Original Part Number: TGS CDBC 455C40 TLF

INPUT SIGNAL CONDITION

Item	Unit	Value
Input	dBm	-80.0
Deviation	KHz	±4.0
Frequency Modulation	KHz	1.0



KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

TEST METHOD

Test Items	Test Method And Conditions	Requirement
Demodulated 3dB Bandwidth	I Input the above signal and sweep the carrier around 455 km2, and lind	
Demodulate Output	Demodulated output shall be measured when carrier frequency is adjusted to 455KHz.	It shall meet the specification
Demodulated Distortion Factor	Carrier frequency is adjusted to 455KHz.And distortion shall be measured with 1 kHz modulation frequency.	It shall meet the specification
Input signal condition	Input signal condition, Input level - 80dBμ; Frequency Deviation – 4.0KHz; Modulation Frequency: 1.0KHz	
Measurement Condition	Measurement Unless otherwise noted, the standard range of atmospheric conditions	



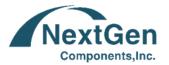
KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

RELIABILITY

Test Items	Test Method And Conditions	Requirement
Humidity	After being placed in a chamber with 90-95% R.H. at 40±2°C for 100 hours and then being placed in room temperature for 1 hour, filter shall be measured.	It shall meet Table 1.
High Temperature	After being placed in a chamber with 80±2 °C,for 100 hours and then being placed in room temperature for 1 hour, filter shall be measured.	It shall meet Table 1.
Low Temperature	After being placed in a chamber with -20±2 °C, for 100 hours and then being placed in room temperature for 1 hour, filter shall be measured.	lt shall meet Table 1.
Heat Shock	After being kept at room temperature, filter shall be placed at temperature of -55 °C, for 30 minutes, then be placed at temperature. 85 °C, for 30 minutes. After that returned to -55 °C again. Repeated above cycle for 5 times. After being kept in room temp. for 1 hour, filter shall be measured	It shall meet Table 1.
Resistance to Solder Heat	Lead terminals are immersed up to 1.5mm from filter's body in soldering bath of 350± 10°C, for 3±0.5 sec. And then filter shall be measured after being placed in room temperature for 1 hour.	It shall meet Table 1.
Solderability	Lead terminals are immersed in aide solder for 5 sec and then immersed in soldering bath of 230±5°C, for 3±0.5 sec. At least 95% lead terminals shall be covered with solder	It shall meet Table 1.
Drop Test	Filter shall be measured after 3 times random drops from the height of 30 cm on concrete floor	No visible damage and it meet Table 1
Adhesion	A static load of 20N to the direction of the arrow (see Fig. 4) shall be applied on the core of the Component and hold for 10 seconds. Filter shall be soldered correctly and tightly to PCB.	It shall meet Table 1.
Vibration	Filter shall be measured after being applied vibration of amplitude of 1.5mm with 10-55Hz band of vibration frequency to each of 3 perpendicular directions for 2 hours	No visible damage and it meet Table 1
Substrate Bending Test	Apply pressure in the direction of arrow (see Fig. 3) at a rate of about 0.5mm per second until it reaches a bend of 3mm and hold for 30 seconds.	It shall meet Table 1.

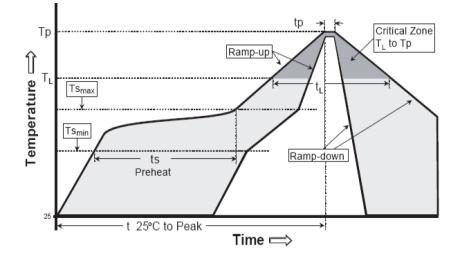
Table1

Item	Center	Demodulated	Demodulated	Demodulated
	Frequency	Bandwidth(3dB)	Output	Distortion
Specification	455KHz	±4.0KHz Min.	40±20mV. (at 455 kHz)	3.5%Max.

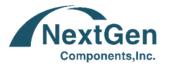


KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

SUGGESTED REFLOW PROFILE (For Reference Only)



Profile Feature		Pb-Free Assembly
Average Ramp-up Rate (Ts Max to Tp)		3°C/second Max
Preheat	Temperature Min (Ts Min.)	125°C
	Temperature Max (Ts Max.)	200°C
	Time (ts Min. to ts Max.)	60 ~ 180 seconds
Time maintained above	Temperature (TL)	217°C
	Time (tL)	60 ~ 150 seconds
Peak/Classification Temperature (Tp)		260 °C
Time within 5°C of actual Peak Temperature (tp)		20 ~ 40 seconds
Ramp-down rate		6 °C /Second Max.
Time 25 °C to Peak Temperature		8 minutes Max.
Suggest reflow times		3 Times Max.



Code

W

F

Е

Ρ0

Ρ1

P 2

D 0

D 1

t 2

А

Dimension

16.0+/-0.30

6.80+/-0.20

1.75+/-0.10

4.00+/-0.10

7.80+/-0.10

2.00+/-0.05

Ø1.5+/-0.10

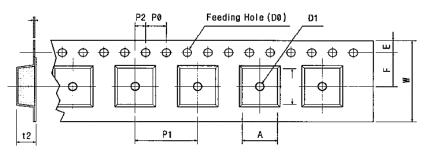
Ø1.5+/-0.10

3.60+/-0.10

7.70+/-0.10

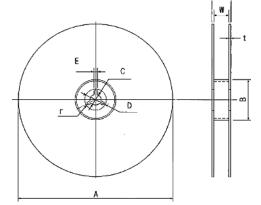
KHZ SMD CERAMIC DISCRIMINATOR CDBC SERIES

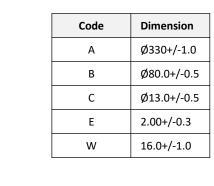
REEL DIMENSION (Unit: mm, 2000pcs/Reel)

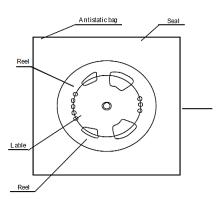


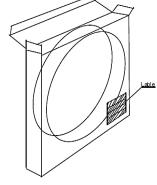
Tape Running Direction

TAPE DIMENSION (Unit: mm)









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