

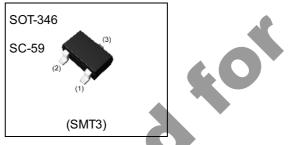
-500mA/-40V Digital transistor (with built-in resistor)

| Parameter | Value | |
|------------------|--------|--|
| V _{CEO} | -40V | |
| I _C | -500mA | |
| R ₁ | 10kΩ | |

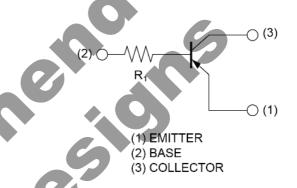
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3)Only the on/off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

Outline



•Inner circuit



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTB114TK | SOT-346 (SMT3) | 2928 | T146 | 180 | 8 | 3000 | F94 |

• Absolute maximum ratings ($T_a = 25$ °C)

| Parameter | Symbol | Values | Unit |
|------------------------------|-------------------|-------------|------|
| Collector-base voltage | V_{CBO} | -50 | V |
| Collector-emitter voltage | V _{CEO} | -40 | V |
| Emitter-base voltage | V _{EBO} | -5 | V |
| Collector current | I _C *1 | -500 | mA |
| Power dissipation | P _D *2 | 200 | mW |
| Junction temperature | T _j | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

● Electrical characteristics (T_a = 25°C)

| Dayanatay | Cymah al | Condition | Values | | | 1.1:4 |
|--------------------------------------|----------------------|--|--------|-----|------|-------|
| Parameter | Symbol | Conditions | Min. | | Max. | Unit |
| Collector-base breakdown voltage | BV _{CBO} | I _C = -50µA | -50 | - | 1 | V |
| Collector-emitter breakdown voltage | BV _{CEO} | I _C = -1mA | -40 | - | - | V |
| Emitter-base breakdown voltage | BV _{EBO} | I _E = -50μA | -5 | - | - | V |
| Collector cut-off current | I _{CBO} | V _{CB} = -50V | - | - | -500 | nA |
| Emitter cut-off current | I _{EBO} | V _{EB} = -4V | - | - | -500 | nA |
| Collector-emitter saturation voltage | V _{CE(sat)} | $I_C = -50 \text{mA}, I_B = -2.5 \text{mA}$ | - | - | -300 | mV |
| DC current gain | h _{FE} *3 | $V_{CE} = -5V, I_{C} = -50mA$ | 100 | 250 | 600 | - |
| Input resistance | R ₁ | - | - | 10 | 1 | kΩ |
| Transition frequency | f _T *1 | V _{CE} = -10V, I _E = 50mA, f = 100MHz | - | 200 | - | MHz |

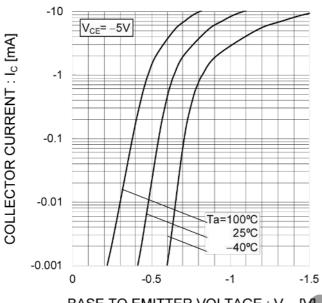
^{*1} Characteristics of built-in transistor.

^{*2} Each terminal mounted on a reference land.

^{*3} Pulsed

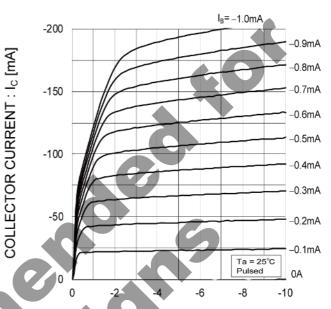
● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded Emitter Propagation Characteristics



BASE TO EMITTER VOLTAGE: VBE [V]

Fig.2 Grounded Emitter Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: VCE [V]

Fig.3 DC Current Gain vs. Collector Current

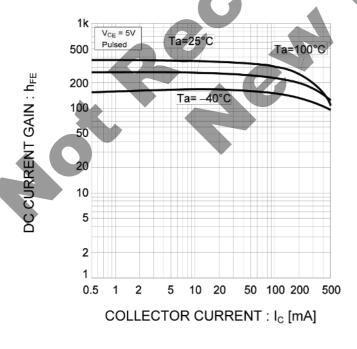
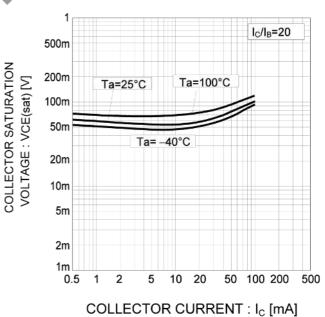
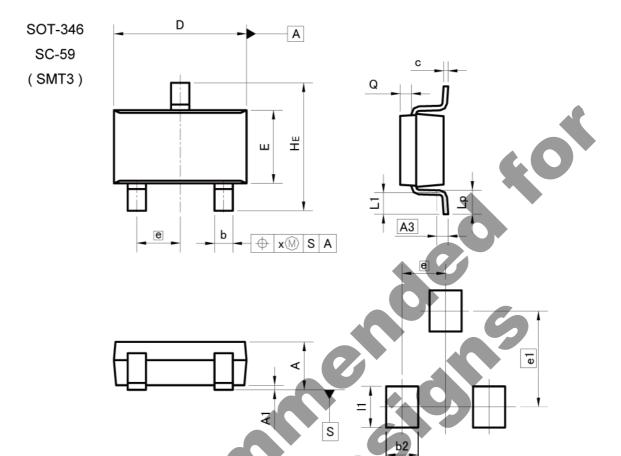


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current



Dimensions



| Pattern of terminal position | areas |
|------------------------------|-------|
| Not a pattern of soldering | pads] |

| DIM | MILIM | ETERS | INC | HES | |
|-------|-------|-------|------------------|-------|--|
| DIW | MIN | MAX | MIN | MAX | |
| A | 1.00 | 1.30 | 0.039 | 0.051 | |
| (A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0 | 25 | 0.0 | 10 | |
| b | 0.35 | 0.50 | 0.014 | 0.020 | |
| С | 0.09 | 0.25 | 0.004 | 0.010 | |
| D | 2.80 | 3.00 | 0.110 | 0.118 | |
| E | 1.50 | 1.80 | 0.059 | 0.071 | |
| e 0.9 | | 95 | 0.037 | | |
| HE | 2.60 | 3.00 | 0.102 | 0.118 | |
| L1 | 0.30 | 0.60 | 0.012 | 0.024 | |
| Lp | 0.40 | 0.70 | 0.016 | 0.028 | |
| Q | 0.20 | 0.30 | 0.008 | 0.012 | |
| х | - | 0.10 | 19 17 | 0.004 | |
| У | - | 0.10 | 2 - | 0.004 | |

| DIM | MILIM | ETERS | INC | HES |
|-----|-------|-------|---------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| b2 | | 0.60 | 8 | 0.024 |
| e1 | 2.10 | | 0.0 | 083 |
| 11 | -0 | 0.90 | 0 | 0.035 |

Dimension in mm/inches



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| JAPAN | USA | EU | CHINA |
|---------|----------|----------|----------|
| CLASSⅢ | CLASSⅢ | CLASSIIb | CLASSII |
| CLASSIV | CLASSIII | CLASSⅢ | CLASSIII |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- Confirm that operation temperature is within the specified range described in the product specification.
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- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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