

MCCOG21605B6W-BNMLWI	2 x16	I2C	LCD Module	
		Specification		
Version: 4 Date: 05/09/2018				
		Revision		
1 0	1/08/2011	First Issue.		
		Modify Note 3.		
3 2	1/11/2014	Remove IC Information.		
4 25	5/02/2016	Modify Precautions in use of LCD Module	s & Static Electricity Test.	

Display F			
Resolution	2 x 16		
Appearance	White on Blue		
Logic Voltage	5V/ 3V		1
Interface	I2C		CHS
Font Set	English / Japanese	CC	ompliant
Display Mode	Transmiss <mark>iv</mark> e		mphant
LC Type	BSTN		
Module Size	51.20 x 20.70 x 6.30 mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COG	Box Quantity	Weight / Display
LED Backlight	White		

\* - For full design functionality, please use this specification in conjunction with the ST7032i specification. (Provided Separately)

Display Accessories					
Part Number	Description				
MCCOG-I2C-I-8	COG LCD I2C interface board, compatible with Arduino or UC32 SBC.				

Optional Variants					
Appearances	Voltage				
White on Black					
Black on Yellow/Green					

### **General Specification**

The Features of the Module is description as follow:

■ Module dimension: 51.2 x 20.7 x 6.3 (max.) mm

■ View area: 40.0 x 10.0 mm

■ Active area: 38.0 x 8.0 mm

■ Number of Characters: 16 characters x 2 Lines

■ Dot size: 0.36 x 0.43 mm

Dot pitch: 0.41 x 0.48 mm

■ Character size: 2.00 x 3.79 mm

■ Character pitch: 2.40 x 4.19 mm

■ LCD type: STN Negative, Blue Transmissive

■ Duty: 1/16 , 1/5 Bias

■ View direction: 6 o'clock

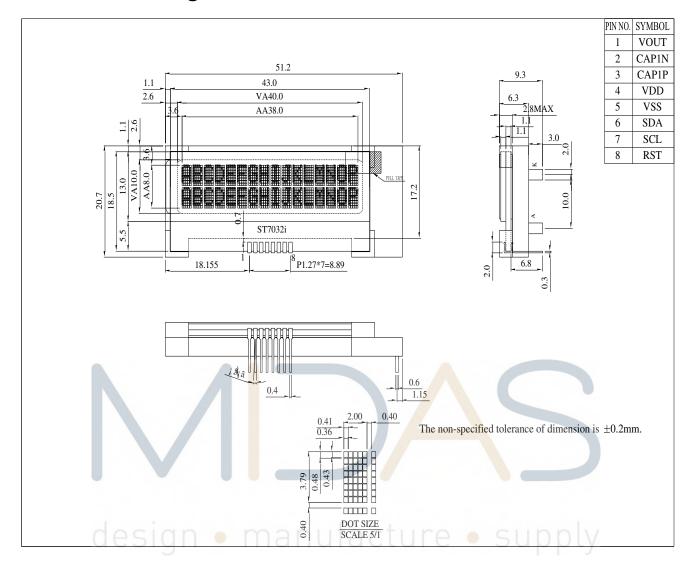
■ Backlight Type: LED, White

IC: ST7032ign • manufacture • supply

## **Interface Pin Function**

Pin No.	Symbol	Level	Description
1	VOUT		DC/DC voltage converter. Connect a capacitor between this terminal and VIN when the built-in booster is used.
2	CAP1N		For voltage booster circuit(VDD-VSS)
3	CAP1P		External capacitor about 0.1u~4.7uf
4	VDD	3.0/5.0V	Power supply
5	VSS		GND
6	SDA	1	(In I2C interface DB7 (SDA) is input data.  SDA and SCL must connect to I2C bus (I2C bus is to connect a resister between SDA/SCL and the power of I2C bus).
7	sc. desig	n • r	(In I2C interface DB6 (SCL) is clock input. SDA and SCL must connect to I2C bus (I2C bus is to connect a resister between SDA/SCL and the power of I2C bus ).
8	RST		RESET

## **Contour Drawing**



# Application schematic

VDD=3.0V

1	VOUT -	
2	CAP1N	
3	CAP1P	TIUF TIUF
4	VDD	VDD TIER TIER
5	VSS	VSS
6	SDA	<u> </u>
7	SCL	\$10K VDD
8	RST	

VDD=5.0V

V I	JD 3.0	
1	VOUT	
2	CAP1N	NC NC
3	CAP1P	
4	VDD	VDD anufacture supply
5	VSS	VSS
6	SDA	<b>\$</b>
7	SCL	VDD \$10K
8	RST	

INITIALIZE: (3V)

MOV I2C\_CONTROL,#00H ;WRITE COMMAND

MOV I2C DATA,#38H ;Function Set

LCALL WRITE CODE

MOV I2C CONTROL,#00H ;WRITE COMMAND

MOV I2C\_DATA,#39H ;Function Set

LCALL WRITE CODE

MOV I2C DATA,#14H ;Internal OSC frequency

LCALL WRITE CODE

MOV I2C DATA,#74H ;Contrast set

LCALL WRITE\_CODE

MOV I2C DATA,#54H ;Power/ICON control/Contrast set

LCALL WRITE CODE

MOV I2C DATA,#6FH ;Follower control

LCALL WRITE CODE

MOV I2C DATA,#0CH ;Display ON/OFF

LCALL WRITE\_CODE

MOV I2C DATA,#01H ;Clear Display

LCALL WRITE CODE

design • manufacture • supply

#### **INITIALIZE: (5V)**

MOV I2C CONTROL,#00H;WRITE COMMAND

MOV I2C\_DATA,#38H ;Function Set

LCALL WRITE CODE

MOV I2C\_CONTROL,#00H;WRITE COMMAND

MOV I2C DATA,#39H ;Function Set

LCALL WRITE CODE

MOV I2C\_DATA,#14H ;Internal OSC frequency

LCALL WRITE CODE

MOV I2C\_DATA,#79H ;Contrast set

LCALL WRITE\_CODE

MOV I2C DATA,#50H ;Power/ICON control/Contrast set

LCALL WRITE\_CODE

MOV I2C\_DATA,#6CH ;Follower control

LCALL WRITE\_CODE

MOV I2C DATA,#0CH ;Display ON/OFF

LCALL WRITE CODE

MOV I2C DATA,#01H ;Clear Display

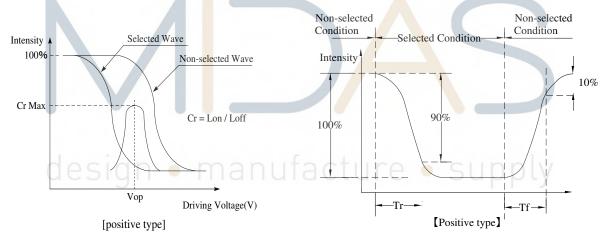
LCALL WRITE\_CODE

### **Optical Characteristics**

Item	Symbol	Condition	Min	Тур	Max	Unit
	θ	CR≧2	0	_	20	ψ= 180°
View Apple	θ	CR≧2	0	_	40	ψ= 0°
View Angle	θ	CR≧2	0	_	30	ψ= 90°
	θ	CR≧2	0	_	30	ψ= 270°
Contrast Ratio	CR	_	_	3	_	_
Response Time	T rise	_	_	150	200	ms
	T fall	_	_	150	200	ms

**Definition of Operation Voltage (Vop)** 

Definition of Response Time ( Tr , Tf )

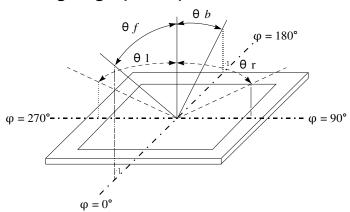


#### **Conditions:**

Operating Voltage : Vop Viewing Angle( $\theta$ ,  $\phi$ ) :  $0^{\circ}$ ,  $0^{\circ}$ 

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

#### **Definition of viewing angle(CR≧2)**



## **Absolute Maximum Ratings**

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	T <sub>OP</sub>	-20	_	+70	°C
Storage Temperature	T <sub>ST</sub>	-30	_	+80	°C
Input Voltage	VIN	-0.3	_	V <sub>DD</sub> +0.3	V
Power Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	_	+6.0	V
LCD Driver Voltage	V <sub>LCD</sub>	2.7	_	7.0	V

## **Electrical Characteristics**

Item	S <mark>y</mark> mbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V <sub>DD</sub> -V <sub>SS</sub>		3	3.3	5 (bon=1 max=3.5V)	V
desian	manı	Ta=-20°C	re	SU	ıpōlv	V
Supply Voltage For LCD	V <sub>LCD</sub>	Ta=25°C	_	4.5		V
		Ta=70°C	_	_	_	٧
Input High Volt.	VIH	_	0.7 V <sub>DD</sub>	_	V <sub>DD</sub>	V
Input Low Volt.	VıL	_		_	0.2 V <sub>DD</sub>	٧
Output High Volt.	V <sub>OH</sub>	_	0.8 V <sub>DD</sub>	_	$V_{DD}$	V
Output Low Volt.	Vol	_	_	_	0.2V <sub>DD</sub>	V
Supply Current(No include LED Backlight)	loo	_	_	0.17	_	mA

Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance.

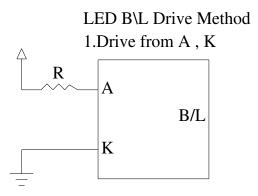
## **Backlight Information**

#### **Specification**

PARAMETER	SYMBOL	MIN	ТҮР	мах	UNIT	TEST CONDITION
Supply Current	ILED	_	32	40	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	v	_
Reverse Voltage	VR	_	_	5	V	_
Luminance (Without LCD)	IV	616	880	_	CD/M <sup>2</sup>	ILED=32mA
LED Life Time (For Reference only)		1	50K		Hr.	ILED=32mA 25°C,50-60%RH, (Note 1)
Color	White					

Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).

Note 1:50K hours is only an estimate for reference.



## Reliability

#### Content of Reliability Test (Wide temperature, -20°c~70°C)

Environmental Test						
Test Item	Content of Test	Test Condition	Note			
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2			
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2			
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs				
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1			
High Temperature/ Humidity storage	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2			
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C  30min 5min 30min 1 cycle	-20°C/70°C 10 cycles				
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude:  1.5mm  Vibration Frequency:  10~55Hz  One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3			
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330 Ω CS=150pF 10 times				

Note1: No dew condensation to be observed.

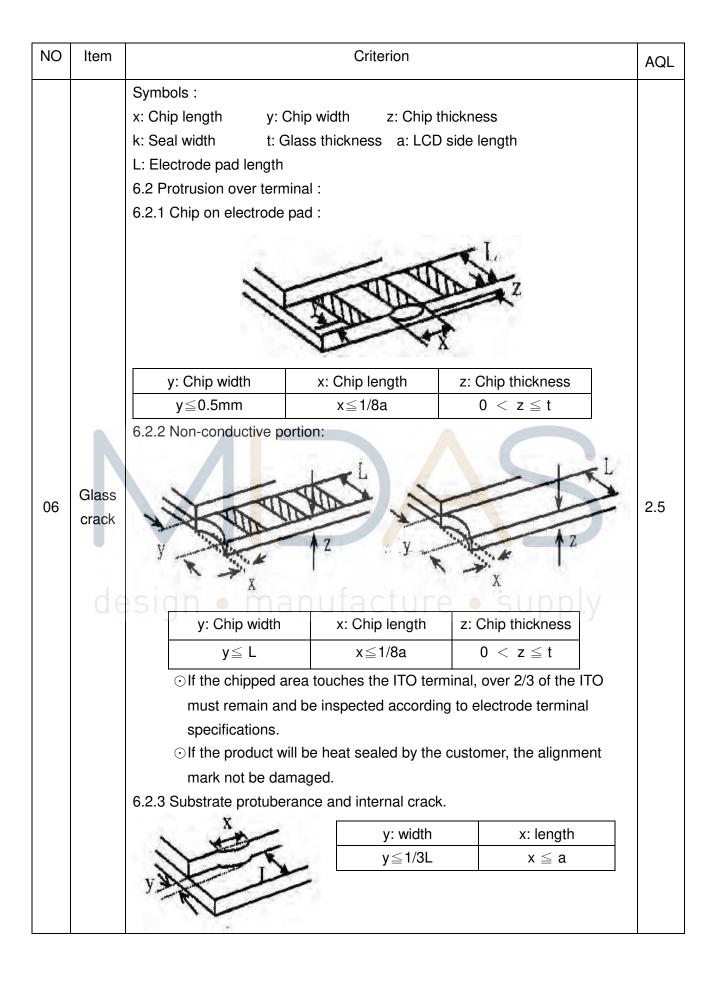
Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

# **Inspection specification**

NO	Item	Criterion				
01	Electrical Testing	<ol> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 LCD viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Contrast defect.</li> </ol>				0.65
02	Black or white spots on LCD (display only)	<ul> <li>2.1 White and black spots on display ≤0.25mm, no more than three white or black spots present.</li> <li>2.2 Densely spaced: No more than two spots or lines within 3mm</li> </ul>			2.5	
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As fo $\Phi = (x + y)/2$ $X \qquad Y$	0	fing drawing $Φ ≤ 0.10$ $0.10 < Φ ≤ 0.20$ $0.20 < Φ ≤ 0.25$ $0.25 < Φ$	Acceptable Q TY Accept no dense 2 1 0	2.5
		3.2 Line type : (As follows)  Leng  Leng  L≤3  L≤2	3.0 2.5	yidth W≤0.02 0.02 <w≤0.03 0.03<w≤0.05="" 0.05<w<="" td=""><td>Acceptable Q TY Accept no dense  2 As round type</td><td>2.5</td></w≤0.03>	Acceptable Q TY Accept no dense  2 As round type	2.5
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not east to find, must check in specify direction.		Size Φ $Φ \le 0.20$ $0.20 < Φ \le 0.50$ $0.50 < Φ \le 1.00$ $1.00 < Φ$ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5

NO	Item	Criterion			
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination			
		Symbols Define: x: Chip length y: 0	Chip width z: Chip Glass thickness a: LCE	thickness O side length	
06	Chipped glass	z: Chip thickness Z≤1/2t  1/2t <z≤2t 2="" 6.1.2="" are="" corner="" crack:<="" if="" more="" or="" td="" there="" ⊙=""><td>y: Chip width  Not over viewing area  Not exceed 1/3k  chips, x is total length of</td><td>x: Chip length <math display="block">x \le 1/8a</math> <math display="block">x \le 1/8a</math> of each chip.</td><td>2.5</td></z≤2t>	y: Chip width  Not over viewing area  Not exceed 1/3k  chips, x is total length of	x: Chip length $x \le 1/8a$ $x \le 1/8a$ of each chip.	2.5
		z: Chip thickness $Z \leq 1/2t$ $1/2t < z \leq 2t$ ① If there are 2 or more	y: Chip width  Not over viewing area  Not exceed 1/3k chips, x is the total leng	x: Chip length $x \le 1/8a$ $x \le 1/8a$ gth of each chip.	



NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	<ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul>	0.65 2.5 0.65
09	Bezel	<ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>	2.5 0.65
10	PCB · COB desig	<ul> <li>9.2 Bezel must comply with job specifications.</li> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> <li>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>10.5 No oxidation or contamination PCB terminals.</li> <li>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li> <li>10.9 The Scraping testing standard for Copper Coating of PCB</li> </ul>	
11	Soldering	<ul> <li>11.1 No un-melted solder paste may be present on the PCB.</li> <li>11.2 No cold solder joints, missing solder connections, oxidation or icicle.</li> <li>11.3 No residue or solder balls on PCB.</li> <li>11.4 No short circuits in components on PCB.</li> </ul>	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
		12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface	2.5
	General appearance	pin must be present or look as if it cause the interface pin to	
		sever.	2.5
12		12.6 The residual rosin or tin oil of soldering (component or chip	
		component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		<ul><li>12.8 Pin type must match type in specification sheet.</li><li>12.9 LCD pin loose or missing pins.</li></ul>	
		12.10 Product packaging must the same as specified on	
		packaging specification sheet.	0.65
		12.11 Pr <mark>od</mark> uct dimension and str <mark>ucture must c</mark> onform to product	
		specification sheet.	
		12.12 Visual defect outside of VA is not considered to be rejection.	

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#### Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Midas have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) Midas have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Midas have the right to modify the version.)
- (10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

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#### **Material List of Components for RoHs**

1. Midas hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

- 2.Process for RoHS requirement: (only for RoHS inspection)
  - (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
  - (2) Heat-resistance temp. :

Reflow: 250°C,30 seconds Max.;

Connector soldering wave or hand soldering: 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C;

Recommended customer's soldering temp. of connector: 280°C, 3 seconds.

#### Recommendable Storage

- 1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
- 2. Do not place the module near organics solvents or corrosive gases.
- 3. Do not crush, shake, or jolt the module.