Dual Bus Driver/Receiver with 4-to-1 Output Multiplexers

Description

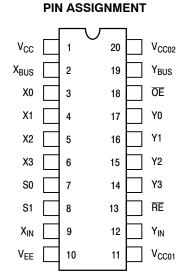
The MC10H332 is a Dual Bus Driver/Receiver with four-to-one output multiplexers. These multiplexers have common selects and output enable. When disabled, (\overline{OE} = high) the bus outputs go to -2.0 V. The parameters specified are with 25 Ω loading on the bus drivers and 50 Ω loads on the receivers.

Features

- Propagation Delay, 1.5 ns Typical Data-to-Output
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)

DIP & PLLC

- Voltage Compensated
- MECL 10KTM Compatible
- Pb-Free Packages are Available*



Pin assignment is for Dual-in-Line Package..

NOTE:

Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 frpm is maintained. Receiver outputs are terminated through a 50 Ω resistor to –2.0 Vdc. Bus outputs are terminated through a 25 Ω resistor to –2.0 Vdc.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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MARKING DIAGRAMS*

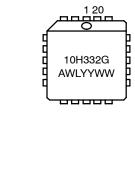


MC10H352P AWLYYWWG

PDIP-20 P SUFFIX CASE 738

PLLC-20 FN SUFFIX

CASE 775



Α	= Assembly Location
WL	= Wafer Lot
YY	= Year
WW	= Work Week
G	= Pb-Free Package
	-

*For additional marking information, refer to Application Note AND8002/D.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Table 1. MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
V_{EE}	Power Supply (V _{CC} = 0)	-8.0 to 0	Vdc
VI	Input Voltage (V _{CC} = 0)	0 to V _{EE}	Vdc
l _{out}	Output Current – Continuous – Surge	50 100	mA
T _A	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range – Plastic – Ceramic	–55 to +150 –55 to +165	°C ℃

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

		0 °		25°		75 °		
Symbol	Characteristic	Min	Мах	Min	Max	Min	Max	Unit
١ _E	Power Supply Current	-	115	-	110	-	115	mA
l _{inH}	Input Current High Pins 3,4,5,6,14, 15,16,17 Pins 7,8 Pins 13, 18	- - -	667 437 456		417 273 285	- - -	417 273 285	μΑ
l _{inL}	Input Current Low	0.5	-	0.5	-	0.3	-	μΑ
V _{OH}	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V _{IH}	High Input Voltage	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
V _{IL}	Low Input Voltage	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

1. Each MECL 10H[™] series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

Table 3. AC PARAMETERS

		0 °		25 °		75 °		
Symbol	Characteristic	Min	Мах	Min	Max	Min	Max	Unit
t _{pd}	Propagation Delay							ns
	Data-to-Bus Output	0.8	3.0	0.8	3.0	0.8	3.2	
	Select-to-Bus							
	Output	0.8	3.4	0.8	3.4	0.8	3.8	
	OE-to-Bus Output	0.8	2.4	0.8	2.4	0.8	2.6	
	Bus-to-Receiver	0.8	2.1	0.8	2.1	0.8	2.4	
	Select-to-Receiver	1.8	4.5	1.8	4.5	1.8	5.0	
	RE-to-Receiver	0.8	2.2	0.8	2.2	0.8	2.5	
	Data-to-Receiver	1.3	4.0	1.3	4.0	1.3	4.5	
t _r	Rise Time	0.5	2.0	0.5	2.0	0.5	2.1	ns
t _f	Fall Time	0.5	2.0	0.5	2.0	0.5	2.1	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

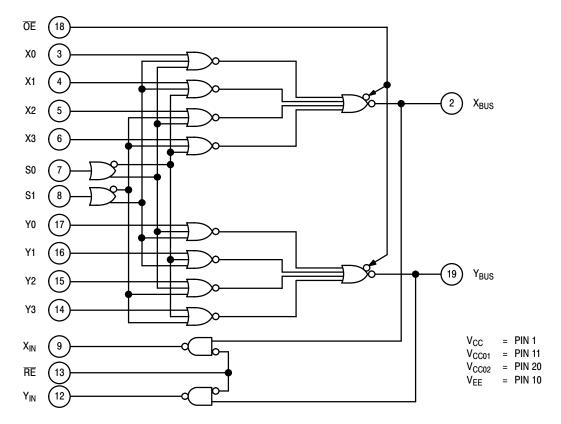
Table 4. MULTIPLEXER TRUTH TABLE

OE	S1	S0	X _{Bus}	Y _{Bus}
HLLL	X L L H H	X L H L H	-2.0V X0 X1 X2 X3	-2.0V Y0 Y1 Y2 Y3

Table 5. RECEIVER TRUTH TABLE

RE	X _{in}	Y _{in}
Н	L	L
L	X _{Bus}	Y _{Bus}

LOGIC DIAGRAM



ORDERING INFORMATION

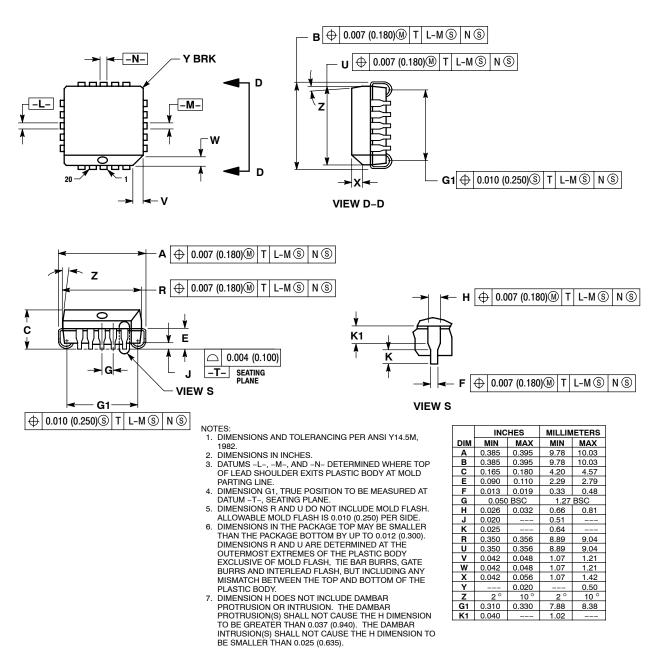
Device	Package	Shipping [†]
MC10H332FN	PLLC-20	46 Units / Rail
MC10H332FNG	PLLC-20 (Pb-Free)	46 Units / Rail
MC10H332FNR2	PLLC-20	500 / Tape & Reel
MC10H332FNR2G	PLLC-20 (Pb-Free)	500 / Tape & Reel
MC10H332P	PDIP-20	18 Unit / Rail
MC10H332PG	PDIP-20 (Pb-Free)	18 Unit / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

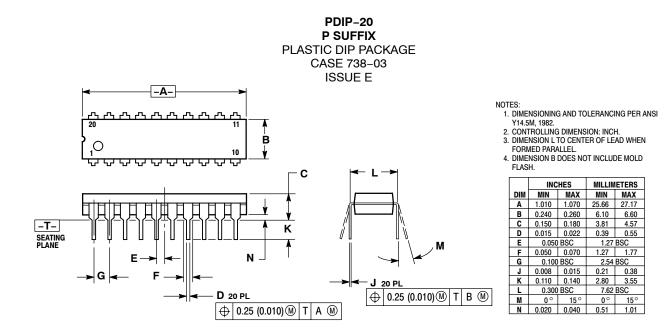
PACKAGE DIMENSIONS



CASE 775-02 ISSUE E



PACKAGE DIMENSIONS



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