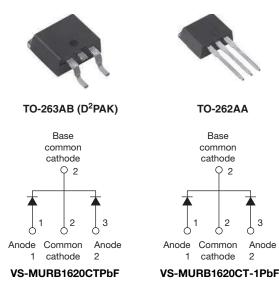
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# VS-MURB1620CTPbF, VS-MURB1620CT-1PbF

Vishay Semiconductors

# Ultrafast Rectifier, 2 x 8 A FRED Pt<sup>®</sup>



PRODUCT SUMMARY					
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA				
I <sub>F(AV)</sub>	2 x 8 A				
V <sub>R</sub>	200 V				
V <sub>F</sub> at I <sub>F</sub>	0.895 V				
t <sub>rr</sub>	35 ns				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				

### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum compliant peak of 260 °C
- e3 RoHS COMPLIANT

- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage		V <sub>RRM</sub>		200	V		
Average rectified forward current	per leg	1		8.0			
	total device	I <sub>F(AV)</sub>	Rated V <sub>R</sub> , T <sub>C</sub> = 150 °C	16			
Non-repetitive peak surge current per leg		I <sub>FSM</sub>		100	A		
Peak repetitive forward current per leg		I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 150 $^{\circ}$ C	16	1		
Operating junction and storage temperatures		$T_J, T_Stg$		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A	-	-	0.975	V	
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	-	0.895		
<b>D</b>		V <sub>R</sub> = V <sub>R</sub> rated	-	-	5		
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	- μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	25	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

Revision: 10-Jul-15

Document Number: 94519

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1



# VS-MURB1620CTPbF, VS-MURB1620CT-1PbF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	50 A/µs, V <sub>R</sub> = 30 V	-	-	35		
		$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25	20	
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	ns	
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	-	34	-		
Peak recovery current	IRRM	T <sub>J</sub> = 25 °C		-	1.7	-	- A	
		T <sub>J</sub> = 125 °C		-	4.2	-		
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	23	-	nC	
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	75	-	nc	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	3.0		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Maight			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Maultine alartica		Case style TO-263AB (D <sup>2</sup> PAK)	MURB1620CT				
Marking device		Case style TO-262		MURB1	620CT-1		



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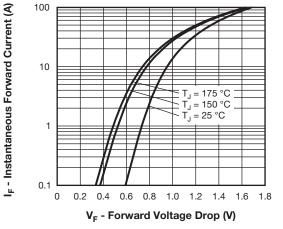
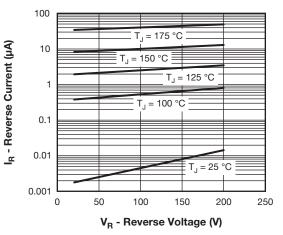
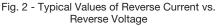


Fig. 1 - Typical Forward Voltage Drop Characteristics





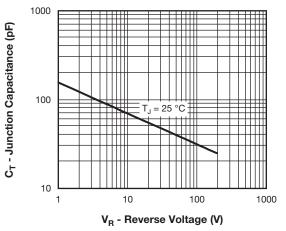


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

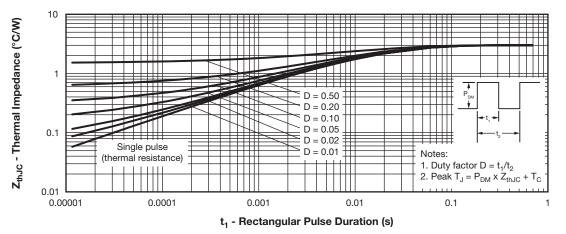


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

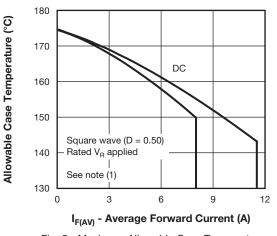
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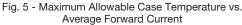
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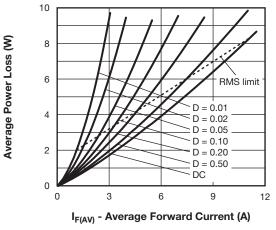
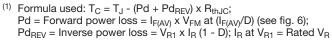


Fig. 6 - Forward Power Loss Characteristics

#### Note



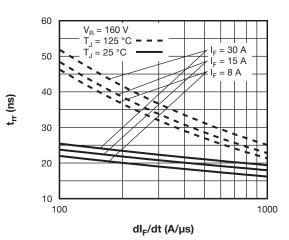


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

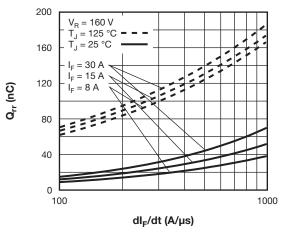


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

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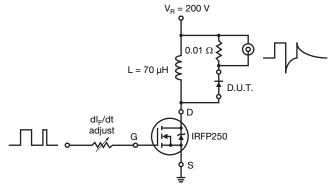


Fig. 9 - Reverse Recovery Parameter Test Circuit

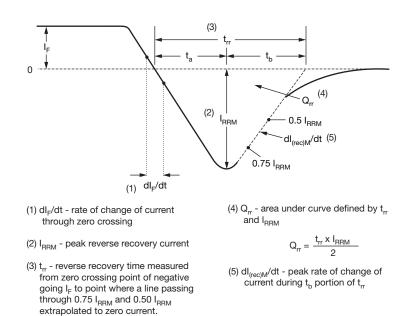


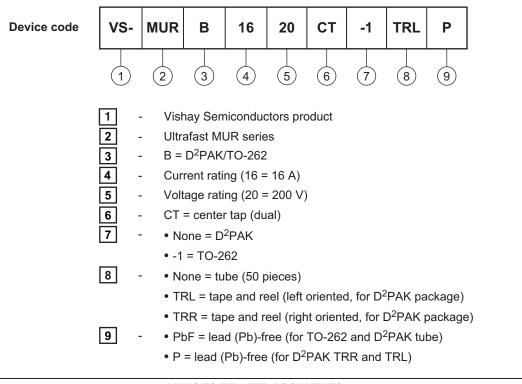
Fig. 10 - Reverse Recovery Waveform and Definitions

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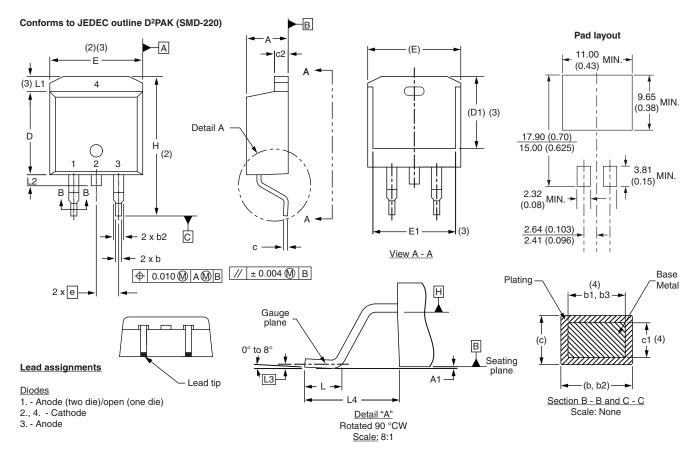
### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95014					
Part marking information	www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032					

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## D<sup>2</sup>PAK, TO-262



### DIMENSIONS - D<sup>2</sup>PAK in millimeters and inches

SHA

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

(7) Outline conforms to JEDEC outline TO-263AB

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

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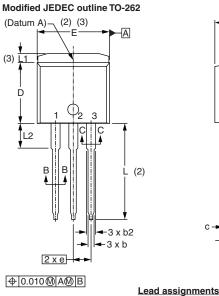
# **Outline Dimensions**

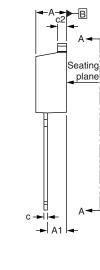
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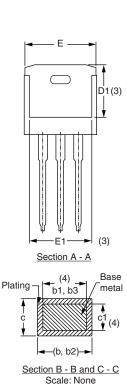
D<sup>2</sup>PAK, TO-262



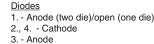
### **DIMENSIONS - TO-262** in millimeters and inches

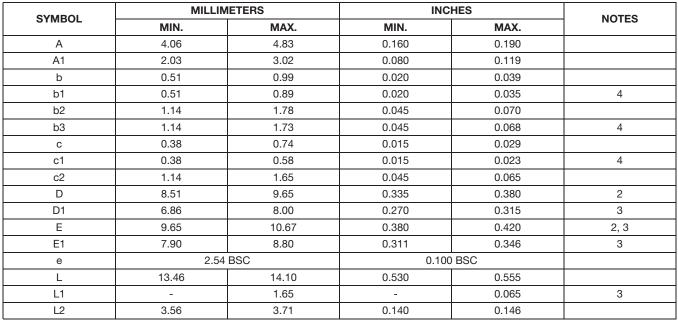






Lead tip





#### Notes

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- <sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
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- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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