

## Evaluation board for ISOSD61L isolated sigma-delta converter.



### Features

- Low voltage differential signaling
- 6 kV galvanic isolation
- 30 kV/ $\mu$ s high common-mode transient immunity
- 16-bit resolution, no missing codes
- $\pm 320$  mV full scale differential input signal range
- Up to 25 MHz external clock input
- High SNR (86 dB) and bandwidth (49 kHz)
- $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  extended industrial temperature range
- Coaxial and header pins available for flexible access to the inputs and outputs
- LVDS digital I/O (ISOSD61L) with on-board termination resistors
- Additional pads for the connection of a shunt current sensor

### Description

The EVALST-ISOSD61L is a full-featured evaluation board designed to allow the user to evaluate all the features of the ISOSD61L isolated analog-to-digital converter (ADC).

The ISOSD61L device is a 1-bit sigma-delta modulator with an output buffer separated from the input interface circuitry by a galvanic isolation barrier. The isolation barrier provides galvanic isolation of up to  $6000 V_{\text{PEAK}}$ . When used in combination with a digital filter, the ISOSD61L device can be used to achieve 16-bit analog-to-digital (A/D) conversion with no missing codes.

Product status link

[EVALST-ISOSD61L](#)

## Revision history

**Table 1. Document revision history**

Date	Version	Changes
11-Jan-2021	1	Initial release.
15-Jan-2021	2	Title updated.



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