ECE1105

GPIO Expansion, PS/2, 23x8 Keyscan Matrix Interface via SMBus or BC-Link Bus

General Description

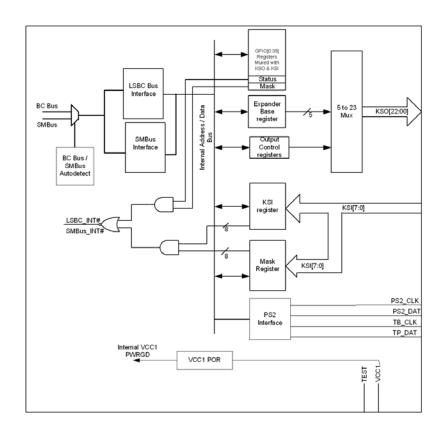
The ECE1105 is a 48-Pin 3.3V Keyboard Scan Expansion or GPIO Expansion device. The device supports a keyboard scan matrix of 23x8 and has two PS/2 ports for touchpad and/or pointer stick support. The device is connected to a Master via the BC-LinkTM interface or via the SMBus.

KSI and KSO signals are multiplexed with GPIOs.

Features

- · Up to 23x8 Keyboard Scan Matrix
- Two PS/2 Ports
 - Touch Pad Support
 - Pointer Stick Support
- 40 Multiplexed General Purpose I/O pins
 - All are MCU addressable I/O Pins
- · BC-Link Interconnect Bus
 - Link to embedded controller
- · SMBus Interconnect
 - One of two address selection
- · 3.3V Operation
 - 48-Pin, QFN RoHS Compliant package
 - 0.5mm Pitch
 - 7x7m Body size

Block Diagram



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PACKAGE OUTLINE

FIGURE 1: PACKAGE OUTLINE: 48-PIN QFN, 7X7 MM BODY (1 OF 2)

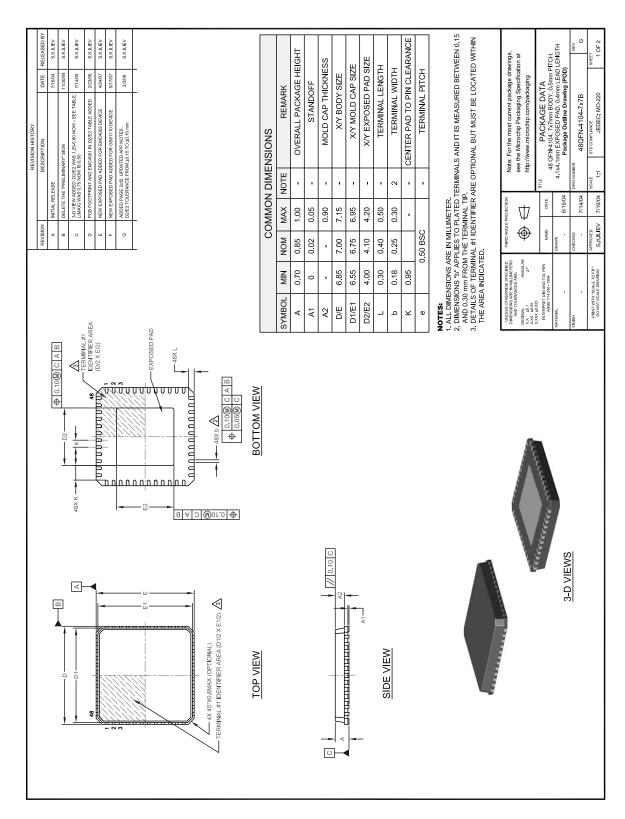


FIGURE 2: PACKAGE OUTLINE: 48-PIN QFN, 7X7 MM BODY (2 OF 2)

REVISION HISTORY	REV DESCRIPTION DATE RELEASED BY	G ADDED PAGE 20/2, UPDATED APP NOTES AND 2/3/09 S.K.ILEV D2/EZ TOLERANGE FROM ±0.15 TO ±0.10 mm S.K.ILEV	LAND PATTERN DIMENSIONS SYMBOL MIN NOM MAX GD/GE 6.00 - 6.10 GD/GE 6.00 - 1 GD/GE GD/GE	1. THE USER MAY MODIFY THE PCB LAND PATTERN DIMENSIONS BASED ON THEIR EXPERIENCE AND/OR PROCESS CAPABILITY. 2. THE LAND PATTERN CORRESPONDING TO THE PACKAGE EXPOSED PAD (IN THE CENTER) CAN BE LARGER, AND WITH DIFFERENT SHAPE THAN THE EXPOSED PAD ON THE PACKAGE. HOWEVER, THE SOLDERABLE AREA, AS DEFINED BY THE SOLDER MASK (SMD), OR NON-SOLDER MASK DEFINED (KSMD), SHOULD BE AS SHOWN FOR THE BEST THERMAL & ELECTRICAL PERFORMANCE. 3. MAXIMUM THERMAL AND ELECTRICAL PERFORMANCE IS AND THE REST THERMAL & ALCOTTER LAND PATTERN. (See Options 18. 2.) 4. THE VAS SHOULD BE AT 0.8 to 1.2MM PITCH WITH 0.30 TO 0.40MM DIAMETER, AND 1.0Z COPPER VIA BARREL PLATING. 5. NON SOLDER MASK DEFINED (NSMD) PAD DESIGN IS RECOMMENDED FOR PERIMETER LAND. 6. A LASER-CUT STAINLESS STEEL STENCIL IS RECOMMENDED WITH ELECTRO POLISED THE PETCH OF STENCIL THICKNESS	IS 0.125 mm FOR PITCHES 0.4 and 0.5 mm. 7. RECOMMENDED STENCIL AREA & ASPECT RATIOS ARE 0.66 & 1.5 (MIN) RESPECTIVELY. 8. RECOMMENDED STENCIL APERTURES ARE AS SHOWN. 9. IT IS RECOMMENDED TO USE "NO-CLEAN". TYPE 3 SOLDER PASTE. 10. THE REFLOW PROFILE DEPENDS ON THE EXACT SOLDER PASTE USED AND THE GIVEN BOARD DETAILS, SUCH AS GEOMETRY, COMPONENTS ETC.	UNISSO IMPROMS SECURITY UNISSO IMPROMS SECURITY WAS SOLD THE MISSO OF THE MISSON O	Title PACKAGE DATA	PINISH CHECKED DIVIGE DIVIGE REV REV
	UVD	FOR PERIMETER PAD AND SOLDER MASK	GE EZ GES GES GE EZ GES GES GES GES GES GES GES G	STENCIL OPENING - PERIMETER LANDS	OPTION 1 (NON-FLUCGED THERMAL VIAS) (LIMIN (MIN) — — — — 0.2-0.3 mm	Thermal Vass: 00.30mm.	Sternell Openinger: 00 52-0 56mm Sternell Openinger: 05 20 0 52mm	THERMAL VIAS and STENCIL OPENING - CENTER PAD

APPENDIX A: PRODUCT BRIEF REVISION HISTORY

TABLE A-1: REVISION HISTORY

Revision	Section/Figure/Entry	Correction		
DS00001729A (04-29-14)	REV A replaces previous SMSC version Rev. 1.6 (02-21-11)			

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PART NO. (1) Device	[X] - XXX (2) - [X] (3) Temperature Package Tape and Reel Range Option	a) b)		les: E1105-HZH = 48-pin QFN, Commercial E1105I-HZH = 48-pin QFN, Industrial
Device:	ECE1105 ⁽¹⁾	Note	e 1:	These products meet the halogen maximum concentration values per IEC61249-2-21.
Temperature Range: Package:	Blank = Commercial 0°C to 70°C I = Industrial -40°C to 85°C HZH = 48 pin QFN (2)	Note	e 2:	All package options are RoHS compliant. For RoHS compliance and environmental information, please visit http://www.micro-chip.com/pagehandler/en-us/aboutus/ehs.html .
Tape and Reel Option:	Blank = Tray packaging TR = Tape and Reel (3)	Note	e 3:	Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

ECE1105

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