



BSS84DWQ

Product Summary

| BV _{DSS} | RDS(on) Max | I D Max T _A = +25°С |
|-------------------|-----------------------------|--|
| -50V | 10Ω @ V _{GS} = -5V | -130mA |

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The BSS84DWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

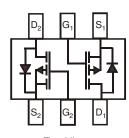
Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



SOT363

Top View



Top View Internal Schematic

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|--------|--------------------|
| BSS84DWQ-7 | SOT363 | 3,000/Tape & Reel |
| BSS84DWQ-13 | SOT363 | 10,000/Tape & Reel |

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

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| _ | | | | | |

K84 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

| Year | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 | 2032 |
|-------|------|------|------|------|------|------|------|------|------|------|------|------|
| Code | | J | K | L | М | N | 0 | Р | R | S | Т | U |
| | | | | | | | | | | | | |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec |

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit |
|-----------------------------|------------|------------------|-------|------|
| Drain-Source Voltage | | VDSS | -50 | V |
| Drain-Gate Voltage (Note 5) | | V _{DGR} | -50 | V |
| Gate-Source Voltage | Continuous | VGSS | ±20 | V |
| Drain Current (Note 6) | Continuous | ID | -130 | mA |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---|----------|-------------|------|
| Total Power Dissipation (Note 6) | PD | 300 | mW |
| Thermal Resistance, Junction to Ambient | Reja | 417 | °C/W |
| Operating and Storage Temperature Range | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|-------------------|------|------|------|------|---|
| OFF CHARACTERISTICS (Note 7) | | | | | | · |
| Drain-Source Breakdown Voltage | BV _{DSS} | -50 | -75 | _ | V | $V_{GS} = 0V, I_D = -250\mu A$ |
| | | — | | -1 | μA | V _{DS} = -50V, V _{GS} = 0V, T _J = +25°C |
| Zero Gate Voltage Drain Current | IDSS | | _ | -2 | μA | V _{DS} = -50V, V _{GS} = 0V, T _J = +125°C |
| Zero Gale Vollage Drain Gurrent | | — | — | -100 | nA | $V_{DS} = -25V, V_{GS} = 0V, T_J = +25^{\circ}C$ |
| Gate-Body Leakage | lgss | _ | _ | ±10 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | VGS(th) | -0.8 | -1.6 | -2.0 | V | $V_{DS} = V_{GS}$, $I_D = -1mA$ |
| Static Drain-Source On-Resistance | Rds(on) | — | 6 | 10 | Ω | V _{GS} = -5V, I _D = -0.1A |
| Forward Transconductance | g fs | 0.05 | _ | _ | S | V _{DS} = -25V, I _D = -0.1A |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | · |
| Input Capacitance | Ciss | _ | _ | 45 | pF | |
| Output Capacitance | Coss | _ | _ | 25 | pF | V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz |
| Reverse Transfer Capacitance | Crss | _ | _ | 12 | pF | |
| SWITCHING CHARACTERISTICS (Note 8) | - | | | | | · |
| Turn-On Delay Time | td(on) | _ | 10 | _ | ns | V _{DD} = -30V, I _D = -0.27A, |
| Turn-Off Delay Time | tD(OFF) | | 18 | | ns | $R_{GEN} = 50\Omega$, $V_{GS} = -10V$ |

 $Notes: \qquad 5. \ R_{GS} \leq 20 k \Omega.$

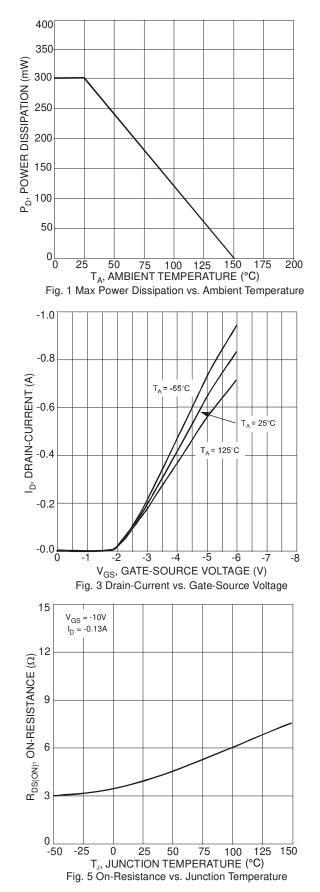
6. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Incorporated's suggested pad layout document,

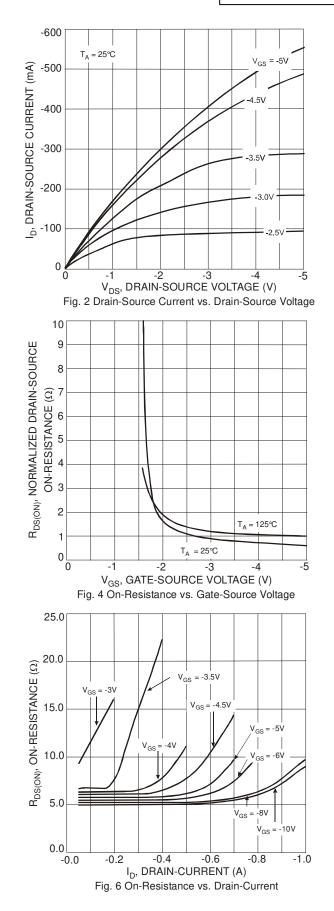
which can be found on our website at http://www.diodes.com/package-outlines.html.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



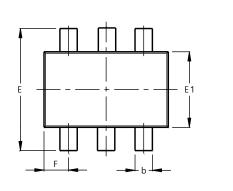


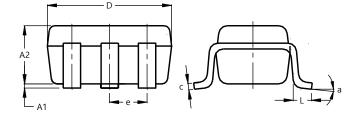




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

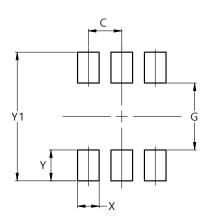




| | SOT363 | | | | | | | |
|-------|--------|-------------|-------|--|--|--|--|--|
| Dim | Min | Min Max Typ | | | | | | |
| A1 | 0.00 | 0.10 | 0.05 | | | | | |
| A2 | 0.90 | 1.00 | 0.95 | | | | | |
| b | 0.10 | 0.30 | 0.25 | | | | | |
| С | 0.10 | 0.22 | 0.11 | | | | | |
| D | 1.80 | 2.20 | 2.15 | | | | | |
| Е | 2.00 | 2.20 | 2.10 | | | | | |
| E1 | 1.15 | 1.35 | 1.30 | | | | | |
| e | C |).650 E | SC | | | | | |
| F | 0.40 | 0.45 | 0.425 | | | | | |
| L | 0.25 | 0.40 | 0.30 | | | | | |
| а | 0° | 8° | | | | | | |
| All I | Dimen | sions | in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



 Dimensions
 Value (in mm)

 C
 0.650

 G
 1.300

 X
 0.420

 Y
 0.600

 Y1
 2.500

SOT363

SOT363



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