

Vishay High Power Products

## Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

3

Anode

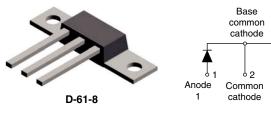
2

3

Anode

2

VS-113CNQ100APbF

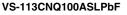


#### VS-113CNQ100ASMPbF



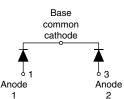


#### D-01-0-310





D-61-8-SL



۶2

Common

cathode

01

Anode

1

# PRODUCT SUMMARY IF(AV) 2 x 55 A VR 100 V

## FEATURES

- 175 °C T<sub>J</sub> operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

## DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	110	A	
V <sub>RRM</sub>		100	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	7000	A	
V <sub>F</sub>	55 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.67	V	
TJ	Range	- 55 to 175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-113CNQ100APbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v	



<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply



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ABSOLUTE MAXIMUM RATINGS					
PARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg	levu a	50 % duty cycle at $T_{C}$ = 150 °C, rectangular waveform		55	А
See fig. 5 per device	I <sub>F(AV)</sub>			110	
Maximum peak one cycle	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	7000	А
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	720	~
Non-repetitive avalanche energy per leg E		T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 30 mH		15	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu s$ Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	55 A	T <sub>J</sub> = 25 °C	0.81	V
Maximum forward voltage drop per leg		110 A		1.00	
See fig. 1		55 A	T <sub>J</sub> = 125 °C	0.66	
		110 A		0.79	
Maximum reverse leakage current per lea	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{R} = Rated V_{R}$	1.0	mA
		T <sub>J</sub> = 125 °C		32	
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1960	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

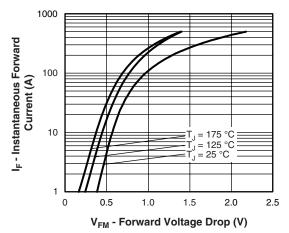
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

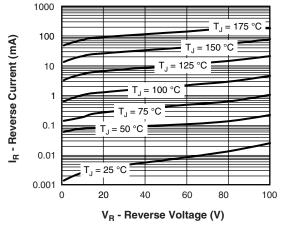
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	0.5		
Maximum thermal resistance, junction to case per package	R <sub>thJC</sub>	DC operation	0.25	°C/W	
Typical thermal resistance, case to heatsink (D-61-8 only)	R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimento uvoight			7.8	g	
Approximate weight			0.28	oz.	
Mounting torque minimum		Recommended hardware 3M stainless screw	12 (10)	kgf∙cm	
(D-61-8 only) maximum			24 (20)	(lbf · in)	
		Case style D-61	113CN	Q100A	
Marking device		Case style D-61-8-SM	113CNQ	100ASM	
		Case style D-61-8-SL	113CNQ	100ASL	



Schottky Rectifier Vishay H New Generation 3 D-61 Package, 2 x 55 A

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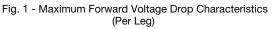


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

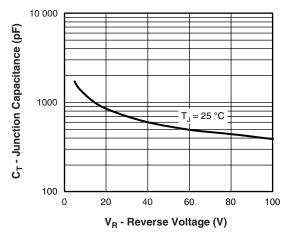


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

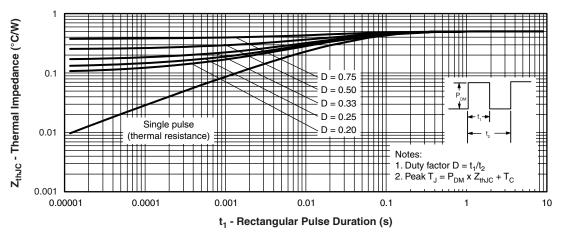
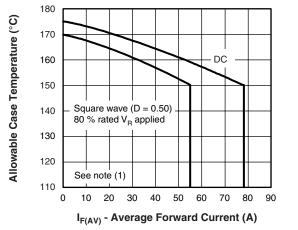


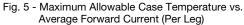
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

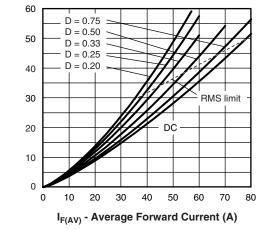
## Vishay High Power Products

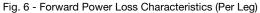
Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

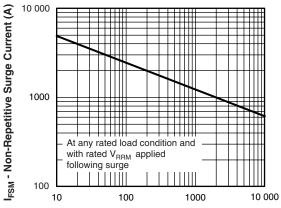
Average Power Loss (W)











t<sub>n</sub> - Square Wave Pulse Duration (µs)



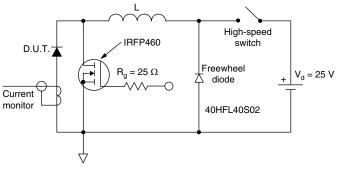


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$  Pd = Forward power loss = 1 + x V + ct (U + (D) + ct (U + (D)))
  - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

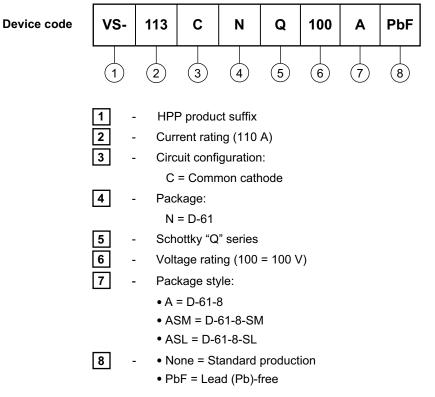


Schottky Rectifier

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### ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

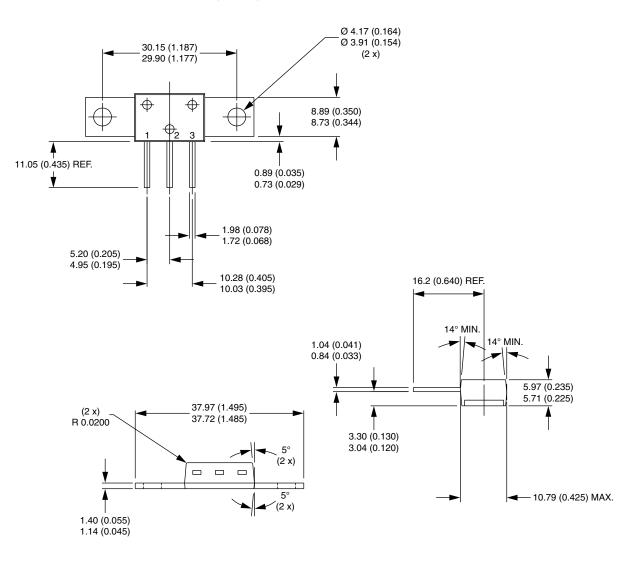
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95354			
Part marking information	www.vishay.com/doc?95356			

**Vishay Semiconductors** 



D-61-8, D-61-8-SM, D-61-8-SL

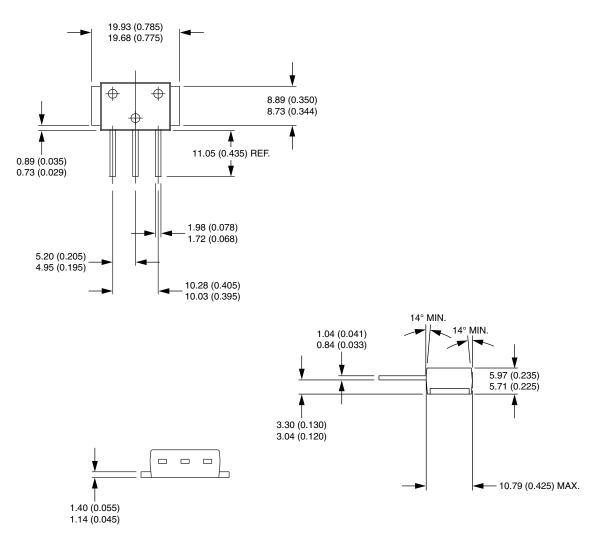
#### DIMENSIONS - D-61-8 in millimeters (inches)





### DIMENSIONS - D-61-8-SM in millimeters (inches)

Vishay Semiconductors



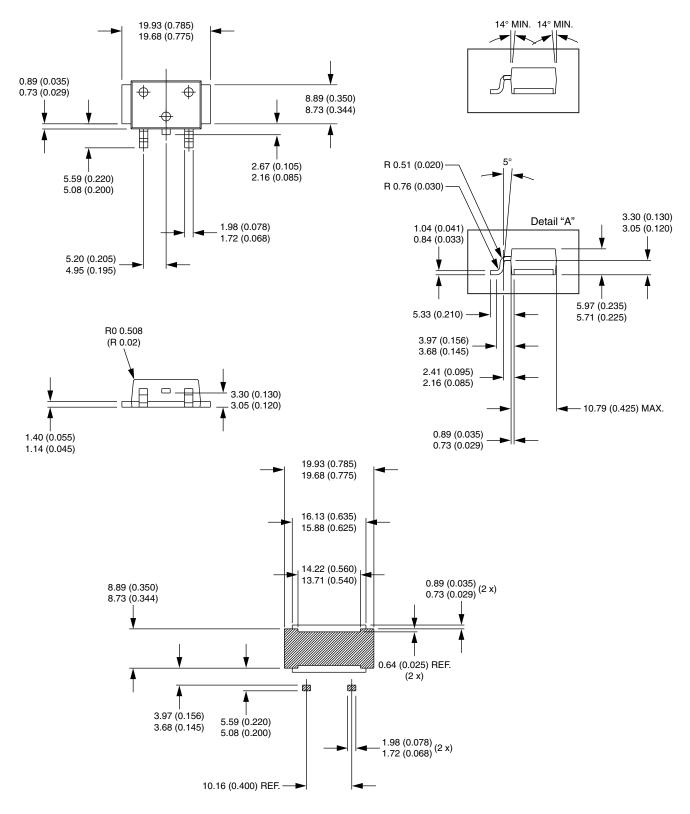
 Revision: 28-Sep-11
 2
 Document Number: 95354

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## DIMENSIONS - D-61-8-SL in millimeters (inches)

**Vishay Semiconductors** 





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