Product data sheet

1. General description

The ESDAHD712BE2 is designed for asymmetrical (12V to -7V) protection in multi-point data transmission standard RS-485 applications. The ESDAHD712BE2 can be used to protect devices from transient voltages resulting from electrostatic discharge (ESD), electrical fast transients (EFT), and lightning induced surges.



2. Features and benefits

- Peak pulse power 500W @ 8/20us waveform
- IEC 61000-4-2 (ESD) ±30kV(air), ±30kV(contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 19A (8/20μs)
- Protects two +12V to -7V lines
- Low capacitance
- Low leakage current
- Low clamping voltage
- Meet MSL level1
- Halogen free and RoHS compliant





3. Applications

- Protection of RS-485 transceivers with extended common-mode range
- · Security systems
- Automatic Teller Machines
- · HFC systems
- Networks

4. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
ESDAHD712BE2	SOT23	ESDAHD712BE2X	Tape and reel	3000	SOT23X	13-Oct-2020

5. Absolute maximum ratings

In accordance with the Absolute Maximum Rating System (IEC 60134). T = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Values	Unit	
Absolute maximum rating					
P _{PPM}	peak pulse power	t _p = 8/20 μs	500	W	
I _{PP}	peak pulse current	t _p = 8/20 μs	19	А	
V _{ESD}	ESD per IEC 61000-4-2 (air) ESD per IEC 61000-4-2 (contact)		±30 ±30	kV kV	
T _{stg}	storage temperature range		-55 to 150	°C	
T _j	operating temperature range		-55 to 150	°C	

6. Characteristics

 T_i = 25 °C unless otherwise specified.

Symbol	Parameter	Condition	Pin 1 to 3 and Pin 2 to 3 (12V TVS)			Pin 3 to 1 and Pin 3 to 2 (7V TVS)			Unit
			Min	Тур	Max	Min	Тур	Max	
V_{RWM}	Reverse Working Voltage	Pin 3 to 1 or Pin 2 to 1	-	-	12	-	-	7	V
V_{BR}	Reverse Breakdown Voltage	I _T = 1 mA	13.3			7.5	-	-	V
I _R	Reverse Leakage Current	$V_R = V_{RWM}$	-	-	1	-	-	20	μA
V _C	Clamping Voltage	$I_{PP} = 5 \text{ A}; t_p = 8/20 \mu\text{s}$	-	-	22	-	-	15	V
		$I_{PP} = 19 \text{ A}; t_p = 8/20 \mu\text{s}$	-	-	30	-	-	18	V
CJ	Junction Capacitance	V _R = 0 V; f = 1 MHz	-	-	75	-	-	75	pF
		$V_R = V_{RWM}$; $f = 1 \text{ MHz}$	-	45	-	-	45	-	pF

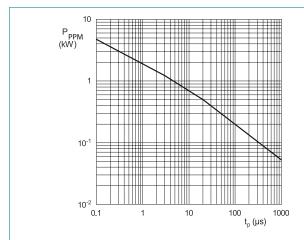


Fig. 1. Pulse rating curve

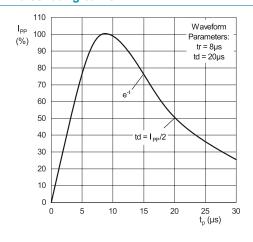


Fig. 3. Pulse waveform

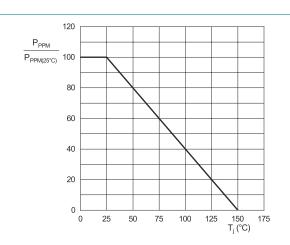


Fig. 2. Peak pulse power derating curve

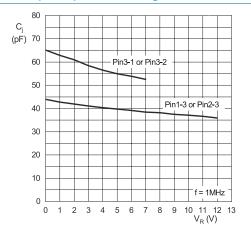
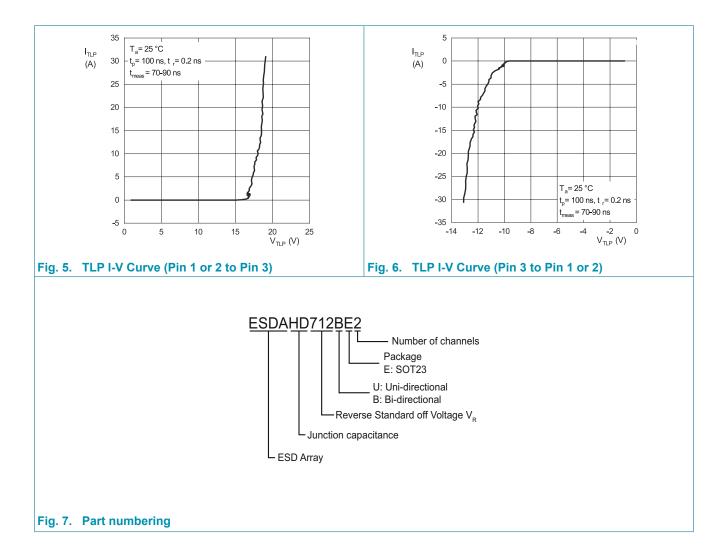
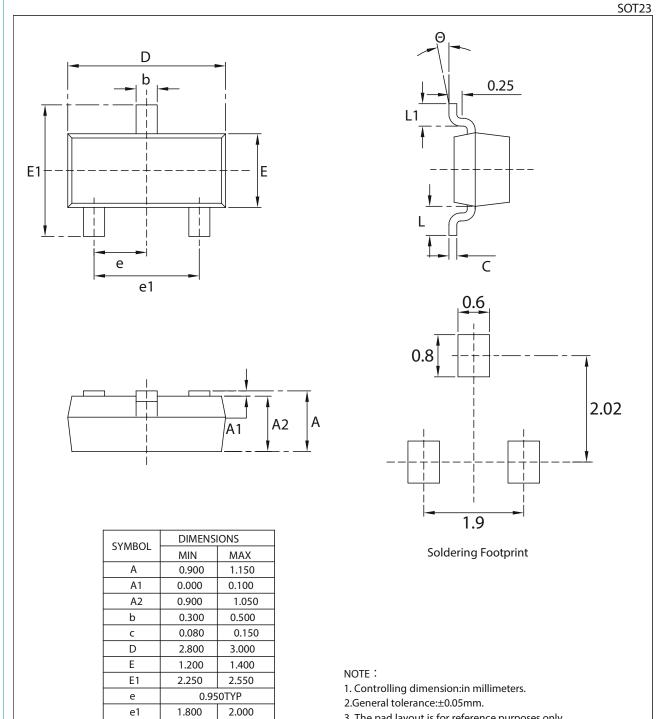


Fig. 4. Capacitance vs reverse voltage

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7. Package outline



- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

ESDAHD712BE2

0.550REF

0.500

Unit: mm

0.300

L1

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8. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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