



SCHOTTKY BARRIER DIODE

Product Summary (@TA = +25°C)

Name	V _{RRM} (V)	I _O (A)	V _F Max (V)	I _R Max (μΑ)
SD103AW	40	0.2	0.60	5.0µA@30V
SD103BW	30	0.2	0.60	5.0µA@20V
SD103CW	20	0.2	0.60	5.0µA@10V

Description

These are 0.2A, 20V/30V/40V Schottky rectifiers packaged in SOD123 package.

Applications

Providing low V_F and low reserve leakage, this device is ideal for use in general rectification applications such as:

- Low voltage rectifications
- High-efficiency DC-DC conversions
- Switch mode power supplies
- Inverse polarity protections

Features and Benefits

- Low Forward Voltage Drop (VF)
- Better Efficiency and Cooler Operation
- Guard Ring Construction for Transient Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOD123
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (€3)
- Polarity: Cathode Band
- Weight: 0.01 grams (Approximate)

SOD123



Top View

Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Package	Qty.	Carrier	
SD103AW-7-F	SOD123	3000	Tape and Reel	
SD103BW-7-F	SOD123	3000	Tape and Reel	
SD103CW-7-F	SOD123	3000	Tape and Reel	
SD103CW-13-F	SOD123	10,000	Tape and Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



Bar Denotes Cathode Pin

XX = Product Type Marking Code S4 = SD103AWS5 or S4 = SD103BW S6 or S5 or S4 = SD103CW

YM & $\overline{Y}M$ = Date Code Marking Y& \overline{Y} = Year (ex: J = 2022) M = Month (ex: 9 = September)



Date Code Kev

Year	2003		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Р		J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@ $TA = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	SD103AW	SD103BW	SD103CW	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM	40	30	20	V
RMS Reverse Voltage	V _R (RMS)	28	21	14	V
Forward Continuous Current (Note 5)	I _{FM}		350		mA
Non-Repetitive Peak Forward Surge Current @ t ≤ 1.0s	IFSM		1.5		Α

Thermal Characteristics

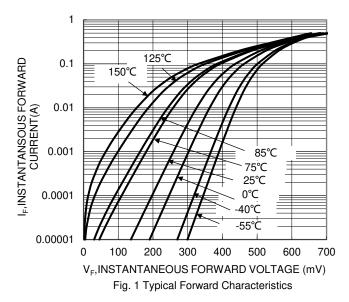
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	367	mW
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	340	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

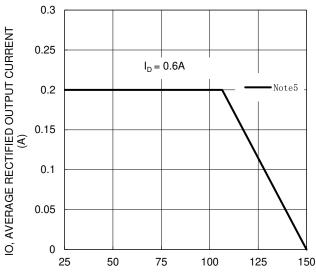
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	SD103AW SD103BW SD103CW	V _{(BR)R}	40 30 20		_	V	I _R = 100μA
Forward Voltage Drop		V _{FM}	I	١	0.37 0.60	V	IF = 20mA IF = 200mA
Peak Reverse Current (Note 6)	SD103AW SD103BW SD103CW	I _{RM}			5.0	μΑ	$\begin{aligned} V_R &= 30V \\ V_R &= 20V \\ V_R &= 10V \end{aligned}$
Total Capacitance		Ст	_	28	_	pF	$V_R = 0V$, $f = 1.0MHz$
Reverse Recovery Time		t _{RR}		10	_	ns	$I_F = I_R = 200 \text{mA},$ $I_{RR} = 0.1 \times I_R, R_L = 100 \Omega$

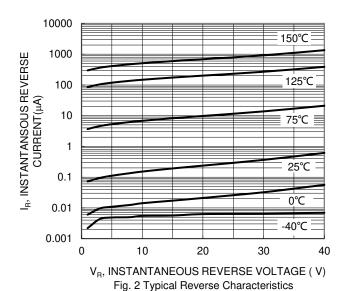
5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
6. Short duration test pulse used to minimize self-heating effect. Notes:







 T_A , Ambient Temperature (°C) Fig 4. DC Forward Current Derating



30 15 10 10 10 10 10 10

0 25 50 75 100
PERCENT OF RATED PEAK REVERSE VOLTAGE (%)
Fig. 3 Total Capacitance vs. Reverse Voltage

5

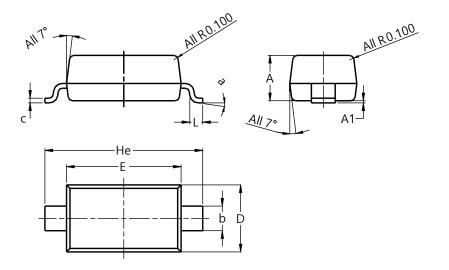
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD123

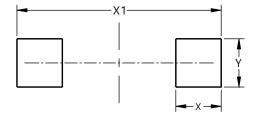


SOD123							
Dim	Min	Max	Тур				
Α	1.00	1.35	1.05				
A1	0.00	0.10	0.05				
b	0.52	0.62	0.57				
С	0.10	0.15	0.11				
D	1.40	1.70	1.55				
Е	2.55	2.85	2.65				
He	3.55	3.85	3.65				
L	0.25	0.40	0.30				
а	0°	8º					
All [Dimens	ions in	mm				

Suggested Pad Layout

 $Please\ see\ http://www.diodes.com/package-outlines.html\ for\ the\ latest\ version.$

SOD123



Dimensions	Value (in mm)
X	0.900
X1	4.050
Υ	0.950



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