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# One Watt Darlington Transistor

# **NPN Silicon**

### Features

• Pb-Free Package is Available\*

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CES</sub>	30	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	30	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	10	Vdc
Collector Current – Continuous	۱ <sub>C</sub>	1.0	Adc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1.0 8.0	W mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	2.5 20	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL CHARACTERISTICS

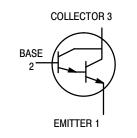
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



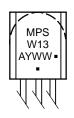
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### MARKING DIAGRAM



MPSW13	= Device Code
А	= Assembly Location
Y	= Year
WW	= Work Week
•	= Pb-Free Package
(Note: Microdo	t may be in either location)

### **ORDERING INFORMATION**

De	evice	Package	Shipping <sup>†</sup>
MPSV	V13RLRA	TO-92	2,000/Tape & Reel
MPSV	V13RLRAG	TO–92 (Pb–Free)	2,000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## MPSW13

### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Мах	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage $(I_C = 100 \ \mu Adc, \ V_{BE} = 0)$	V <sub>(BR)CES</sub>	30	-	Vdc
Collector Cutoff Current ( $V_{CB} = 30$ Vdc, $I_E = 0$ )	Ісво	-	100	nAdc
Emitter Cutoff Current ( $V_{EB} = 10$ Vdc, $I_C = 0$ )	I <sub>EBO</sub>	-	100	nAdc
ON CHARACTERISTICS (Note 1)				
DC Current Gain (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 5.0 Vdc) (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	5,000 10,000		-
Collector-Emitter Saturation Voltage $(I_C = 100 \text{ mAdc}, I_B = 0.1 \text{ mAdc})$	V <sub>CE(sat)</sub>	-	1.5	Vdc
Base-Emitter On Voltage ( $I_C = 100 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$ )	V <sub>BE(on)</sub>	-	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current–Gain – Bandwidth Product (Note 2) $(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz})$	f <sub>T</sub>	125	-	MHz

1. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

2.  $f_T = |h_{fe}| \bullet f_{test}$ .

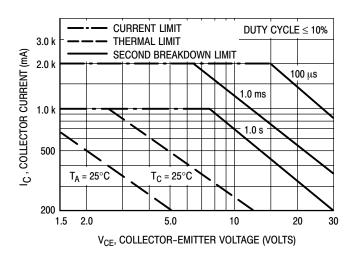
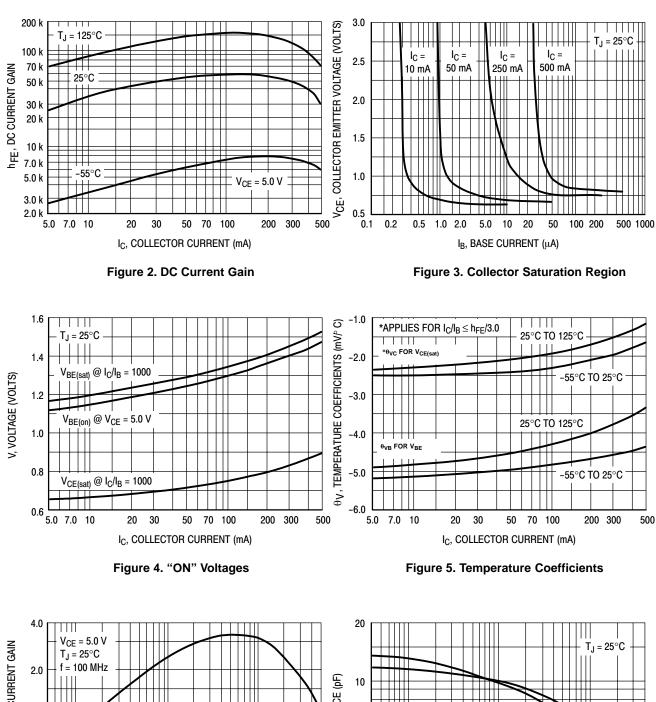
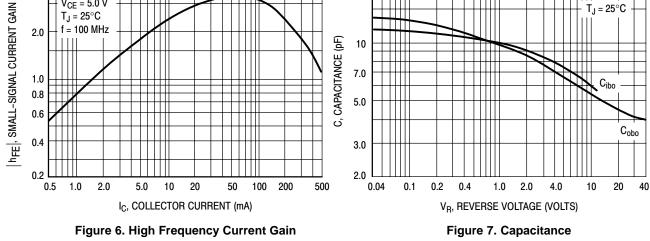


Figure 1. Active Region – Safe Operating Area

MPSW13

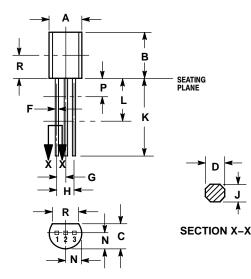




### MPSW13

### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- I. DIMENSION F APPLIES BETWEEN P AND L DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
L	0.018	0.024	0.46	0.61
Κ	0.500		12.70	
Г	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.135		3.43	

STYLE 1: PIN 1. EMITTER

2. BASE

3. COLLECTOR

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