

Product Summary

BVDSS	Rds(on) Max	I _D Max Tc = +25°C
001/	6.9mΩ @ V _{GS} = 10V	70A
80V	10.4mΩ @ V _{GS} = 4.5V	57A

Description

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

Backlighting

- **Power Management Functions**
- **DC-DC Converters**

Features and Benefits

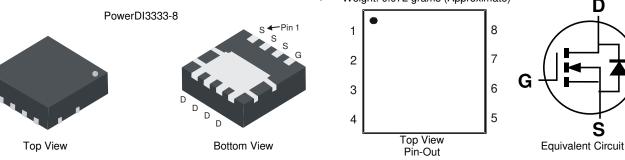
- Rated to +175°C Ideal for High Ambient Temperature Environments
- Low RDS(ON) Ensures On-State Losses are Minimized
- Excellent Q_{ad} × R_{DS(ON)} Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher **Density End Products**
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH8008LFG-7	PowerDI3333-8	2000/Tape & Reel
DMTH8008LFG-13	PowerDI3333-8	3000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. Notes:

2. See https://www.diodes.com/guality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free. "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



HX8 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 20 = 2020) WW = Week Code (01 to 53)

D

S



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	80	V	
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 7) V _{GS} = 10V	Tc = +25°C Tc = +100°C	ID	70 49	А
Continuous Drain Current (Note 6) V _{GS} = 10V	T _A = +25°C T _A = +100°C	ID	17 12	А
Maximum Continuous Body Diode Forward Current (Note 6)		ls	17	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	ldм	280	А	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycl	lsм	280	А	
Avalanche Current, L = 1mH (Note 8)	IAS	18	А	
Avalanche Energy, L = 1mH (Note 8)	Eas	162	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	124	°C/W
Total Power Dissipation (Note 6)	TA = +25°C	PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	53	°C/W
Total Power Dissipation (Note 7)	Tc = +25°C	PD	50	W
Thermal Resistance, Junction to Case (Note 7)	Rejc	3	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	٥°	

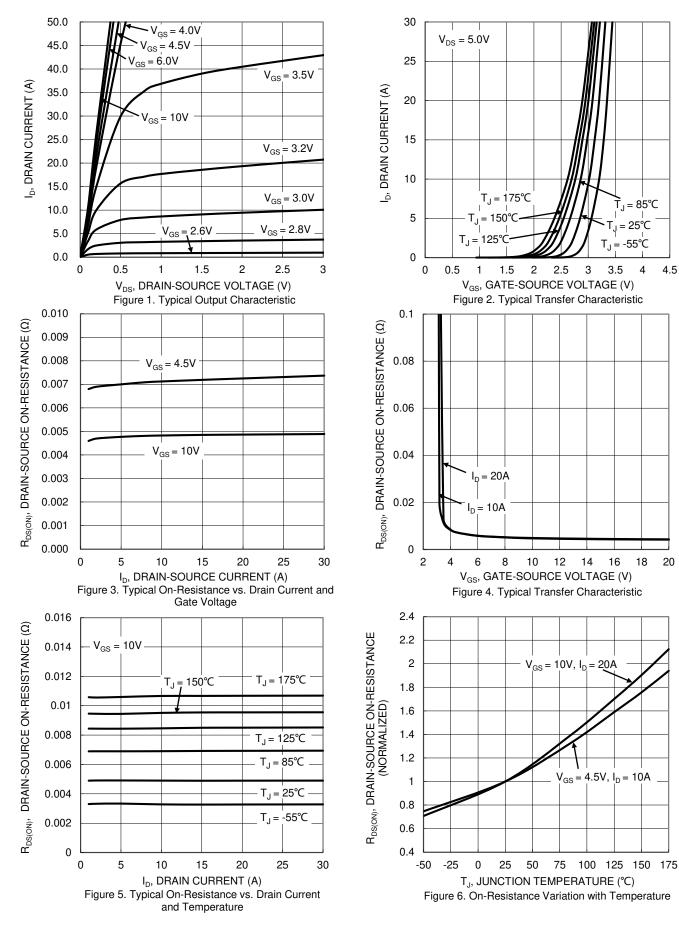
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Obevestavistic	Cumahal	Min	True	May	11	Test Candition	
	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)						T	
Drain-Source Breakdown Voltage	BVDSS	80	—	—	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	—	—	1	μA	$V_{DS} = 64V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	VGS(TH)	1.2		2.5	V	$V_{DS} = V_{GS}, I_D = 1mA$	
Static Drain-Source On-Resistance	Deserve	—	5.3	6.9	mΩ	V _{GS} = 10V, I _D = 20A	
	R _{DS(ON)}	_	7.9	10.4	11152	V _{GS} = 4.5V, I _D = 10A	
Diode Forward Voltage	Vsd	_	0.8	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 10)						·	
Input Capacitance	Ciss	_	2254	—		$V_{DS} = 40V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	_	745	_	pF		
Reverse Transfer Capacitance	Crss	_	31	_			
Gate Resistance	Rg	_	1.98	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	18.3	_		V _{DS} = 40V, I _D = 14A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	37.7	_	nC		
Gate-Source Charge	Qgs	_	5.3	_	nc		
Gate-Drain Charge	Q _{gd}	_	7.8	_			
Turn-On Delay Time	tD(ON)	_	6.9	_		$\label{eq:VDD} \begin{split} V_{DD} &= 40V, \ V_{GS} = 10V, \\ I_D &= 14A, \ R_G = 6\Omega \end{split}$	
Turn-On Rise Time	t _R	_	12	_			
Turn-Off Delay Time	tD(OFF)	_	37	_	ns		
Turn-Off Fall Time	tF		21	_	1		
Body Diode Reverse Recovery Time	trr		42	—	ns	L 144 di/dt 1004/0-	
Body Diode Reverse Recovery Charge	Qrr	—	53	—	nC	—I _S = 14A, di/dt = 100A/μs	

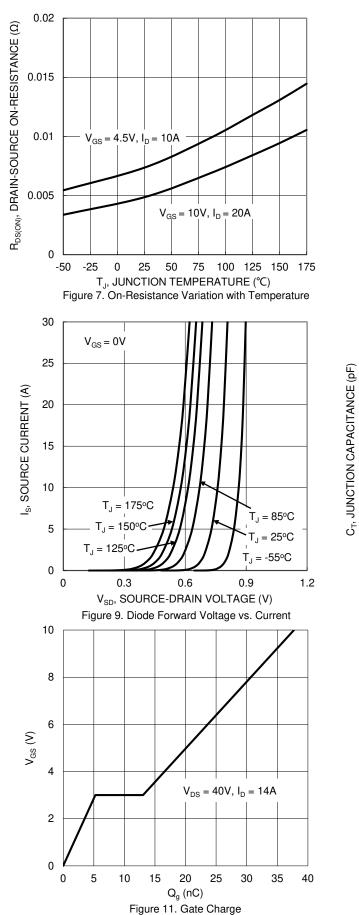
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Thermal resistance from junction to soldering point (on the exposed drain pad).
8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
9. Short duration pulse test used to minimize self-heating effect.
10. Guaranteed by design. Not subject to product testing.

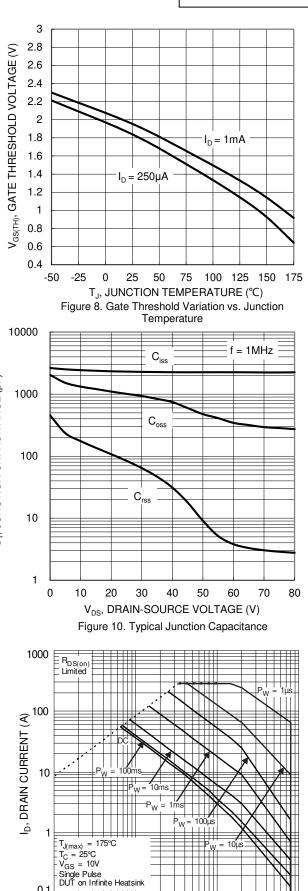
Notes:











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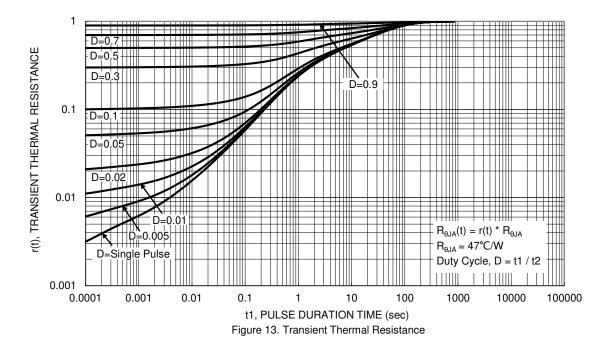
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V_{DS} DRAIN-SOURCE VOLTAGE (V)

Figure 12 SOA, Safe Operation Area

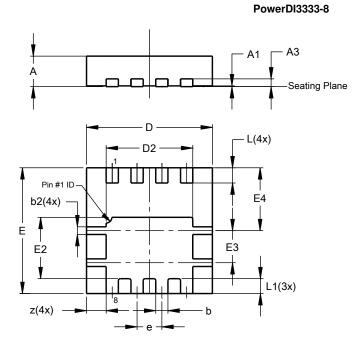






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

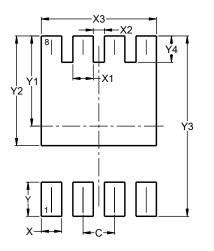


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3			0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
Е	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
e			0.65		
L	0.35	0.45	0.40		
L1	_	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
X3	2.370		
Y	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



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