CTMMP2818AF Series From 0.47 µH to 22 µH



CHARACTERISTICS

Description: SMD (shielded) Power Inductor.

Applications: Notebook, Desktop, Server applications, Low profile, high current power supplies, battery powered devices, DC/DC converter for Field Programmable Gate Array (FPGA).

Operating Temperature: -40°C to +125°C (The part temperature (ambient + temp. rise) should not exceed 125°C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application)

Inductance Tolerance: ±20%

Testing: Inductance is tested on an HP4285A at 100KHz, 1.0V

Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical

information available upon request.

Samples available. See website for ordering information.

SPECIFICATIONS

Parts are available in $\pm 20\%$ inductance tolerance only. *Irms: Will cause the coil temp. rise approximately ΔT of 40°C. (Keep 1 Min) **Isat: Will cause L0 to drop 20% typical. (Keep quickly)

Part Number	Inductance (µH)	*Irms (A) Typ.	**Isat (A) Typ.	DCR (mΩ) Typ.	DCR (mΩ) Max.		
CTMMP2818AF-R47M CTMMP2818AF-R56M CTMMP2818AF-R60M CTMMP2818AF-R68M CTMMP2818AF-R82M	0.47 0.56 0.60 0.68 0.82	22 20 19 18 16.5	30 27 25 24 22	3.5 3.6 3.8 4.0 4.6	3.9 4.2 4.3 4.5 4.9		
CTMMP2818AF-1R0M CTMMP2818AF-1R2M CTMMP2818AF-1R5M CTMMP2818AF-2R2M CTMMP2818AF-3R3M CTMMP2818AF-4R7M CTMMP2818AF-5R6M CTMMP2818AF-6R6M CTMMP2818AF-6R8M CTMMP2818AF-8R2M	1.00 1.20 1.50 2.20 3.30 4.70 5.60 6.80 8.20	15 14 12 10 8 6.5 6.0 5.5 5.0	20 18 16.5 14 12 10 9.0 8.5 8.0	6.1 6.7 8.6 11.2 19 28 43.5 46 56	6.5 7.5 9.0 12.0 20.9 30.8 49 51.5 63		
CTMMP2818AF-100M CTMMP2818AF-220M	10.0 22.0	4.0 2.5	7.5 5.5	60 140	69 170		

PHYSICAL DIMENSIONS

Size	Α	В	C	D	E
mm inches	7.3±0.3 0.29±0.012	6.6±0.3 0.26±0.012	4.8±0.2 0.19±0.008	1.8±0.3 0.071±0.012	3.0±0.3 0.12±0.012
	XX	X	-Marking: I	nductance Co	de

PAD LAYOUT

