

#### 1 SCOPE

## 1.1 Content

This specification covers the performance, test and quality requirements for the Tyco Electronics High Speed 8 pairs IO board and cable connector. The cable plug assembly is used to connect the cable to the board connector.

#### 1.2 Qualification

When tests are performed on subject product, procedures specified in this Product Specification shall be used. All inspections shall be performed using applicable inspection plan and product drawing.

#### 1.3 Applicable partnumbers

The partnumbers of subject products:

- X-2042847-x HSIO Cable Plug Assembly
- X-2042853-x HSIO Board Connector Assembly, version 1 (referred to as BCv1) •
- X-1551966-x HSIO Board Connector Assembly, version 2 (referred to as BCv2)

#### 1.4 Completion

Qualification testing of the HSIO Board Connector Assembly version 1 has successfully been completed on 20JUL2011 and is reported in Qualification Test Report with number 501-19154. This documentation is on file and available from Engineering Practices and Standards (EPS).

Qualification testing of HSIO Board Connector Assembly version 2 is yet to be completed.

#### **Applicable Documents** 2

The following documents form a part of this specification to extend specified herein. Unless otherwise specified, latest edition of the document applies. In the event of conflict between the requirements in this specification and the product drawing, product drawing shall take precedence. In the event of conflict between requirements of this specification and referenced documents, this specification shall take precedence.

#### 2.1 Tyco Documents

501-19154 Test report. Cable/Board High Speed Connector assembly (version 1). Application specification High Speed Cable/Board Connector System. 114-19129

#### 2.2 Other Documents

IEC 60512 Basic testing procedures and measuring methods for electromechanical components for electronic equipment.

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# 3 <u>Requirements:</u>

# 3.1 Design and Construction.

Products shall be of design, construction and physical dimensions as specified on the applicable customer product drawings C-2042847 (Cable-connector), C-2042847 (Board Connector version 1) and C-1551966 (Board-connector version 2).

## 3.2 Materials and Finish.

Materials used in the construction of this product shall be as specified on the applicable customer drawing.

## 3.3 <u>Ratings</u>

Α.	Data-rate (differential)	- 10 Gb/s

- B. Impedance 100 ohm
- C. Operating voltage 30 Volt DC max.
- D. Current 0,5 Amp Max.
- E. Operating temperature  $-55^{\circ}$  to  $105^{\circ}$ .
- F. Durability 100 cycles.

# 3.4 Performance and Test Description.

The product is designed to meet mechanical and environmental performance specified in this paragraph as tested per test sequence specified in paragraph 3.5.

Unless otherwise specified, all tests are performed at ambient environmental conditions per IEC specification 60068-1 clause 5.3 and are performed with connectors in mated condition.

	VISUAL						
Para	Test Description	Procedures					
3.4.1.	Examination of product	Meets requirements of product Drawing and applicable instructions on customer drawing, instruction sheet, application specification.	Visual, dimensional and functional per applicable inspection plan.				

		ELECTRICAL	
Para	Test Description	Performance Requirements or Severity	Procedures
3.4.2	Termination resistance Board connector – Cable connector (signal and ground)	Max. open voltage 20mV. Max. current 100 mA DC. All contacts to be measured. Measuring points shall be as indicated in fig.1 Requirement: 30 m $\Omega$ max. (Initial; exclusive bulk) 40 m $\Omega$ max. (Final; exclusive bulk)	In acc. With IEC 60512-2-1
3.4.3	Termination resistance Board connector – Cable connector (shield)	Max. open voltage 20mV. Max. current 100 mA DC. Measuring points shall be as indicated in fig 2. Requirement: Initial: 10 m $\Omega$ max. Final: 20 m $\Omega$ max.	In acc. With IEC 60512-2-1
3.4.4	Insulation resistance	Test voltage 100V DC. Duration: 1 minute. Test between adjacent contacts. Requirement: $10^3 M\Omega$ min.	In acc. with IEC 60512-3-1
3.4.5	Voltage proof	Test voltage 300 VDC for adjacent contacts. Duration 1 minute. Requirement: no break-down or flash-over	In acc. with IEC 60512-4-1
3.4.6	Current temperature de-rating curve	Temperature rise: 30°C maximum over ambient temperature (65°C) at 0,5 A	In acc. with IEC 60512-5-2 / test 5b



	MECHANICAL						
Para	Test Description	Performance Requirements or Severity	Procedures				
3.4.10	Signal Contact spring force	Min. 0,5N	At deflection of 0.165 mm.				
3.4.11	Shielding Contact spring force (outside springs on	Min. 2,5N	At deflection: till Board connector shield				
	BCv1)						
3.4.12	Shielding Contact spring force	Min. 2,5N at A=0,25mm (see figure 3)	At required deflection to reach dimension A (see figure 3).				
	(outside springs on BCv2)		Maximum deflection to be applied: A=0,15mm.				
3.4.13	Shielding Contact spring force	Min. 1,0N at B=0,25mm (see figure 4)	At required deflection to reach dimension B (see figure 5).				
	(inside-bended springs on board connector version 2)		Maximum deflection to be applied: B=0,15mm.				
3.4.14	Vibration	10-500 Hz sweeping 1 oct./min., displacement 0,75mm peak/accel. 10 g , 30 minutes in each of 3 mutual perpendicular axes. Requirement:	In acc. with IEC 60068-2-6				
		No Physical damage. No discontinuity > 1 μsec.					
3.4.15	Physical shock	Subject connector to 50 g half sine shock pulses of 11 ms duration. 6 shocks in two directions of 3 mutual perpendicular axes.	In acc. with IEC 60512-6-3				
3.4.16	Insertion-force during wrong polarization.	Apply 100 N straight force at the cable connector (placed upside down) in mating direction during 10 sec.	In acc. with IEC 60512-15-1				
		Requirement: No functional damaging and no electrical contacting on signal-traces.					
3.4.17	Mate / un-mate force without latch.	Mate and un-mate connector-pair. Speed: 2 mm/sec. rest 30 sec min.	In acc. with IEC 60512-13-2				
		Requirement: Total mating force 50 N max. Total un-mating force 10 N min to 40 N max.					
3.4.18	Mechanical operation (Inclusive latch)	Mate and un-mate specimens for 100 cycles at a maximum rate of 500 cycles per hour with cage latch operable at room conditions.	In acc. With IEC 60512-9-1				



3.4.19	<ul> <li>A.19 Side-load in 4 directions (up-down- right-left).</li> <li>Cable connector mated on board connector.</li> <li>BCv1: Apply 40 N on cable-connector in every direction (individually) (at 27mm from edge of pcb), during 10 sec. (see figure 5)</li> <li>BCv2: Apply 60 N on cable-connector in every direction (individually) (at 27mm from edge of pcb), during 10 sec. (see figure 5)</li> </ul>		In acc. with IEC 60512-17-3
		Requirement: No functional damage, latch should be in place.	
3.4.21	Locking latch strength	Apply 100 N straight force at the mated cable connector, in un-mating direction.	In acc. with IEC 60512-15-1
3.4.22	Rotational pull force (not applicable for BCv1)	Load cabled module into board-connector. Apply an axial pull-force of 60N on the cable, the pull- force will be in a 33 degrees angle with the axial direction of the connector. Rotate the load 3 full rotations (1080 degrees) with respect to the axial direction of the connector. Requirement: plug shall remain mated with no evidence of housing damage.	
3.4.23	Mounting-force board- connector. (not applicable for BCv1)	Measure force necessary to push the board- connector into the host board at a maximum rate of 12.7mm per minute. Requirement: 20N maximum.	

		SIGNALINTEGRITY	
Para	Test Description	Performance Requirements or Severity	Procedures
3.4.30	Characteristic Impedance	Time Domain (TDR) measurement Impedance profile : the test board, PCB cable connector and 1000ps cable Rise-time of incident pulse : 30ps (10%-90%) Pairs 15-16 and 18-19 Requirement : $100 \Omega$ +/- 10%; 100 $\Omega$ -20% for footprint for time period of 250ps	IEC 60512-25-7
3.4.31	Skew	Time Domain (TDT) measurement Risetime of incident pulse: 30ps (10%-90%) Pairs 15-16 and 18-19 Samples : 1m and 8m Requirement : 15ps	IEC 60512-25-4



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3.4.32	Return Loss	Frequency Domain (FDR) S-parameter measurement Pairs 15-16 and 18-19	IEC 60512-25-5
		Samples :	Reference :
		1m with AWG26	IEEE Std 802.3ap-
			2007 – Amendment to
		Requirement ·	IFFF Std 802 3-
		IFFE 900 Jon for 100 Decel/D. JdD	
		IEEE 802.3ap 101 10GBaseKR -30B.	2005:CSIVIA/CD -
			Annex 69B
3.4.33	Insertion Loss	Frequency Domain (FDT) S-parameter measurement	IEC 60512-25-5
		Pairs 15-16 and 18-19	
		Samples ·	Reference ·
		1 m Am and 9 m with AMC26	
		rm, 4m and 8m with AWG26	IEEE Sid 802.3ap-
			2007 – Amendment to
		Requirement :	IEEE Std 802.3-
		& IEEE 902 2pp for 10CPocoKP	
		SIEEE OUZ. SAP IUI IUGBASERK	2005.CSIVIA/CD -
		§ 1.1dB/GHz -0.5dB @ 1GHz to 5GHz for 1m with	Annex 69B
		AWG26	
		8 Suck out frequency (resonance)	
	a <del>-</del>		
3.4.34	Cross Lalk	Frequency Domain (FDT) S-parameter measurement	IEC 60512-25-1
		Samples :	
		1m, 4m, 8m with AWG26	
		Requirement on 1m:	
		Pairs 15-16 and 18-10	
		- NEXT < -30dB up to 1GHZ.	
		All pairs	
		- Power sum NEXT and FEXT	
		< -24dB up to 1GHz	
3 1 35	Insertion Loss to	Frequency Domain (FDT) S-parameter measurement	Calculated values
5.4.55		Deire 45 40 and 40 40	
	Cross Talk Ratio	Pairs 15-16 and 18-19	based on 3.4.11 and
	over 10Gbps link	Samples :	3.4.12
		1m, 4m and 8m with AWG26	
		Requirement :	
		Samples 1m and 4m	
		IEEE 802 3ap for 10GBasaKP	
		Samples 8m	
		- < -55dB up to 100MHz	
		- < -36dB up to 1GHz	
		- < -280B up to 2.5GHZ	
		- < -23dB up to 5.0GHz	
3.4.36	Screening	Samples :	IEC 62153-4-4 and
	Attenuation	1m with AWG26	IEC 62153-4-7
		Requirement :	
		< -40dB at 0 to 5GHz for cable assembly and PCB	
		connector	
3.4.37	Time Delay Rx to	Max 400ps asymmetry between pairs (Rx - Tx)	Measurements to be
0.1.07			performed on 1m and
	· ^		
			orn cable assemblies



	ENVIRONMENTAL						
Para	Test Description	Performance Requirements or Severity	Procedures				
3.4.40	Rapid change of Temperature	-40%90℃, 0,5 hrs / 0,5 hrs, 5 cycles	In acc. with IEC 60512-11-4				
3.4.41	Climatic sequence Dry heat 1 <sup>st</sup> Damp heat cycle Cold 2 <sup>nd</sup> Damp heat cycle	90℃, 16 hrs 25%55℃, RH 93%, 24 hrs -40℃, 2 hrs 25%55℃, RH 93%, 24 hrs	In acc. with IEC 60512-11-1				
3.4.42	Damp/heat steady state	Temperature 40℃, RH 93%, Duration: 21 days	In acc. with IEC 60512-11-3				
3.4.43	Temperature life.	Subject mated specimens to $105^{\circ}$ for 240 hours without electrical load.	In acc. with IEC 60512-11-9				
3.4.44	Corrosion mixed flowing gas	Temperature 25°C, RH 75%, Cl <sub>2</sub> 10 ppb, NO <sub>2</sub> 200 ppb, H2S 10 ppb, SO <sub>2</sub> 200 ppb. Duration: 20 days (un-mated)	In acc. with IEC 60512-11-7				
3.4.45	Resistance to soldering heat board connector	Specimens were subject to the following reflow profile. Fig 6	Tyco 109-201, method C				

























Figure 6



# 3.5 <u>Product Qualification and Regualification Test Sequence</u>

# 3.5.1 Test sequence applicable for Board Connector version 1

Test-sequence applicable for Board Connector version 1								
test or examination	paragraph	h Testgroup (a)						
		1	2	3	4	5	6	7
				Test-s	sequen	ce (b)		
Examination of product	3.4.1	1,7	1,7	1,11	1,6	1,17	1,7	1,10
Termination resistance	3.4.2	2,4	2,4,6	2,4,6,8	2,5	2,6,10,		
board connector - cable connector						14,16		
(signal and ground)								
Termination resistance	3.4.3							
board connector - cable connector								
(shield)								
Insulation resistance	3.4.4					3,7,11		
Voltage proof	3.4.5					4,8,12		
Current temperature derating curve	3.4.6	3						
Signal Contact spring force	3.4.10						2,5	
Shield contact spring force	3.4.11						3,6	
(board connector version 1)								
Shield contact spring force	3.4.12							
(outside-bended springs on board								
connector version 2)								
Shield contact spring force	3.4.13							
(inside-bended springs on board								
connector version 2)								
Vibration	3.4.14				3			
Physical shock	3.4.15				4			
Insertion force during wrong polarization	3.4.16	6						
Mate / unmate force without latch	3.4.17	5						
Mechanical operation (half of numbers)	3.4.18			3,7				
Side-load in 4 directions	3.4.19		3					
Locking latch strength	3.4.20		5					
Rapid change of temperature	3.4.40					5		
Climatic sequence	3.4.41					9		
Damp/heat steady state	3.4.42					13		
Temperature life	3.4.43					15		
Corrosion mixed flowing gas (board-	3.4.44			5				
connector)								
Resistance to soldering heat (board-	3.4.45						4	
connector)								
Characteristic impedance	3.4.30							2
Skew	3.4.31							3
Return loss	3.4.32							4
Insertion loss	3.4.33							5
Cross talk	3.4.34							6
Insertion loss to crosstalk ratio	3.4.35							7
Screening attenuation	3.4.36							8
Time delay Rx to Tx	3.4.37							9

(a) See paragraph 4.1

(b) Numbers indicate sequence in which tests are performed



Test-sequence applicable for Board Connector version 2									
test or examination	paragraph				Testgr	oup (a)			
		1	2	3	4	5	6	7	8
				T	est-seq	uence (	b)		
Examination of product	3.4.1	1, 7	1, 9	1, 11	1, 8	1, 21	1, 10	1, 9	1, 3
Termination resistance	3.4.2	2, 4	2, 4, 6,	2, 5, 8	2, 7	2, 7,			
board connector - cable connector			8			11, 16,			
(signal and ground)						19			
Termination resistance	3.4.3			3, 6, 9	3, 6	3, 12,			
board connector - cable connector						17, 20			
(shield)									
Insulation resistance	3.4.4					4, 8,			
						13			
Voltage proof	3.4.5					5, 9,			
						14			
Current temperature derating curve	3.4.6	3							
Signal Contact spring force	3.4.10						2, 7		
Shield contact spring force	3.4.11								
(board connector version 1)									
Shield contact spring force	3.4.12						3, 8		
(outside-bended springs on board									
connector version 2)									
Shield contact spring force	3.4.13						4, 9		
(inside-bended springs on board									
connector version 2)									
Vibration	3.4.14				4				
Physical shock	3.4.15				5				
Insertion force during wrong polarization	3.4.16	6							
Mate / unmate force without latch	3.4.1/	5							
Mechanical operation (half of numbers)	3.4.18			4, 10					
Side-load in 4 directions	3.4.19		3						
Locking latch strength	3.4.20		5						
Rotational pull force	3.4.21		(				-		
Mounting-force board-connector	3.4.22						5		
Rapid change of temperature	3.4.40					6			
Climatic sequence	3.4.41					10			
Damp/heat steady state	3.4.42					15			
l'emperature life	3.4.43			7		18			
Corrosion mixed flowing gas (board-	3.4.44								
connector)	2.4.45								
Resistance to soldering heat (board-	3.4.45						6		
connector)	24.20							0	
Characteristic impedance	3.4.30							2	
Skew Deturn lage	3.4.31							5	
Return loss	3.4.32							4	
Insertion loss	3.4.33							с С	
Cross talk	3.4.34							0	
Researching attenuation	3.4.35							1	2
Screening attenuation	3.4.30							0	2
Time delay KX to TX	3.4.31							Ő	

# 3.5.2 Test sequence applicable for HSIO Board Connector version 2



# 4 QUALITY ASSURANCE PROVISIONS.

# 4.1 **Qualification Testing.**

Sample Selection.

Samples shall be prepared in accordance with applicable instructions and shall be selected random from current production.

Unless details to perform test require otherwise, plugs shall be terminated on cables according to applicable instructions and requirements specified in appropriate Application Specification and Instruction Sheet.

Unless otherwise specified all test groups shall consist of a minimum of 5 connectors of applicable type. Qualification inspection shall be verified by testing samples as specified in Para 3.5.

## 4.2 <u>Requalification Testing.</u>

If changes significantly affecting form, fit or function are made to product or manufacturing process, product assurance shall coordinate re-qualification testing, consisting of all or part of original testing sequence as determined by product, quality and reliability engineering.

#### 4.3 <u>Acceptance.</u>

Acceptance is based on verification that product meets requirements of Para 3.4. Failures attributed to equipment, test set-up, test sub-components or operator deficiencies shall not disqualify the product. When product failure occurs, corrective action shall be taken and samples resubmitted for qualification. Testing to confirm corrective action is required before re-submittal.

## 4.4 **Quality Conformance Inspection.**

Applicable TYCO Electronics quality inspection plan will specify sampling acceptable quality level to be used. Dimensional and functional requirements shall be in accordance with applicable product drawing and this specification.