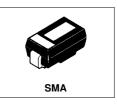
International **tor** Rectifier

SCHOTTKY RECTIFIER

MBRA140TR

1.0 Amp



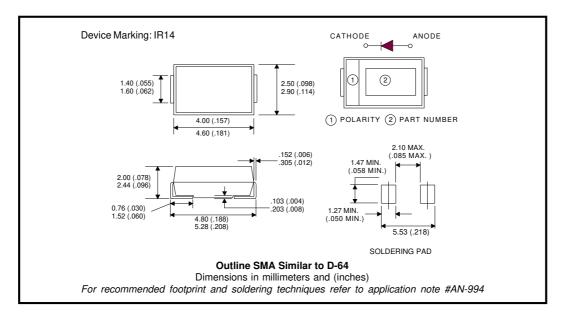
Major Ratings and Characteristics

Characteristics	MBRA140TR	Units
I _{FAV} Rect. Waveform	1.0	A
V _{RRM}	40	V
I _{FSM} @tp=5μssine	120	A
V _F @1.0Apk, T _J =125°C	0.49	V
T _J range	- 55 to 150	°C

Description/ Features

The MBRA140TR surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



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MBRA140TR

Voltage Ratings

Part number	MBRA140TR
V _R Max. DC Reverse Voltage (V)	10
V _{RWM} Max. Working Peak Reverse Voltage (V)	40

Absolute Maximum Ratings

	Parameters	Value	Units	Conditions	
I _{F(AV)}	Max. Average Forward Current * See Fig. 4	1.0	Α	50% duty cycle @ T_L = 118 °C, rectangular wave form On PC board 9mm ² island (.013mm thick copper pad are	
I _{FSM}	Max. Peak One Cycle Non-Repetitive	120	А	5μs Sine or 3μs Rect. pulse	Following any rated load condition and
	Surge Current * See Fig. 6	30		10ms Sine or 6ms Rect. pulse	with rated V _{RRM} applied
E _{AS}	Non-Repetitive Avalanche Energy	3.0	mJ	$T_{J} = 25 \text{ °C}, I_{AS} = 1A, L = 6mH$	
I _{AR}	Repetitive Avalanche Current	1.0	Α		

Electrical Specifications

	Parameters	Value	Units		Conditions
V _{EM}	Max. Forward Voltage Drop (1)	0.55	V	@ 1A	т ос ос
	* See Fig. 1	0.71	V	@ 2A	$T_{J} = 25 \text{ °C}$
		0.5	V	@ 1A	T 100 %C
		0.65	V	@ 2A	T _J = 100 °C
		0.49	V	@ 1A	T 105 %C
		0.63	V	@ 2A	T _J = 125 °C
I _{RM}	Max. Reverse Leakage Current (1)	0.5	mA	T _J = 25 °C	
	* See Fig. 2	10	mA	T _J = 100 °C	$V_{R} = rated V_{R}$
		26	mA	T _J = 125 °C	
V _{F(TO)}	Threshold Voltage	0.36	V	$T_{J} = T_{J} max.$	
r _t	Forward Slope Resistance	104	mΩ		
CT	Typical Junction Capacitance	38	pF	$V_{R} = 10V_{DC}, T_{J} = 25^{\circ}C$, test signal = 1Mhz	
L _S	Typical Series Inductance	2.0	nH	Measured lead to lead 5mm from package body	
dv/dt	Max. Voltage Rate of Change	10000	V/ µs	(Rated V _R)	

(1) Pulse Width < 300 μ s, Duty Cycle < 2%

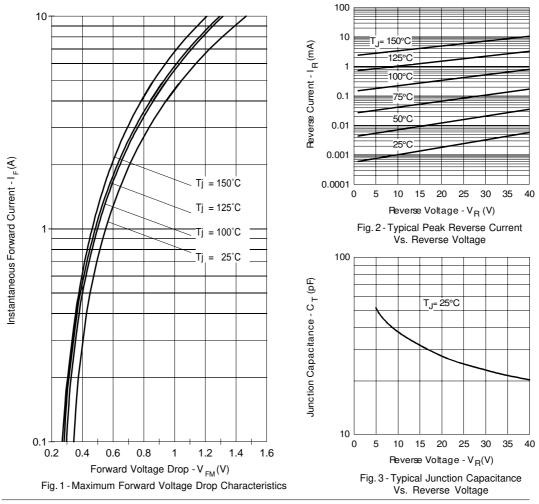
Thermal-Mechanical Specifications

	Parameters	Value	Units	Conditions
TJ	Max. Junction Temperature Range (*)	-55 to 150	°C	
T _{stg}	Max. Storage Temperature Range	-55 to 150	°C	
R _{thJL}	Max. Thermal Resistance Junction to Lead (**)	35	°C/W	DC operation (* See Fig. 4)
R _{thJA}	Max. Thermal Resistance Junction to Ambient	80	°C/W	DC operation
wt	Approximate Weight	0.07(0.002)	g (oz.)	
	Case Style	SMA		Similar D-64
	Device Marking	IR14		
(*) dPtot 1				

(*) dPtot

thermal runaway condition for a diode on its own heatsink $< \overline{\text{Rth}(j-a)}$ dTj

(**) Mounted 1 inch square PCB, Thermal Probe connected to lead 2mm from Package



MBRA140TR Bulletin PD-20582 rev. C 03/03

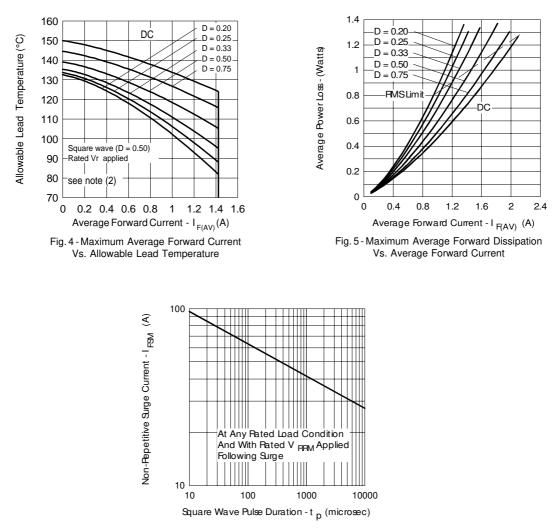


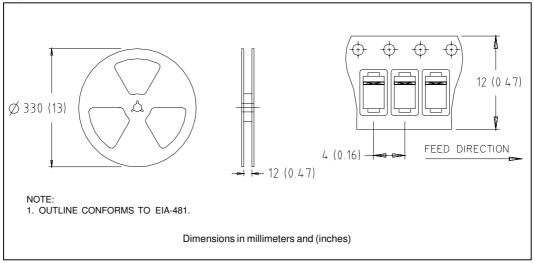
Fig. 6 - Maximum Peak Surge Forward Current Vs. Pulse Duration

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(2) Formula used: T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};

Pd = Forward Power Loss = I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D) (see Fig. 6);

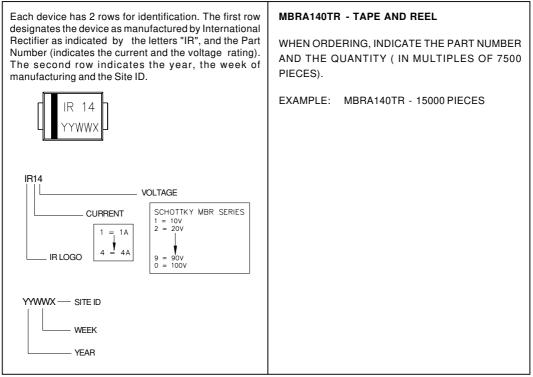
Pd_{REV} = Inverse Power Loss = V_{P1} \times I_R (1 - D); I_R @ V_{P1} = 80\% rated V_R
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Tape & Reel Information



Marking & Identification

Ordering Information



Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level. Qualification Standards can be found on IR's Web site.



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