





DMN6068SE

60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = +25°C
60V	68mΩ @ V _{GS} = 10V	5.6A
000	100mΩ @ V _{GS} = 4.5V	4.7A

Description

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Transformer Driving Switch
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

Features and Benefits

- 100% Unclamped Inductive Switch (UIS) test in production
- Low on-resistance
- Fast switching speed
- Lead-Free Finish; RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

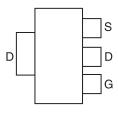
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame.
 Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.112 grams (approximate)

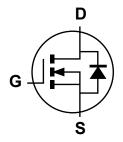
SOT223



Top View



Pin Out - Top View



Equivalent Circuit

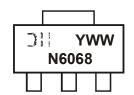
Ordering Information (Note 4 & 5)

Part Number	Qualification	Case	Packaging
DMN6068SE-13	Standard	SOT223	4000 / Tape & Reel
DMN6068SEQ-13	Automotive	SOT223	4000 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
- 5. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.

Marking Information



Office Manufacturer's Marking
N6068 = Product Type Marking Code
YWW = Date Code Marking
Y = Year (ex: 9 = 2009)
WW = Week (01 - 53)





DMN6068SE

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source voltage		V_{DSS}	60	V	
Gate-Source voltage (Note 6)		(Note 6)	V_{GS}	±20	V
Single Pulsed Avalanche En	nergy	(Note 11)	E _{AS}	37.5	mJ
Single Pulsed Avalanche Current		(Note 11)	I _{AS}	5.0	А
Continuous Drain current V _{GS} = 10V		(Note 8)		5.6	
	$T_A = +70^{\circ}C \text{ (Note 8)}$	I _D	4.5	Α	
		(Note 7)		4.1	
Pulsed Drain current	V _{GS} = 10V	(Note 9)	I _{DM}	20.8	Α
Continuous Source current (Body diode) (Note 8)		(Note 8)	I _S	4.9	А
Pulsed Source current (Body	y diode)	(Note 9)	I _{SM}	20.8	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

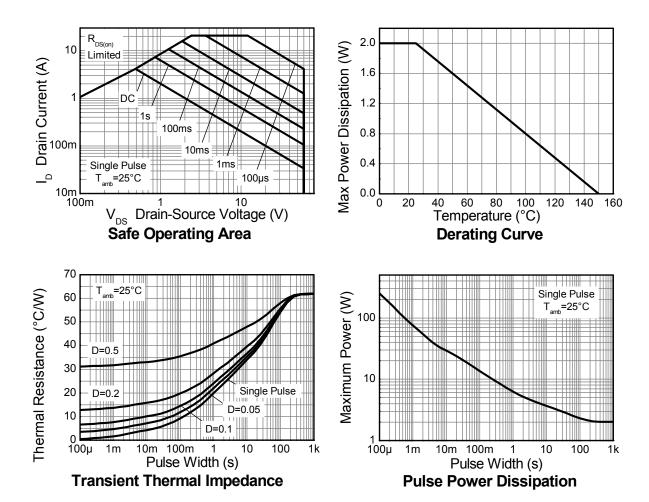
Characteristic	Symbol	Value	Unit		
Power dissipation	(Note 7)		2.0 16.0	W	
Linear derating factor	(Note 8)	P _D	3.7 29.5	mW/°C	
The arrest Decistance . It wasting to Applicat	(Note 7)	Б	62.5		
Thermal Resistance, Junction to Ambient	(Note 8)	$R_{\theta JA}$	34	°C/W	
Thermal Resistance, Junction to Lead	(Note 10)	R _{θJL}	11.5		
Operating and storage temperature range		T _J , T _{STG}	-55 to +150	°C	

Notes:

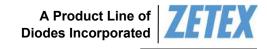
- 6. AEC-Q101 V_{GS} maximum is ±16V.
 7. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 8. Same as note (3), except the device is measured at $t \le 10$ sec.
- 9. Same as note (3), except the device is pulsed with D= 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature.
- 10. Thermal resistance from junction to solder-point (at the end of the drain lead).
 11. UIS in production with L = 3.0mH, I_{AS} = 5.0A, R_{G} = 25 Ω , V_{DD} =50V, starting T_{J} = +25°C.



Thermal Characteristics







DMN6068SE

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

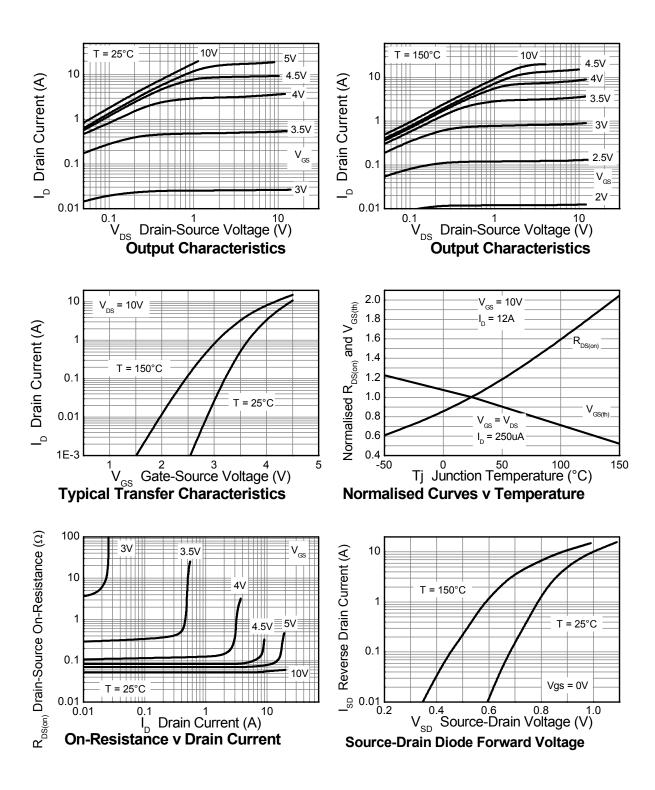
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	I _D = 250μA, V _{GS} = 0V	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μΑ	V _{DS} = 60V, V _{GS} =	= 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS}$	= 0V
ON CHARACTERISTICS				•	•		
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	I _D = 250μA, V _{DS} = V _{GS}	
Static Dunin Course On Desigtance (Note 12)	_			0.068	0	V _{GS} = 10V, I _D = 12A	
Static Drain-Source On-Resistance (Note 12)	R _{DS (ON)}	_	_	0.100	Ω	$V_{GS} = 4.5V, I_{D} =$	6A
Forward Transconductance (Notes 12 & 13)	9fs		19.7	_	S	$V_{DS} = 15V, I_D = 1$	2A
Diode Forward Voltage (Note 12)	V_{SD}	_	0.98	1.15	V	I _S = 12A, V _{GS} = 0V	
Reverse recovery time (Note 13)	t _{rr}		145	_	ns	I _S = 12A, di/dt= 100A/μs	
Reverse recovery charge (Note 13)	Q _{rr}	_	929	_	nC		
DYNAMIC CHARACTERISTICS (Note 13)							
Input Capacitance	C _{iss}		502	_	pF	V _{DS} = 30V, V _{GS} = 0V f= 1MHz	
Output Capacitance	Coss	_	45.7	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	27.1	_	pF		
Total Gate Charge (Note 14)	Qg	_	5.55	_	nC	V _{GS} = 4.5V	
Total Gate Charge (Note 14)	Qg	_	10.3	_	nC	V _{DS} = 30V I _D = 12A	
Gate-Source Charge (Note 14)	Qgs	_	1.6	_	nC		
Gate-Drain Charge(Note 14)	Q _{gd}	_	3.5	_	nC		
Turn-On Delay Time (Note 14)	t _{D(on)}	_	3.6	_	ns	V_{DD} = 30V, V_{GS} = 10V I_D = 12A, $R_G \cong 6.0\Omega$	
Turn-On Rise Time (Note 14)	t _r		10.8	_	ns		
Turn-Off Delay Time (Note 14)	t _{D(off)}	_	11.9	_	ns		
Turn-Off Fall Time (Note 14)	t _f		8.7		ns		

Notes:

^{12.} Measured under pulsed conditions. Pulse width \leq 300µs; duty cycle \leq 2% 13. For design aid only, not subject to production testing. 14. Switching characteristics are independent of operating junction temperatures.

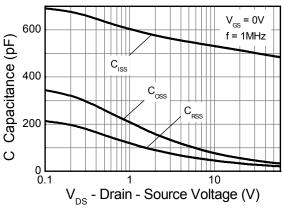


Typical Characteristics





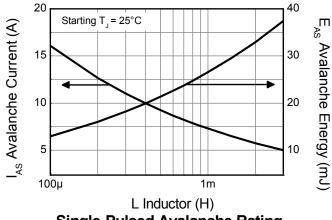
Typical Characteristics (cont.)



10 V_{GS} Gate-Source Voltage (V) $V_{DS} = 30V$ I_D = 12A 2 6 8 10 Q - Charge (nC)

Capacitance v Drain-Source Voltage

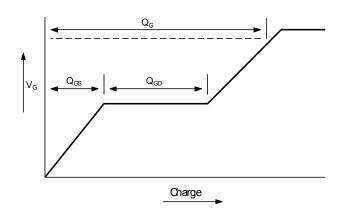
Gate-Source Voltage v Gate Charge



Single-Pulsed Avalanche Rating



Test Circuits



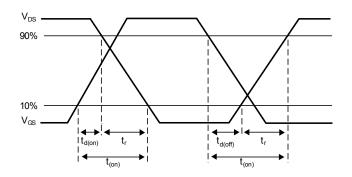
Ourrent regulator

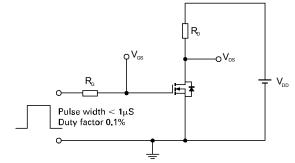
12V 0.2 µ.F 50k 9ame as DUT

Vos

Basic gate charge waveform

Gate charge test circuit





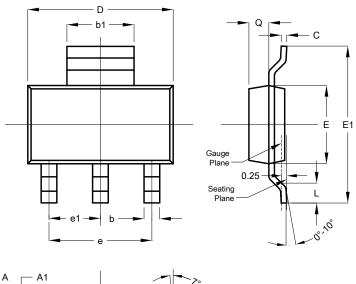
Switching time waveforms

Switching time test circuit

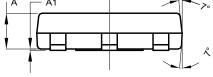


Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

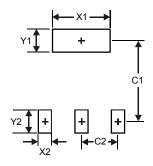


SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
X1	3.3
X2	1.2
Y1	1.6
Y2	1.6
C1	6.4
C2	2.3





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