

Parameter	Rating	Units
Blocking Voltage	350	V <sub>P</sub>
Load Current	100	mA <sub>rms</sub> / mA <sub>DC</sub>
On-Resistance (max)	35	Ω

#### **Features**

- ullet 3750V $_{
  m rms}$  Input/Output Isolation
- Bidirectional Current Sensing
- · Bidirectional Current Switching
- · Replaces up to Three or Four Components
- Three Functions in One Package
- Small 16-Pin SOIC Package
- FCC Compatible
- No EMI/RFI Generation
- Tape & Reel Versions Available

## **Applications**

- Telecommunications
  - Telecom Switching
  - · Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- · Electronic Switching
- I/O Subsystems
- · Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- · Industrial Controls

#### **Description**

The IAD110P Multifunction Telecom switch combines a 350V normally open (1-Form-A) relay and two optocouplers in a single package. The relay uses optically coupled MOSFET technology to provide 3750V<sub>rms</sub> of input to output isolation. The efficient MOSFET switch and photovoltaic die use IXYS Integrated Circuits Division's patented OptoMOS® architecture. The optically coupled output is controlled by highly efficient infrared LEDs. The IAD110P enables telecom circuit designers to combine three discrete functions in a single component that uses less space than traditional discrete component solutions.

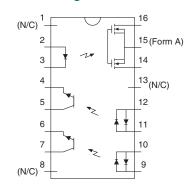
# **Approvals**

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1305490
- EN62368-1 Certified Component: TUV Certificate: B 082667 0008 Rev 00

#### **Ordering Information**

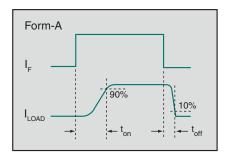
Part #	Description		
IAD110P	16-Pin SOIC (50/Tube)		
IAD110PTR	16-Pin SOIC (1000/Reel)		

## **Pin Configuration**



- 1. (N/C)
- 2. + LED Relay Input 3. - LED - Relay Input
- 4. Emitter Phototransistor #1
- Emitter Phototransistor #1
   Collector Phototransistor #1
- 6 Fmitter Phototransistor #2
- 7. Collector Phototransistor #2
- 8. (N/C)
- 9. LED Phototransistor +/- #2
- 10. LED Phototransistor -/+ #2
- 11. LED Phototransistor +/- #1
- 12. LED Phototransistor -/+ #1
- 13. (N/C) 14. Output - Relay
- 15. Common Relay
- 16. Output Relay

#### Switching Characteristics of Normally Open Devices











# Absolute Maximum Ratings @ 25°C

Parameter	Symbol	Ratings	Units
Input Control Current, Relay	I <sub>F</sub>	50	mA
Total Package Dissipation 1	$P_{T}$	1	W
Isolation Voltage, Input to Output	V <sub>ISO</sub>	3750	$V_{rms}$
Operational Temperature	T <sub>A</sub>	-40 to +85	°C
Storage Temperature	T <sub>STG</sub>	-40 to +125	°C

<sup>&</sup>lt;sup>1</sup> Derate linearly 1.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

# Electrical Characteristics @25°C: Relay Section

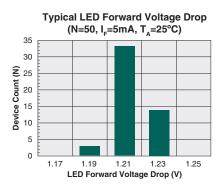
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics						
Blocking Voltage (Peak)	I <sub>L</sub> =1μA	$V_L$	-	-	350	V <sub>P</sub>
Load Current						
Continuous	-	IL	-	-	100	$mA_{rms} / mA_{DC}$
Peak	t=10ms	I <sub>LPK</sub>	-	-	350	$mA_P$
On-Resistance	I <sub>L</sub> =100mA	R <sub>ON</sub>	-	-	35	Ω
Off-State Leakage Current	V <sub>L</sub> =350V, T <sub>J</sub> =25°C	I <sub>LEAK</sub>	-	-	1	μΑ
Switching Speeds						
Turn-On	I -5m/ \/ -10\/	t <sub>on</sub>	-	-	3	ms
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>off</sub>	-	-	3	ms
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	25	-	pF
Input Characteristics			•		•	
Input Control Current to Activate	I <sub>L</sub> =100mA	I <sub>F</sub>	-	-	5	mA
Input Control Current to Deactivate	I <sub>L</sub> =1mA	I <sub>F</sub>	0.4	-	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	$V_{F}$	0.9	1.2	1.4	V
Reverse Input Voltage	-	$V_R$	-	-	5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μΑ

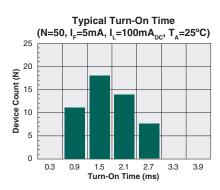
## **Electrical Characteristics @25°C: Detector Section**

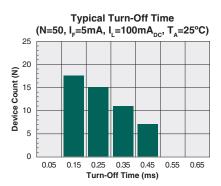
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics						
Phototransistor Blocking Voltage	I <sub>C</sub> =10μA	BV <sub>CEO</sub>	20	50	-	V
Phototransistor Dark Current	V <sub>CE</sub> =5V, I <sub>F</sub> =0mA	I <sub>CEO</sub>	-	50	500	nA
Saturation Voltage	I <sub>C</sub> =2mA, I <sub>F</sub> =16mA	V <sub>SAT</sub>	-	0.3	0.5	V
Current Transfer Ratio	I <sub>F</sub> =6mA, V <sub>CE</sub> =0.5V	CTR	33	-	-	%
Input Characteristics						
Input Control Current	$I_C=2mA, V_{CE}=0.5V$	I <sub>F</sub>	-	2	6	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Input Current (Detector Must be Off)	$I_C=1\mu A, V_{CE}=5V$	-	5	25	-	μΑ
Capacitance, Input to Output	V <sub>L</sub> =50V, f=1MHz	C <sub>I/O</sub>	-	3	-	pF
Isolation, Input to Output	-	V <sub>I/O</sub>	3750	-	-	V <sub>rms</sub>

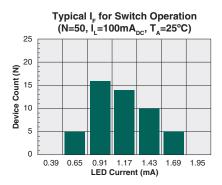


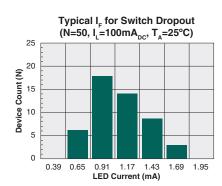
#### **RELAY PERFORMANCE DATA\***

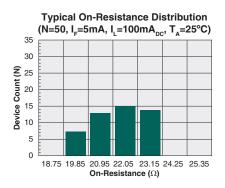


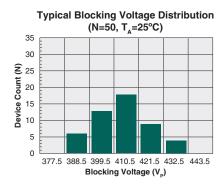


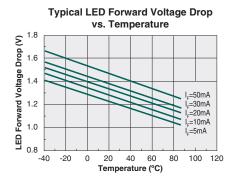


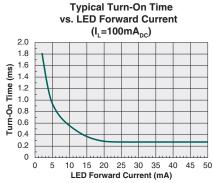


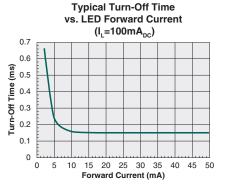








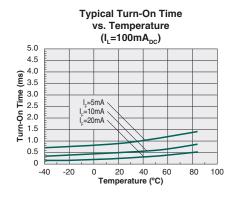


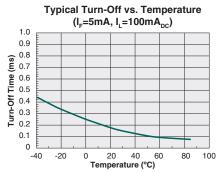


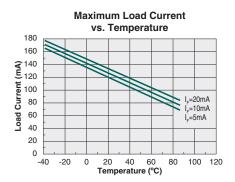
<sup>\*</sup> Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

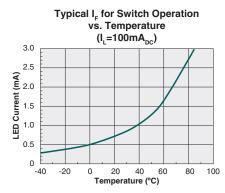


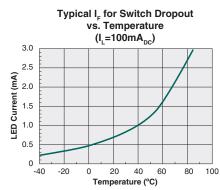
## **RELAY PERFORMANCE DATA (cont.)\***

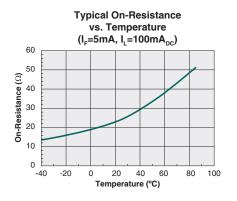


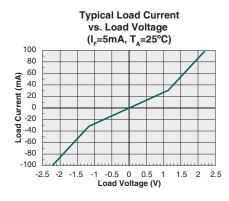


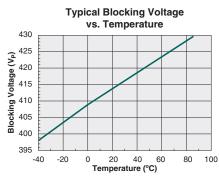


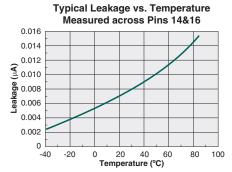


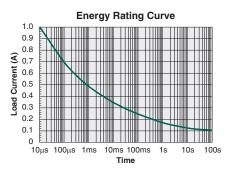








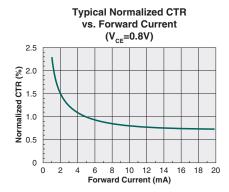


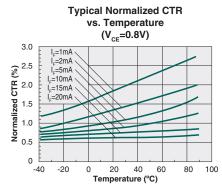


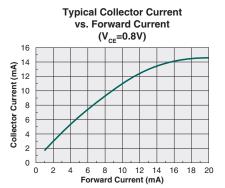
<sup>\*</sup> Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.



# **DETECTOR PERFORMANCE DATA\***







<sup>\*</sup> Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.



## **Manufacturing Information**

#### **Moisture Sensitivity**



All plastic encapsulated semiconductor packages are susceptible to moisture ingression, IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

Device	Moisture Sensitivity Level (MSL) Classification
IAD110P	MSL 3

#### **ESD Sensitivity**



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## **Soldering Profile**

Provided in the table below is the IPC/JEDEC J-STD-020 Classification Temperature ( $T_{\rm C}$ ) and the maximum dwell time the body temperature of these surface mount devices may be  $(T_C - 5)^{\circ}\dot{C}$  or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T <sub>c</sub> )	Dwell Time (t <sub>P</sub> )	Max Reflow Cycles
IAD110P	245°C	30 seconds	3

#### **Board Wash**

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.







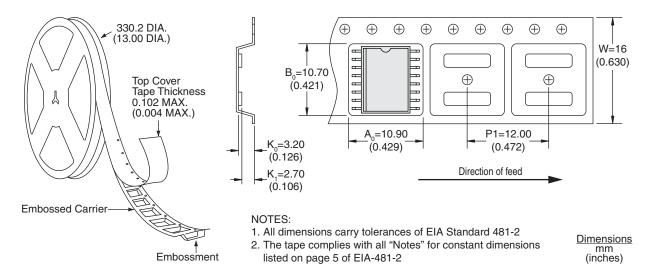


#### **MECHANICAL DIMENSIONS**

#### IAD110P 10.160±0.381 **PCB Land Pattern** (0.400±0.015) 0.254 ±0.0127 **PIN 16** (0.010±0.0005) 10.363±0.127 (0.408±0.005) 0.635 X 45° 9.30 7.493±0.127 (0.366)(0.025 X 45°) 1.016 TYP 1.90 (0.295±0.005) (0.040 TYP) (0.075)PIN 1 1.270 TYP (0.050)(0.050 TYP) 2.057±0.051 0.60 (0.081±0.002) (0.024)**DIMENSIONS** mm (inches) 0.406 TYP (0.016 TYP) Lead to Package Standoff: 8.890 TYP NOTES: MIN: 0.0254 (0.001) (0.350 TYP) 1. Coplanarity = 0.1016 (0.004) max. MAX: 0.102 (0.004) 0.508±0.1016 2. Leadframe thickness does not include solder plating

### IAD110PTR Tape & Reel

(0.020±0.004)



(1000 microinch maximum).

For additional information please visit our website at: https://www.ixysic.com



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