

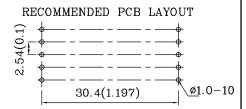
25.4mm (1.0 ") SINGLE DIGIT NUMERIC DISPLAY

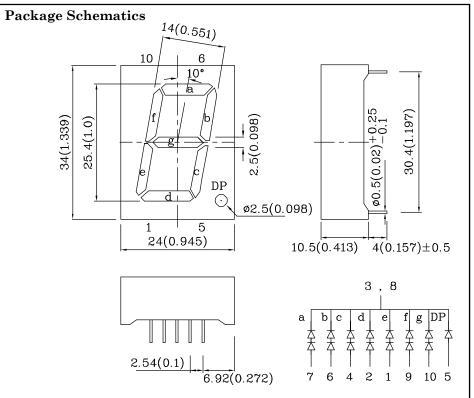
#### **Features**

- Low power consumption
- ullet Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color contrast
- RoHS Compliant









Notes:

- 1. All dimensions are in millimeters (inches), Tolerance is  $\pm$  0.25(0.01")unless otherwise noted.
- 2. Specifications are subject to change without notice.

Absolute Maximum Ratin (T <sub>A</sub> =25°C)	gs	MDK (AlGaInP)		
Reverse Voltage (Per Chip)	$V_{\mathrm{R}}$	5	V	
Forward Current (Dp)		30 (30)	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width (Dp)	ifs	185 (185)	mA	
Power Dissipation (Per Chip)	$P_{D}$	75	mW	
Operating Temperature	$T_{\rm A}$	-40 ~ +85	°C	
Storage Temperature	Tstg	-40 ~ +85		
Lead Solder Temperature [2mm Below Package Base]	260°C For 3~5 Seconds			

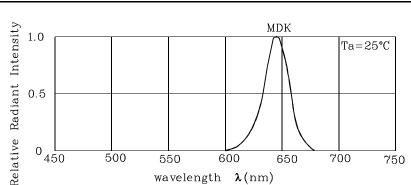
Operating Characteristics (T <sub>A</sub> =25°C)		MDK (AlGaInP)	Unit
Forward Voltage (Typ.) (Dp) (I <sub>F</sub> =10mA)	$V_{\mathrm{F}}$	3.7 (1.85)	V
Forward Voltage (Max.) (Dp) (I <sub>F</sub> =10mA)	$V_{\mathrm{F}}$	5 (2.5)	V
Reverse Current (Max.) (Per Chip) $(V_R=5V)$	$I_{\mathrm{R}}$	10 (10)	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I <sub>F</sub> =10mA)	λΡ	645*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) $(I_F=10\text{mA})$	λD	630*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I <sub>F</sub> =10mA)	$\triangle \lambda$	28	nm
Capacitance (Typ.) (V <sub>F</sub> =0V, f=1MHz)	С	35	pF

Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* (IF=10mA) ucd		Wavelength CIE127-2007* Nm λP	Description
			min.	typ.		
XDMDK25C	Red	AlGaInP	88000 31000*	259990 69990*	645*	Common Cathode, Rt. Hand Decimal.

<sup>\*</sup>Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Jan 18,2014

XDSA7125 V2-X Layout: Maggie L.

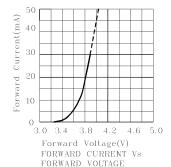


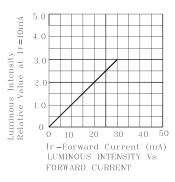


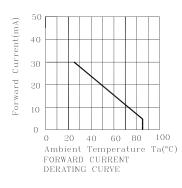
RELATIVE INTENSITY Vs. CIE WAVELENGTH

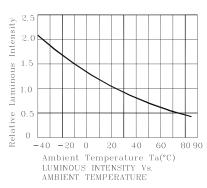
wavelength  $\lambda$ (nm)

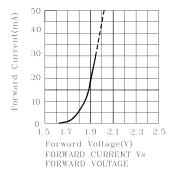
# **❖** MDK

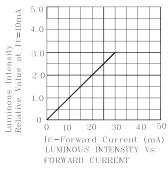


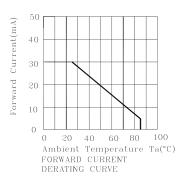


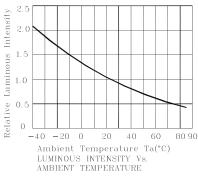




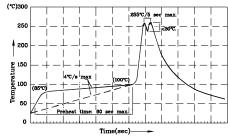








### Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



- Notes:

  1. Recommend pre-heat temperature of 105°C or less (as measured thermocouple attached to the LED pins) prior to immersion in the wave with a maximum solder bath temperature of 260°C

  2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sc
- 2.Peak wave soldering temperature outman, 3.Do not apply stress to the epoxy resin 4.Fixtures should not incur stress on the during soldering process.
  5.SAC 305 solder alloy is recommended.
  6.No more than one wave soldering pass.

## Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength),

the typical accuracy of the sorting process is as follows:

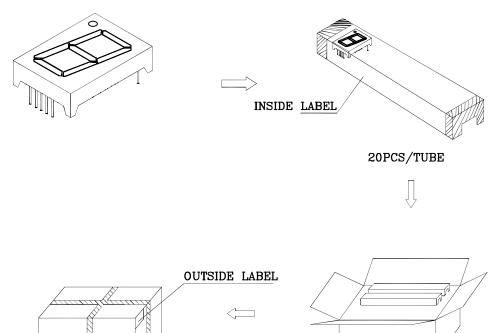
- 1. Wavelength: +/-1nm
- 2. Luminous Intensity / Luminous Flux: +/-15%
- 3. Forward Voltage:  $\pm$ -0.1V

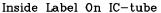
Note: Accuracy may depend on the sorting parameters.

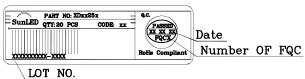
40TUBE/10#BOX



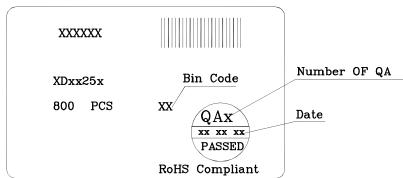
# PACKING & LABEL SPECIFICATIONS







## Outside Label On Box



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800PCS/BOX

 $6. \ Additional\ technical\ notes\ are\ available\ at\ \underline{http://www.SunLEDusa.com/TechnicalNotes.asp}$ 

Jan 18,2014