

RD3R02BBH

Nch 150V 20A Power MOSFET

V _{DSS}	150V
R _{DS(on)} (Max.)	81mΩ
I _D	±20A
P _D	50W

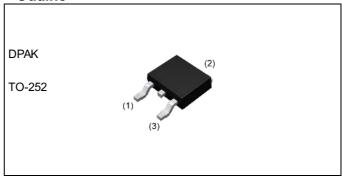
Features

- 1) Low on resistance
- 2) High Power Package(TO-252)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

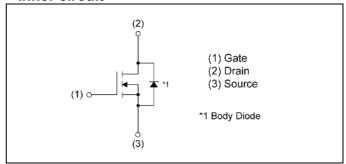


Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL1
	Marking	RD3R02BBH

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	150	V
Continuous drain current	Continuous drain current V _{GS} = 10V		±20	Α
Pulsed drain current	I _{DP} *2	±80	Α	
Gate - Source voltage		V _{GSS}	±20	V
Avalanche current, single pulse		I _{AS} *3	13	Α
Avalanche energy, single pulse		E _{AS} *3	6.6	mJ
Power dissipation		P _D *1	50	W
Junction temperature		T _j	150	°C
Operating junction and storage ter	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Uffil
Thermal resistance, junction - case	R _{thJC} *1	-	-	2.50	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	150	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	98	-	mV/°C	
Zero gate voltage drain current	I_{DSS} $V_{DS} = 150V, V_{GS} = 0V$		1	1	5	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		ı	±500	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 1mA$	2.0	-	4.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-5.7	-	mV/°C	
Static drain - source	R _{DS(on)} *4	V _{GS} = 10V, I _D = 10A	-	62	81	mΩ	
on - state resistance		V _{GS} = 6V, I _D = 10A	1	67	100	11122	
Gate resistance	R _G -		ı	1.5	ı	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 5V, I _D = 10A	7.1	-	-	S	

^{*1} T_c=25°C, Limited only by maximum temperature allowed.

^{*2} Pw≤ 10µs , Duty cycle≤ 1%

^{*3} L \simeq 0.1mH, V_{DD} = 75V, R_G = 25 Ω , Starting T_i = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

Davanastan	Cumbal	Conditions		Lleit			
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	730	-		
Output capacitance	C _{oss}	V _{DS} = 75V	-	65	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz		12	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 75V,V _{GS} = 10V	-	14	-		
Rise time	t _r *4	I _D = 10A	-	10	-		
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 7.5\Omega$	-	28	-	ns	
Fall time	t _f *4	R _G = 10Ω	-	13	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Ol Conditions		Values			l limit	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Tatal mate alcount	O *4		V _{GS} = 10V	-	12.4	-		
Total gate charge	Qg*4		V _{DD} ≈ 75V		-	8.1	-	" C
Gate - Source charge	Q _{gs} *4	I _D = 20A	V _{GS} = 6V	-	2.7	-	nC	
Gate - Drain charge	Q _{gd} *4			-	3.3	-		

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions		Unit				
Parameter	Symbol	bol Conditions		Symbol Conditions		Тур.	Max.	Offic
Continuous forward current	I _S	T _a = 25°C	1	-	20	Α		
Pulse forward current	I _{SP} *2	1 _a - 25 C	-	-	80	Α		
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 20A	-	-	1.2	V		
Reverse recovery time	t _{rr} *4	I _S = 20A, V _{GS} =0V	-	110	-	ns		
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/μs	-	345	-	nC		

Fig.1 Power Dissipation Derating Curve

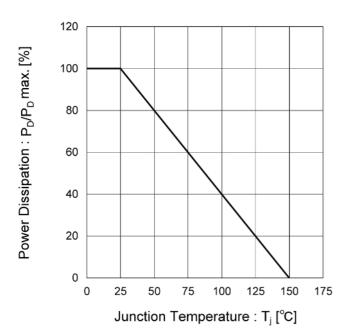
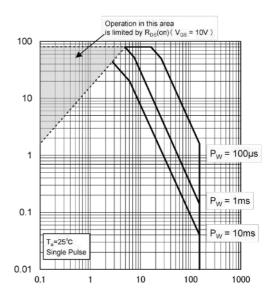


Fig.2 Maximum Safe Operating Area



Drain Current : Ip [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

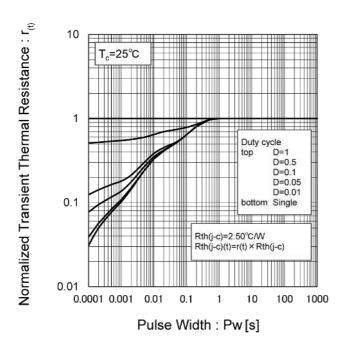


Fig.4 Single Pulse Maximum Power Dissipation

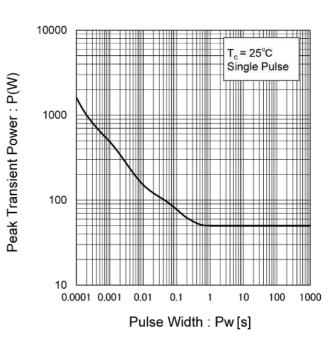


Fig.5 Typical Output Characteristics(I)

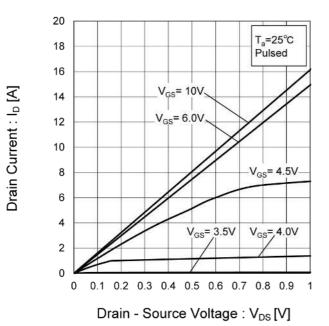
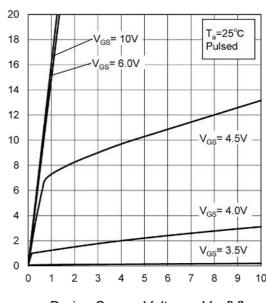


Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

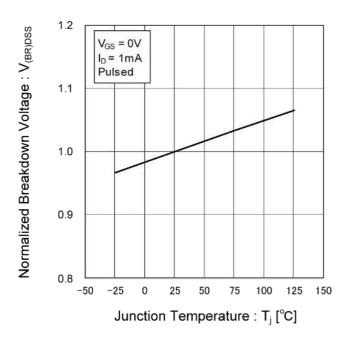
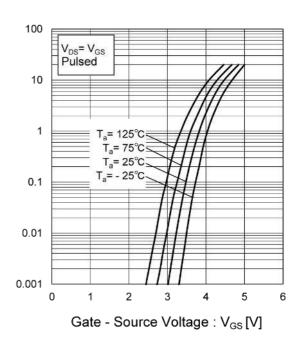


Fig.8 Typical Transfer Characteristics



Drain Current: Ip [A]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

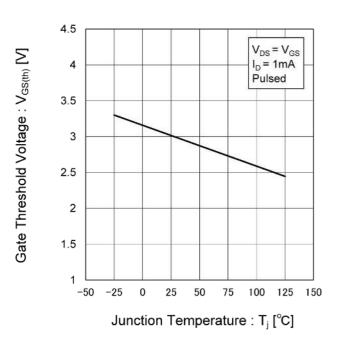


Fig.10 Forward Transfer Admittance vs.
Drain Current

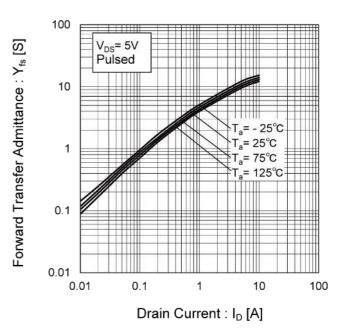


Fig.11 Drain Current Derating Curve

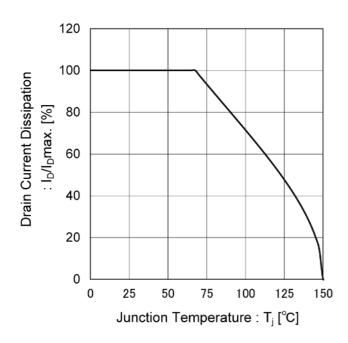


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

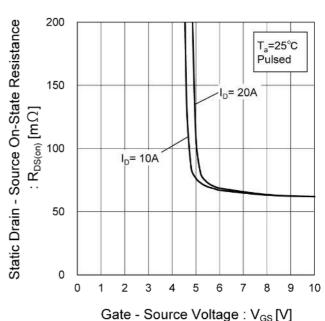


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

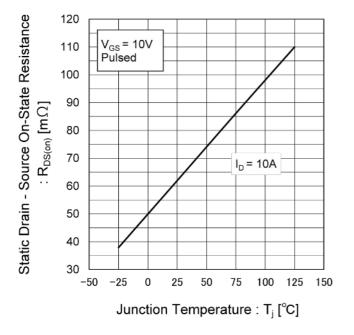


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

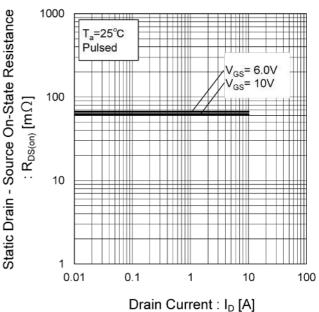


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

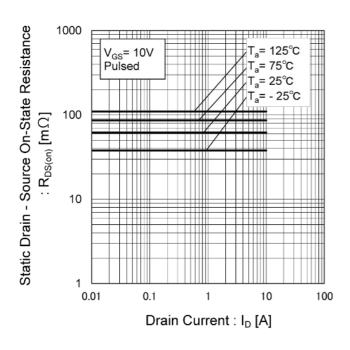


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

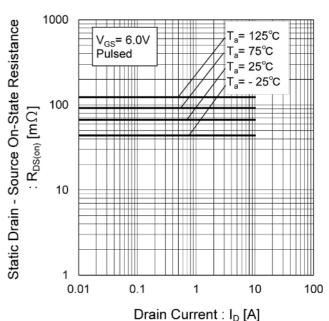


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

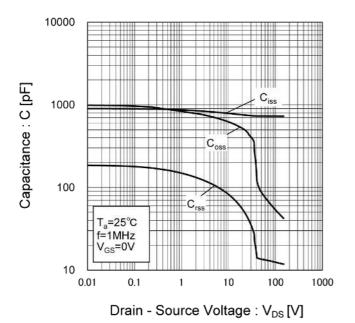


Fig.18 Switching Characteristics

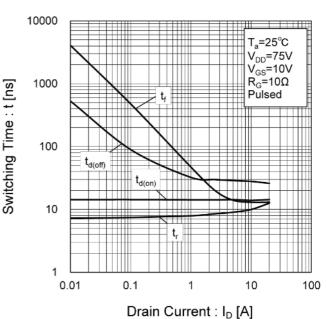


Fig.19 Typical Gate Charge

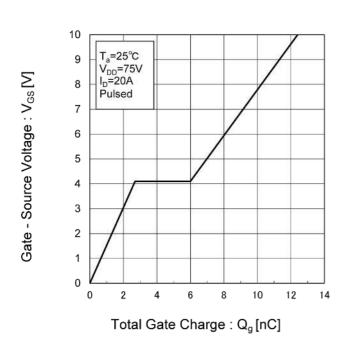
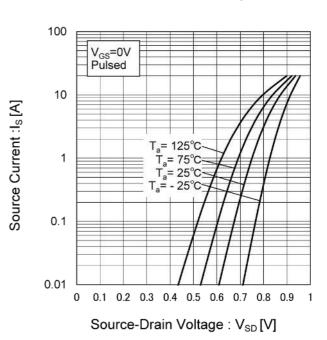


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

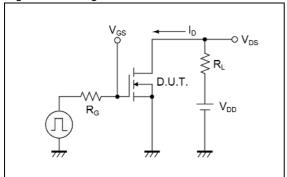


Fig.1-2 Switching Waveforms

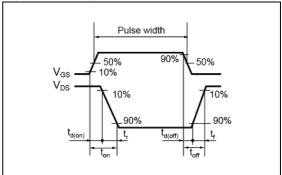


Fig.2-1 Gate Charge Measurement Circuit

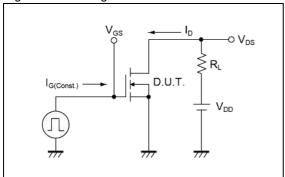


Fig.2-2 Gate Charge Waveform

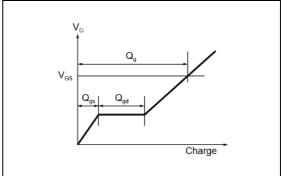


Fig.3-1 Avalanche Measurement Circuit

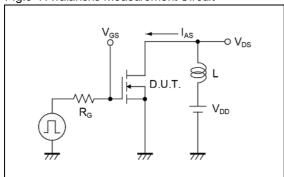
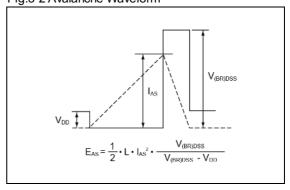
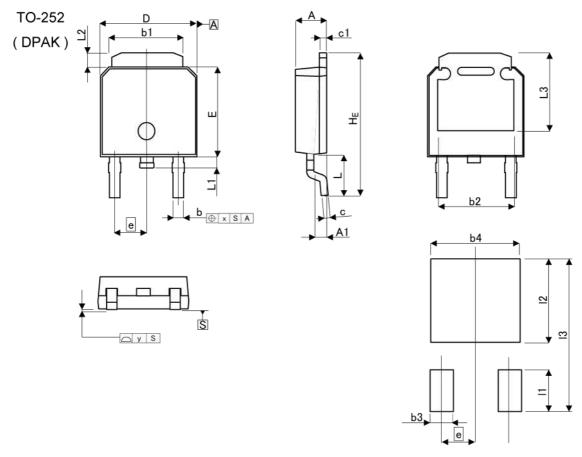


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCI	HES
DIM	MIN	MAX	MIN	MAX
Α	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	89
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.0	91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.114	
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
х		0.25	() () () ()	0.010
у	=	0.10	15//	0.004
5.0.4. T	MILIME	TERS	INCI	HES
DIM	MIN	MAX	MIN	MAX
b3	#	1.15	7#8	0.045
b4	-	5.55	A 5 0.	0.219
11	-	2.77	() () ()	0.109
12	-	5.50	150	0.217
13	2	10.40	(#5	0.409

Dimension in mm/inches



Notice

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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