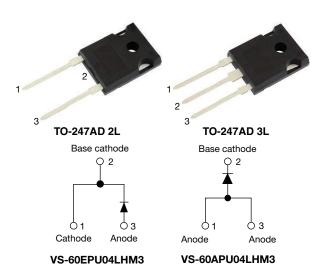


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Vishay Semiconductors

# Ultrafast Soft Recovery Diode, 60 A FRED Pt®



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	60 A					
$V_R$	400 V					
V <sub>F</sub> at I <sub>F</sub>	0.87 V					
t <sub>rr</sub> typ.	50 ns					
T <sub>J</sub> max.	175 °C					
Package	TO-247AD 2L, TO-247AD 3L					
Circuit configuration	Single					

#### **FEATURES**

- Ultrafast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test

compliant HALOGEN FREE

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **BENEFITS**

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

### **DESCRIPTION / APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

### **MECHANICAL DATA**

Case: TO-247AD 2L, TO-247AD 3L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Cathode to anode voltage	$V_{R}$		400	V		
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 127 °C	60			
Single pulse forward current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	600	Α		
Maximum repetitive forward current	I <sub>FRM</sub>	Square wave, 20 kHz	120			
Operating junction and storage temperatures	T <sub>.I</sub> , T <sub>Sta</sub>		-55 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	400	-	-		
		I <sub>F</sub> = 60 A	-	1.05	1.25	V	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 60 A, T <sub>J</sub> = 175 °C	-	0.87	1.03		
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 125 °C	-	0.93	1.10		
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	-	50	μΑ	
neverse leakage current		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	mA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 400 V	-	50	-	pF	
Series inductance	eries inductance L <sub>S</sub>		-	3.5	-	nH	

# VS-60EPU04LHN3, VS-60APU04LHN3

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	50	-		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 60 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	85	-	ns	
		T <sub>J</sub> = 125 °C		-	145	-		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		=	8.8	-	Α	
		T <sub>J</sub> = 125 °C		=	15.4	-		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	375	-	nC	
		T <sub>J</sub> = 125 °C		=	1120	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.70	K/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-	r\/vv	
Weight			-	5.5	-	g	
weigin			-	0.2	-	oz.	
			1.2		2.4	N⋅m	
Mounting torque			(10)	-	(20)	(lbf · in)	
Marking device		Case style TO-247AD 2L	60EPU04LH 60APU04LH				
ivial killig device		Case style TO-247AD 3L					

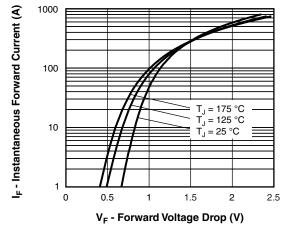


Fig. 1 - Typical Forward Voltage Drop Characteristics

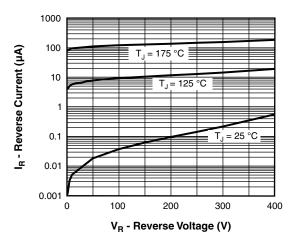


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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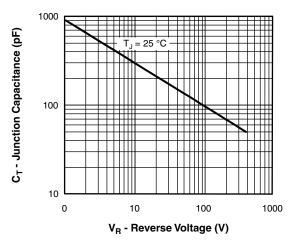


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

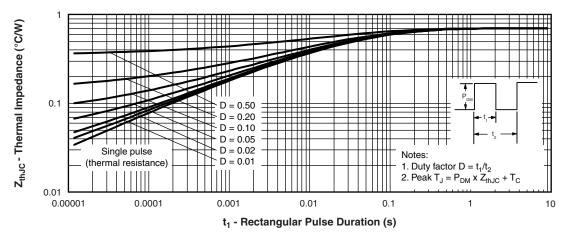


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

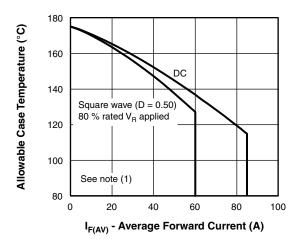


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

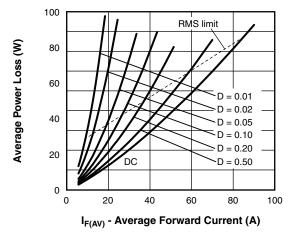


Fig. 6 - Forward Power Loss Characteristics

### Note

Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

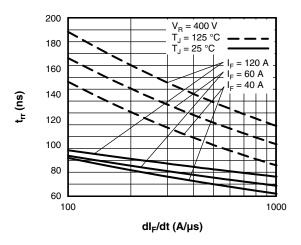


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

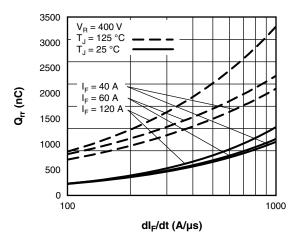
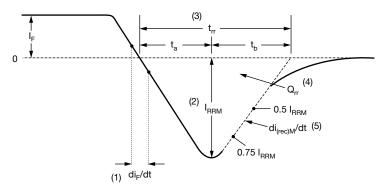


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

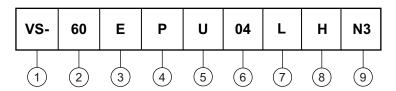
Fig. 9 - Reverse Recovery Waveform and Definitions

## VS-60EPU04LHN3, VS-60APU04LHN3

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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

Circuit configuration:

• E = single diode

• A = single diode, 3 pins

4 - Package:

P = TO-247AC (modified)

- Type of silicon:

U = ultrafast recovery

6 - Voltage rating (04 = 400 V)

7 - L = long lead (TO-247AD)

8 - H = AEC-Q101 qualified

9 - Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-60EPU04LHN3	25	500	Antistatic plastic tube			
VS-60APU04LHN3	25	500	Antistatic plastic tube			

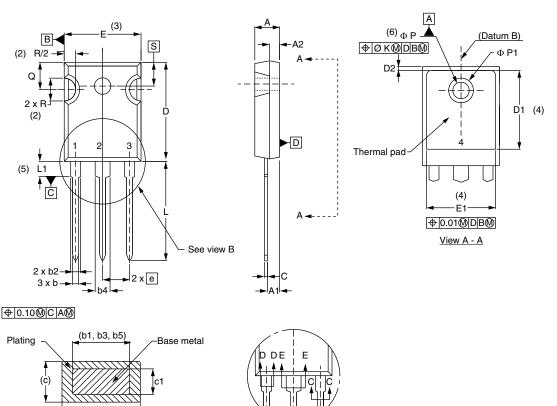
LINKS TO RELATED DOCUMENTS					
Dimensions -	TO-247AD 2L	www.vishay.com/doc?95536			
- Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648			
Part marking information -	TO-247AD 3L	www.vishay.com/doc?95007			
SPICE model		www.vishay.com/doc?96899			



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### **TO-247AD 3L**

### **DIMENSIONS** in millimeters and inches



View B

0)/14001	MILLIN	IETERS	INCHES		NOTES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.34	0.065	0.092		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.89	0.015	0.035		
c1	0.38	0.84	0.015	0.033		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

Section C - C, D - D, E - E

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	0.215 BSC	
ØΚ	0.254		0.0	0.010	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	
		•	•		

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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