MSD42SWT1G

NPN Silicon General Purpose High Voltage Transistor

This NPN Silicon Planar Transistor is designed for general purpose amplifier applications. This device is housed in the SC-70/SOT-323 package which is designed for low power surface mount applications.

Features

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	$V_{(BR)CBO}$	300	Vdc
Collector-Emitter Voltage	$V_{(BR)CEO}$	300	Vdc
Emitter-Base Voltage	$V_{(BR)EBO}$	6.0	Vdc
Collector Current - Continuous	I _C	150	mAdc

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation (Note 1)	P_{D}	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{stg}	- 55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS

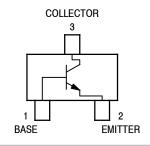
Characteristic	Symbol	Min	Max	Unit
Collector-Emitter Breakdown Voltage $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	300	1	Vdc
Collector-Base Breakdown Voltage ($I_C = 100 \mu Adc, I_E = 0$)	V _{(BR)CBO}	300	1	Vdc
Emitter-Base Breakdown Voltage ($I_E = 100 \mu Adc, I_E = 0$)	V _{(BR)EBO}	6.0	1	Vdc
Collector-Base Cutoff Current (V _{CB} = 300 Vdc, I _E = 0)	I _{CBO}	1	0.1	μΑ
Emitter-Base Cutoff Current (V _{EB} = 6.0 Vdc, I _B = 0)	I _{EBO}	1	0.1	μΑ
DC Current Gain (Note 2) $ (V_{CE} = 10 \text{ Vdc, } I_{C} = 1.0 \text{ mAdc}) $ $ (V_{CE} = 10 \text{ Vdc, } I_{C} = 30 \text{ mAdc}) $	h _{FE1} h _{FE2}	25 40	200	1
Collector-Emitter Saturation Voltage (Note 2) (I _C = 200 mAdc, I _B = 2.0 mAdc)	V _{CE(sat)}	-	0.5	Vdc

Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



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SC-70 (SOT-323) CASE 419 STYLE 3

MARKING DIAGRAM



D4 = Device Code M = Date Code* • Pb-Free Package

(Note: Microdot may be in either location)
*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MSD42SWT1G	SC-70/SOT-323 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{2.} Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.





SC-70 (SOT-323) **CASE 419** ISSUE R

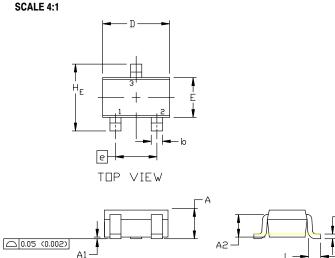
END VIEW

DATE 11 OCT 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2	0.70 REF				0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016	
С	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.00	2.20	0.071	0.080	0.087	
E	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC				0.026 BS	C	
L	0.20	0.38	0.56	0.008	0.015	0.022	
HE	2.00	2.10	2.40	0.079	0.083	0.095	



GENERIC MARKING DIAGRAM

SIDE VIEW

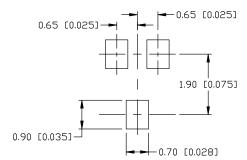


= Specific Device Code XX

Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the ID Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	CATHODE
COLLECTOR	COLLECTOR	3. DRAIN	CATHODE-ANODE	3. ANODE-CATHODE	CATHODE

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