



20V P-Channel Enhancement Mode MOSFET

Voltage -20 V Current -500mA

Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Load switch, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

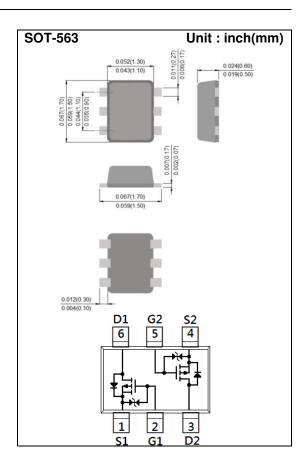
Mechanical Data

• Case: SOT-563 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0026 grams

Marking: X07



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 10	V
Continuous Drain Current		ΙD	-500	mA
Pulsed Drain Current		I _{DM}	-1000	mA
Power Dissipation	T _a =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient ^(Note 3)		RθJA	417	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-0.3	-0.59	-1.0	V
Drain-Source On-State Resistance	RDS(on)	V _{GS} =-4.5V, I _D =-500mA	-	0.9	1.2	Ω
		V _{GS} =-2.5V, I _D =-200mA	-	1.07	1.5	
		V _{GS} =-1.8V, I _D =-100mA	-	1.25	2.2	
		V _{GS} =-1.5V, I _D =-40mA	-	1.42	3.6	
		V _{GS} =-1.2V, I _D =-10mA	-	1.7	6.0	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-16V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	$V_{GS}=\underline{+}8V$, $V_{DS}=0V$	-	<u>+</u> 2	<u>+</u> 10	uA
Dynamic ^(Note 5)						
Total Gate Charge	Q_g	V _{DS} =-10V, I _D =-500mA, V _{GS} =-4.5V ^(Note 1,2)	-	1.4	-	nC
Gate-Source Charge	Q_gs		-	0.19	-	
Gate-Drain Charge	Q_gd		-	0.2	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	38	-	pF
Output Capacitance	Coss		-	15	-	
Reverse Transfer Capacitance	Crss		-	9	-	
Turn-On Delay Time	td _(on)	$\begin{array}{c} V_{DD}\text{=-}10V,\ I_{D}\text{=-}500\text{mA},\\ V_{GS}\text{=-}4.5V,\\ R_{G}\text{=}6\Omega^{(Note\ 1,2)} \end{array}$	-	7.2	-	ns
Turn-On Rise Time	tr		-	21	-	
Turn-Off Delay Time	td _(off)		-	85	-	
Turn-Off Fall Time	tf		-	116	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-500	mA
Diode Forward Voltage	V_{SD}	I _S =-500mA, V _{GS} =0V	-	-0.93	-1.3	٧

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

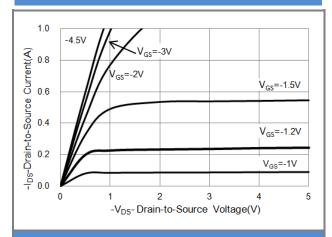


Fig.1 On-Region Characteristics

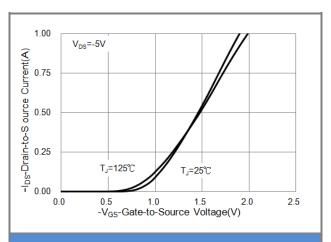


Fig.2 Transfer Characteristics

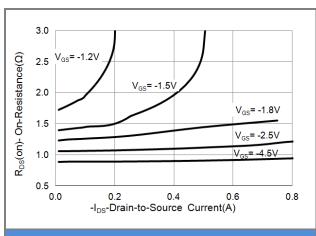


Fig.3 On-Resistance vs. Drain Current

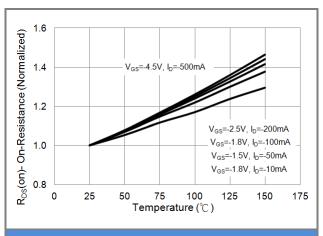


Fig.4 On-Resistance vs. Junction temperature

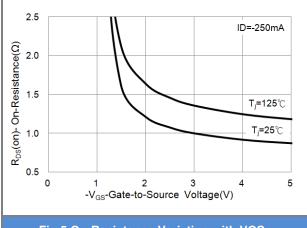
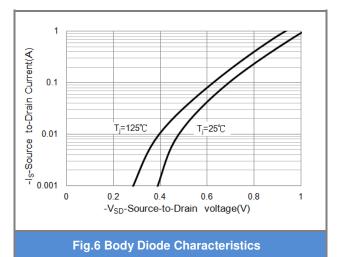


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

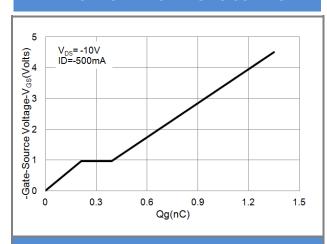


Fig.7 Gate-Charge Characteristics

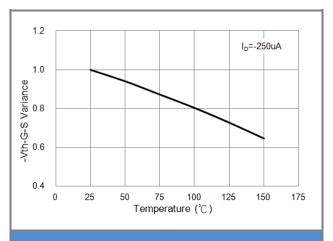


Fig.8 Threshold Voltage Variation with Temperature

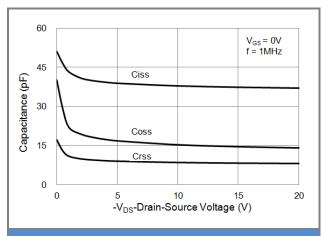


Fig.9 Capacitance vs. Drain-Source Voltage.

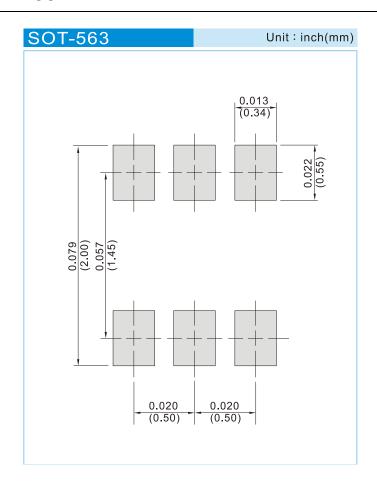




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJX8807_R1_00001	SOT-563	4K pcs / 7" reel	X07	Halogen free RoHS compliant
PJX8807_R2_00001	SOT-563	10K pcs / 13" reel	X07	Halogen free RoHS compliant
PJX8807_R1_00002	SOT-563	8K pcs / 7" reel	X07	Halogen free RoHS compliant
PJX8807_R2_00002	SOT-563	20K pcs / 13" reel	X07	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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