



N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- $25m\Omega$ @ $V_{GS} = 4.5V$
- 29mΩ @ V_{GS} = 2.5V
- 37mΩ @ V_{GS} = 1.8V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

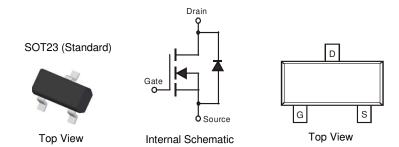
 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMG3414UQ</u>)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208³
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part	Number	Case	Packaging
DMC	G3414U-7	SOT23 (Standard)	3.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\begin{array}{l} \text{MN8} = \text{Product Type Marking Code} \\ \text{Y or } \overline{\text{Y}} = \text{Year (ex: I} = 2021) \\ \text{M} = \text{Month (ex: 9} = \text{September)} \end{array}$

Date Code Key

Year	2009		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	W		I	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage			Vgss	±8	V
Continuous Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +70°C	I _D	4.2 3.2	А
Pulsed Drain Current (Note 6)			Ірм	30	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.78	W
Thermal Resistance, Junction to Ambient @TA = +25°C	Reja	162	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

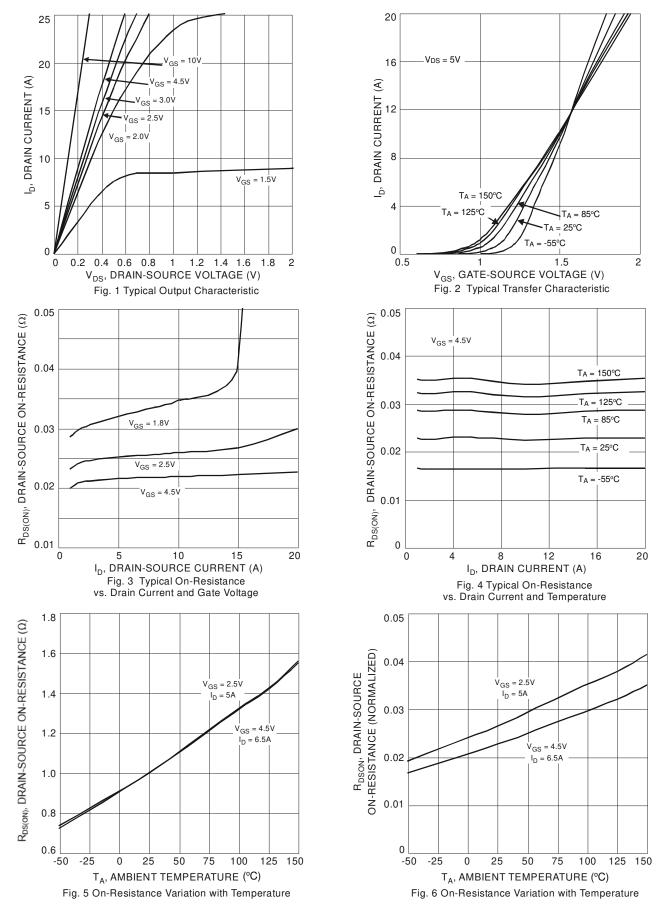
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	1 -			I	I	1
Drain-Source Breakdown Voltage	BVDSS	20		_	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	1.0	μΑ	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	0.9	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			19	25		$V_{GS} = 4.5V, I_{D} = 8.2A$
Static Drain-Source On-Resistance	RDS(ON)	_	22	29	mΩ	V _G S = 2.5V, I _D = 3.3A
			28	37		V _G S = 1.8V, I _D = 2.0A
Forward Transfer Admittance	Y _{FS}	_	7	_	S	$V_{DS} = 10V, I_{D} = 4A$
Diode Forward Voltage	V _{SD}	_	0.6	1	V	V _G S = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance		_	829.9	_	pF	
Output Capacitance	Coss	_	85.3	_	pF	V _{DS} = 10V, V _{GS} = 0V - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	81.2	_	pF	1 - 1.000112
Total Gate Charge	Qg	_	9.6	_	nC	
Gate-Source Charge	Qgs	_	1.5	_	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 8.2A
Gate-Drain Charge	Qgd	_	3.5	_	nC	
Turn-On Delay Time	tD(ON)	_	8.1	_	ns	
Turn-On Rise Time	tr	_	8.3	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	_	40.1	_	ns	$R_L = 10\Omega$, $R_G = 6\Omega$, $I_D = 1A$
Turn-Off Fall Time	tF		9.6	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB with 2oz. copper and test pulse width t ≤ 10s.
 6. Repetitive rating, pulse width limited by junction temperature.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.







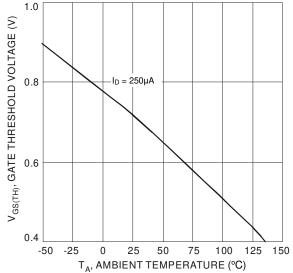
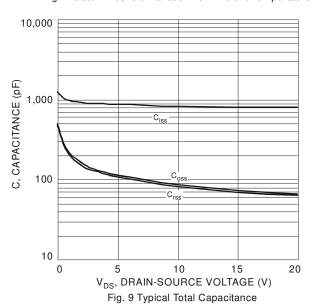


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



10 (Y) TA = 25°C 0.01 0.4 0.5 0.6 0.7 0.8 0.9 1.0 V_{SD}, SOURCE-DRAIN VOLTAGE (V) Fig. 8 Diode Forward Voltage vs. Current

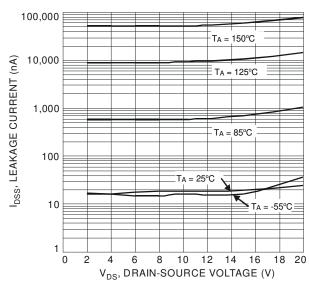


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

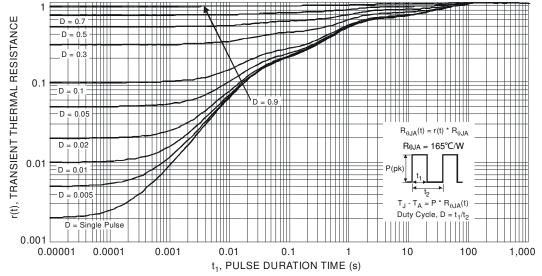


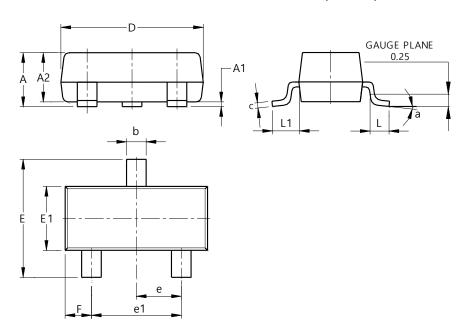
Fig. 11 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)

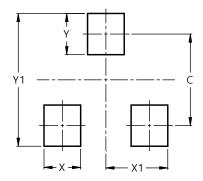


SOT23 (Standard)							
Dim	Min	Max	Тур				
Α	0.90	1.15	1.025				
A1	0.00	0.10	0.05				
A2	0.85	1.10	0.975				
b	0.30	0.51	0.40				
С	0.080	0.202	0.11				
D	2.80	3.00	2.90				
E	2.25	2.55	2.40				
E1	1.20	1.40	1.30				
е	0.89	1.03	0.915				
e1	1.78	2.05	1.83				
F	0.40	0.60	0.535				
L1	0.45	0.61	0.55				
L	0.25	0.55	0.40				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)			
С	2.0			
Х	0.8			
X1	1.35			
Υ	0.9			
Y1	2.9			



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