

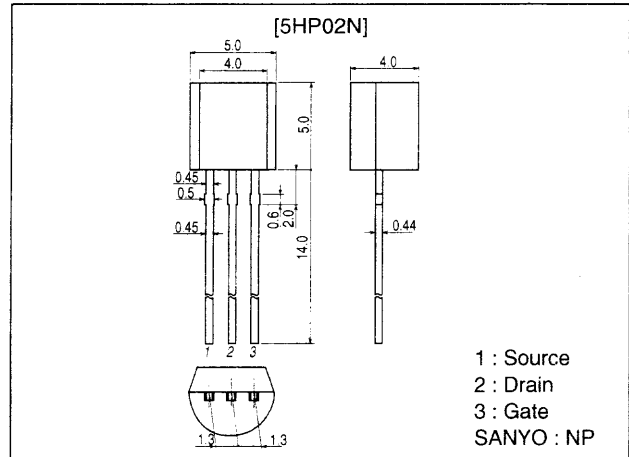
**SANYO****5HP02N****Ultrahigh-Speed Switching Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.

**Package Dimensions**

unit:mm

2178

**Specifications****Absolute Maximum Ratings** at  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DS}$		-50	V
Gate-to-Source Voltage	$V_{GS}$		$\pm 20$	V
Drain Current (DC)	$I_D$		-0.14	A
Drain Current (pulse)	$I_{DP}$	$PW \leq 10\mu\text{s}$ , duty cycles $\leq 1\%$	-0.56	A
Allowable Power Dissipation	$P_D$		0.4	W
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** at  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}$ , $V_{GS} = 0$	-50			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -50\text{V}$ , $V_{GS} = 0$			-10	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 16\text{V}$ , $V_{DS} = 0$			$\pm 10$	$\mu\text{A}$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}$ , $I_D = -100\mu\text{A}$	-1		-2.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}$ , $I_D = -70\text{mA}$	0.12	0.16		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -70\text{mA}$ , $V_{GS} = -10\text{V}$		4.7	6.1	$\Omega$
	$R_{DS(on)2}$	$I_D = -40\text{mA}$ , $V_{GS} = -4\text{V}$		6.5	9.1	$\Omega$

Marking : XF

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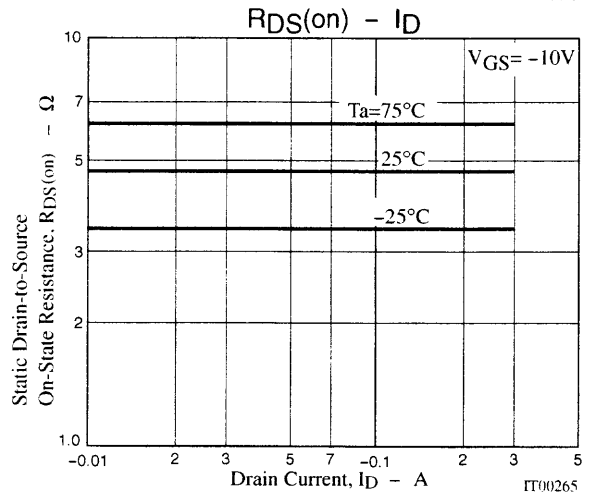
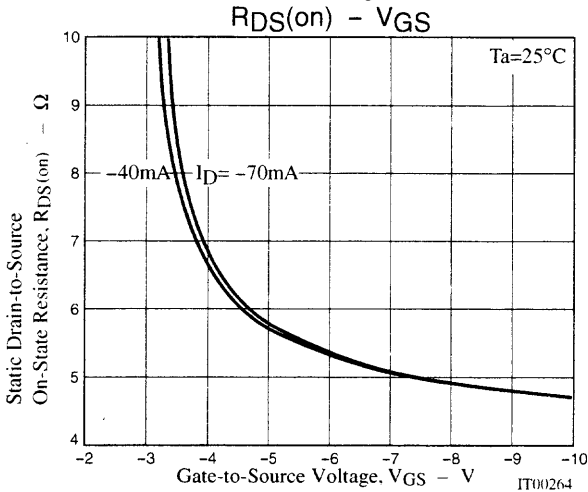
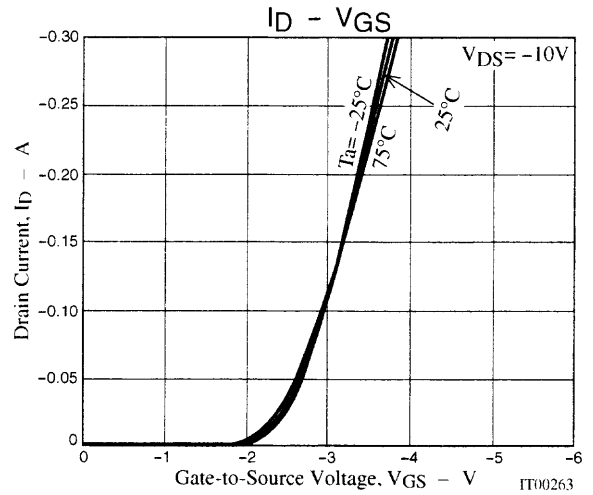
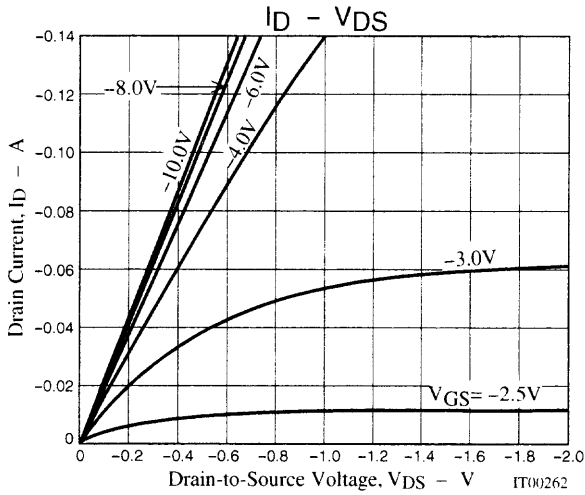
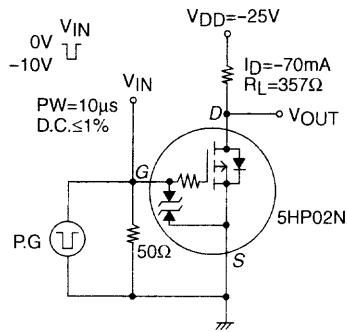
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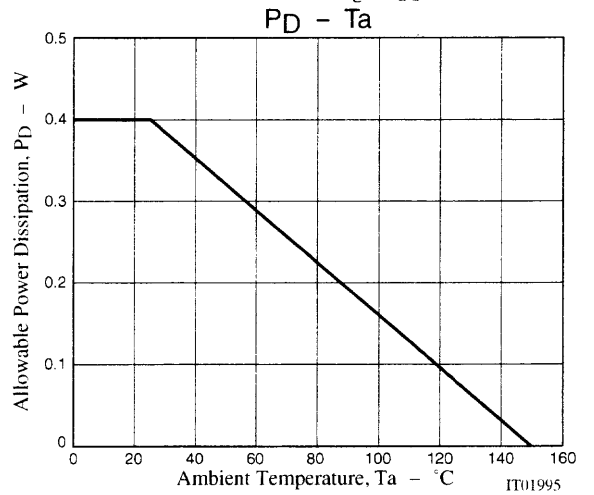
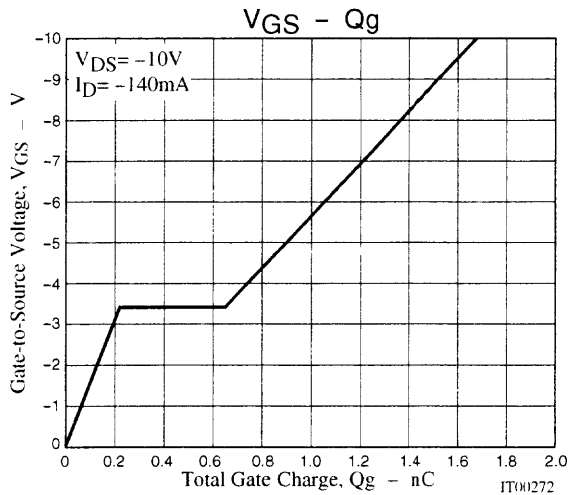
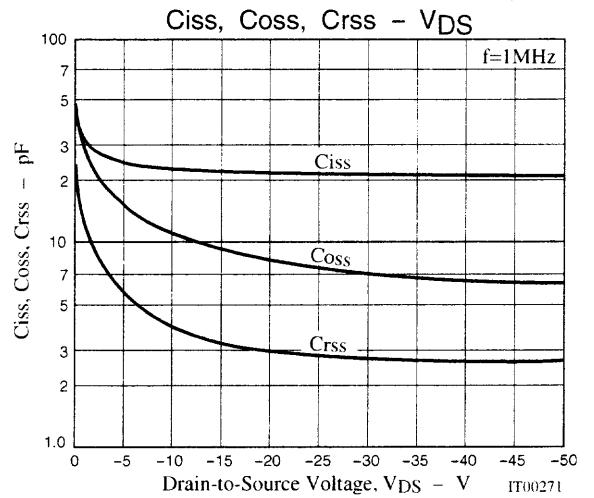
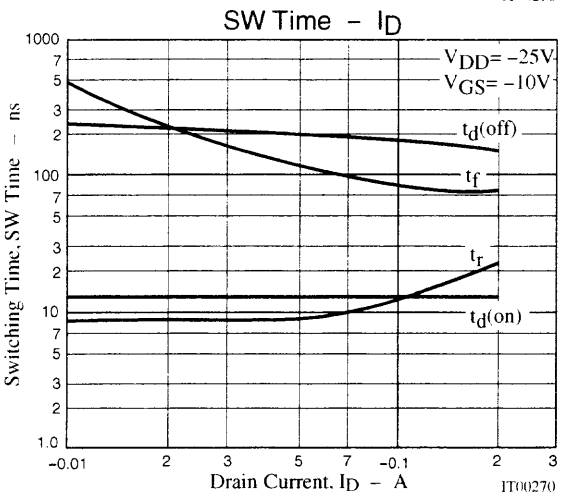
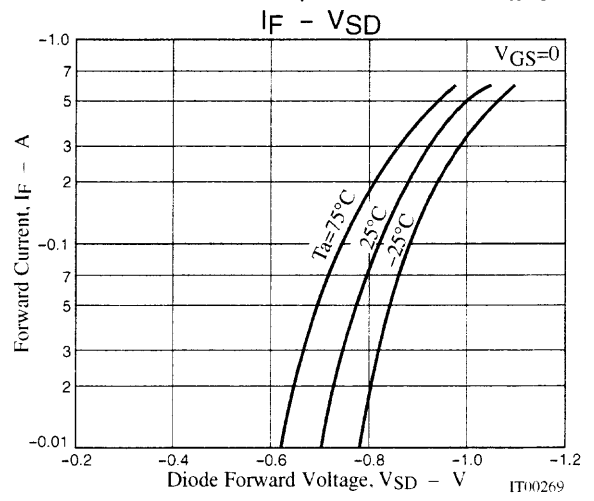
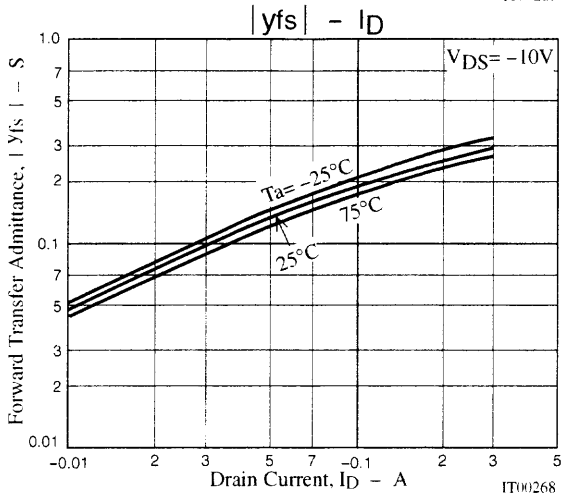
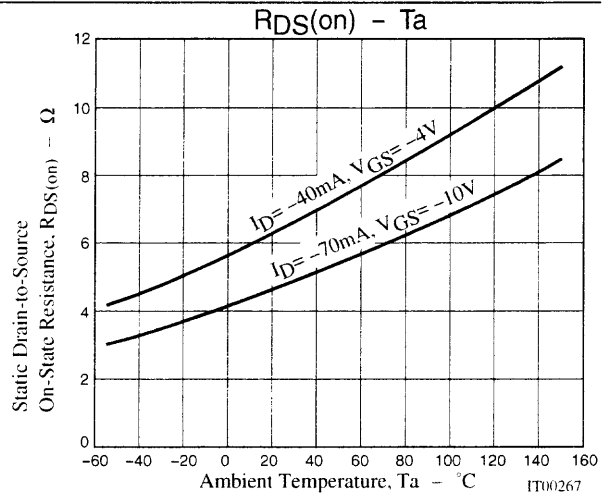
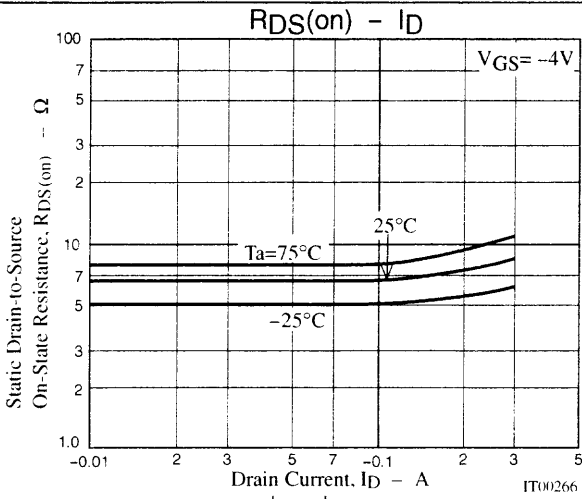
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	$V_{DS} = -10V, f = 1MHz$		23		pF
Output Capacitance	Coss	$V_{DS} = -10V, f = 1MHz$		11		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = -10V, f = 1MHz$		4		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		13		ns
Rise Time	$t_r$	See specified Test Circuit		10		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		190		ns
Fall Time	$t_f$	See specified Test Circuit		95		ns
Total Gate Charge	Qg	$V_{DS} = -10V, V_{GS} = -10V, I_D = -140mA$		1.68		nC
Gate-to-Source Charge	Qgs	$V_{DS} = -10V, V_{GS} = -10V, I_D = -140mA$		0.22		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS} = -10V, V_{GS} = -10V, I_D = -140mA$		0.43		nC
Diode Forward Voltage	VSD	$I_S = -140mA, V_{GS} = 0$	-0.83		-1.2	V

## Switching Time Test Circuit



# 5HP02N



Note on usage : Since the 5HP02N is designed for high-speed switching applications, please avoid using this device in the vicinity of highly charged objects.

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