

October 1986 Revised April 2000

DM74AS805B Hex 2-Input NOR Driver

General Description

These devices contain six independent drivers, each of which performs the logic NOR function. Each driver has increased output drive capability to allow the driving of high capacitive loads.

Features

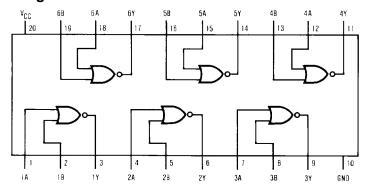
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL
- Functionally and pin for pin compatible with advanced low power Schottky TTL counterpart

Ordering Code:

Order Number	Package Number	Package Description			
DM74AS805BWM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide			
DM74AS805BN	N20A	20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

$$Y = \overline{A} + \overline{B}$$

Inp	Output	
Α	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V
Input Voltage 7V

Operating Free Air Temperature Range 0°C to $+70^{\circ}\text{C}$ Storage Temperature Range -65°C to $+150^{\circ}\text{C}$

Typical θ_{JA}

N Package 58.3°C/W M Package 154.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-48	mA
I _{OL}	LOW Level Output Current			48	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

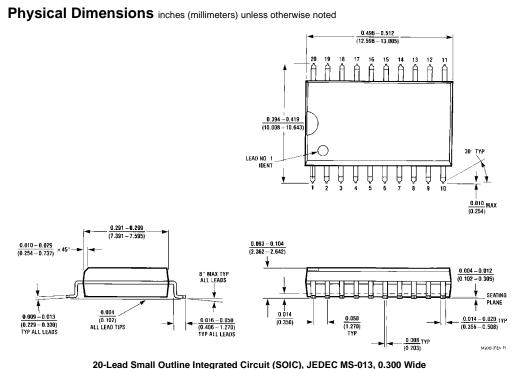
over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.2	V
V _{OH}	HIGH Level	$I_{OH} = -2 \text{ mA}, V_{CC} = 4.5 \text{V to } 5.5 \text{V}$		V _{CC} - 2			
	Output Voltage	$I_{OH} = -3 \text{ mA}, V_{CC} = 4.5 \text{V}$		2.4			V
		$I_{OH} = Max$, $V_{CC} = 4.5V$		2			
V _{OL}	LOW Level	$V_{CC} = 4.5V,$			0.35	0.5	V
	Output Voltage	I _{OL} = Max			0.33	0.5	V
I _I	Input Current @ Max Input Voltage	$V_{CC} = 5.5V, V_{IH} = 7V$				0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.5	mA
Io	Output Drive Current	$V_{CC} = 5.5V, V_{O} = 2.25V$		-50	-135	-200	mA
I _{CC}	Supply Current	V _{CC} = 5.5V	Outputs HIGH		6.5	10	mA
			Outputs LOW		18	32	mA

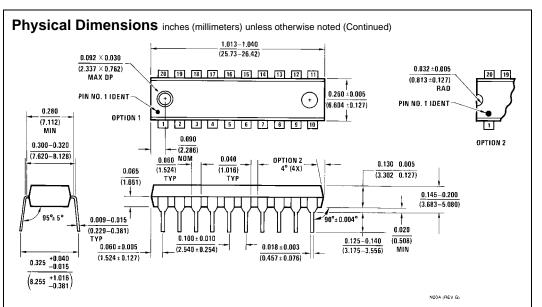
Switching Characteristics

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}		$V_{CC} = 4.5V \text{ to } 5.5V$ $R_L = 500\Omega$	1	4.3	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	C _L = 50 pF	1	4.3	ns



20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide Package Number M20B



20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N20A

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