



DMT12H007SPS

120V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	RDS(ON) Max	I _D Tc = +25°С
120V	8.9mΩ @ V _{GS} = 10V	80A
1200	16mΩ @ V _{GS} = 6V	65A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Switching
- DC-DC converters

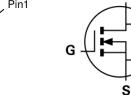
Features

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low RDS(ON) Minimizes On-State Losses
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

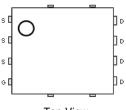
- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (@)
- Weight: 0.097 grams (Approximate)







D



Top View Pin Configuration

Ordering Information (Note 4)

Part Number	Baakaga	Packing		
	Package	Qty.	Carrier	
DMT12H007SPS-13	PowerDI5060-8	2,500	Tape & Reel	

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

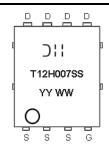
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:



J | | = Manufacturer's Marking
T12H007SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 22 = 2022)
WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Value	Unit
Drain-Source Voltage		VDSS	120	V
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	ID	80 64	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	320	А
Continuous Body Diode Forward Current (Note 5)	Tc = +25°C	ls	80	А
Pulsed Body Diode Forward Current (Note 5)	Tc = +25°C	lsм	320	А
Avalanche Current, L = 3mH		las	15.5	А
Avalanche Energy, L = 3mH		Eas	360.4	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	PD	2.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Reja	43	°C/W
Total Power Dissipation (Note 5)	PD	104	W
Thermal Resistance, Junction to Case (Note 5)	Rejc	1.2	°C/W
Operating and Storage Temperature Range	Tj, Tstg	-55 to +150	°C

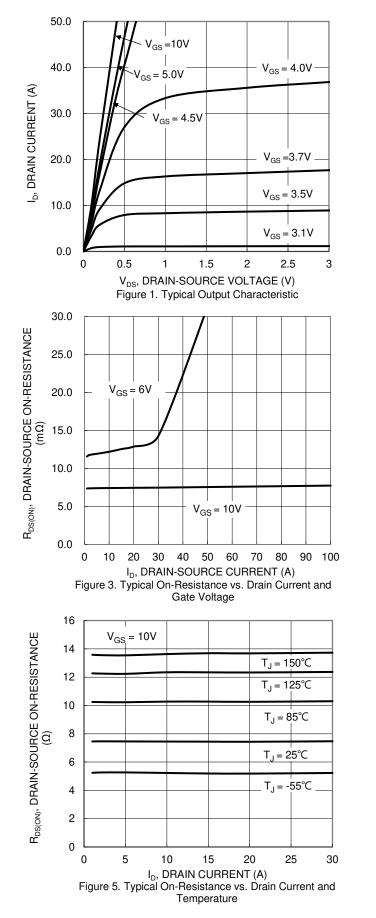
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

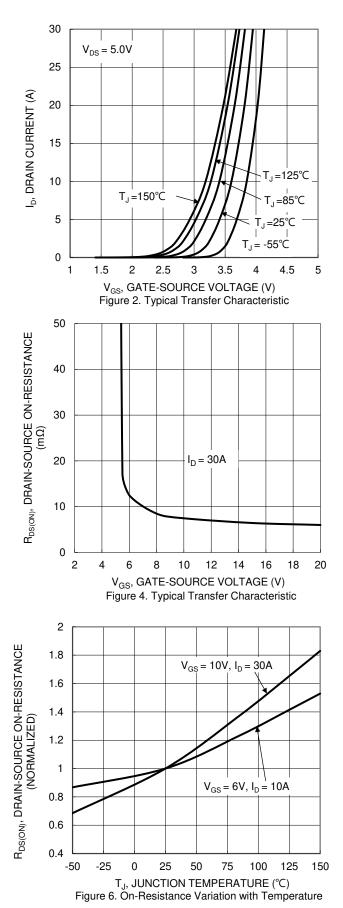
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	120	—	—	V	$V_{GS} = 0V, I_{D} = 10mA$	
Zero Gate Voltage Drain Current	IDSS		_	1	μA	V _{DS} = 96V, V _{GS} = 0V	
Gate-Source Leakage	lgss		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)			•	•	•	·	
Gate Threshold Voltage	VGS(TH)	2	_	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Descent	_	7.5	8.9	mΩ	V _{GS} = 10V, I _D = 30A	
Static Drain-Source On-Resistance	RDS(ON)	_	12	16	11122	$V_{GS} = 6V, I_D = 10A$	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 30A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	3142	_		$V_{DS} = 60V, V_{GS} = 0V$ f = 1MHz	
Output Capacitance	Coss	_	665	_	pF		
Reverse Transfer Capacitance	Crss	_	29	_			
Gate Resistance	Rg	_	1.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 6V)	Qg	—	27	—			
Total Gate Charge (V _{GS} = 10V)	Qg	_	44	_	nC	V _{DS} = 60V, I _D = 25A	
Gate-Source Charge	Qgs	_	15	_	no		
Gate-Drain Charge	Q _{gd}	—	9	_			
Turn-On Delay Time	t _{D(ON)}	_	12.5	_			
Turn-On Rise Time	tR	_	13.7	_		$ V_{DD} = 60V, V_{GS} = 10V \\ I_D = 25A, R_G = 2.7\Omega $	
Turn-Off Delay Time	td(OFF)	_	24.4	_	ns		
Turn-Off Fall Time	tF	_	10.9	_			
Reverse Recovery Time	t _{RR}		55	_	ns	I _F = 25A, dl/dt = 100A/µs	
Reverse Recovery Charge	Qrr		105	—	nC	$TF = 25A$, $u/ut = 100A/\mu s$	

Notes:

Thermal resistance from junction to soldering point (on the exposed drain pad).
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

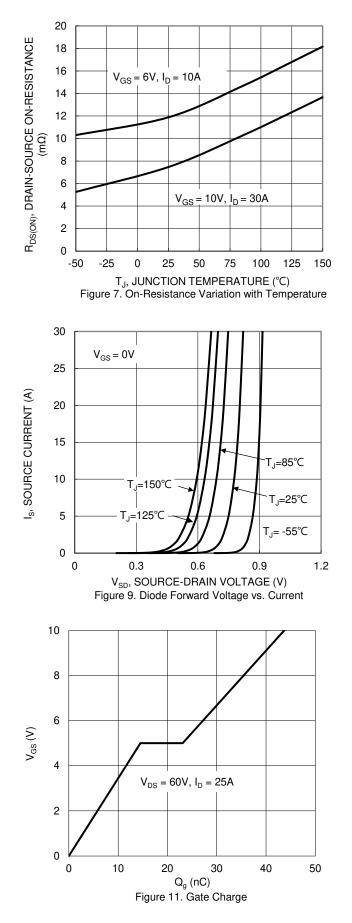


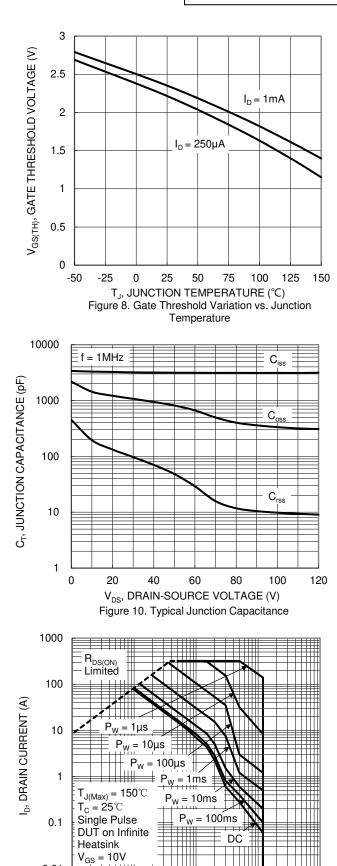




DMT12H007SPS Document number: DS41427 Rev. 5 - 2





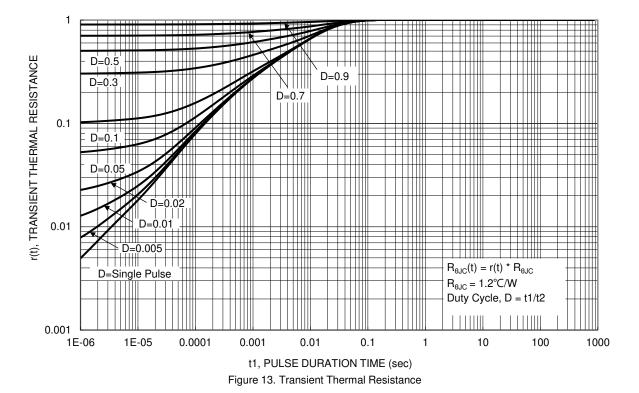


1 10 100 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

0.01

0.1



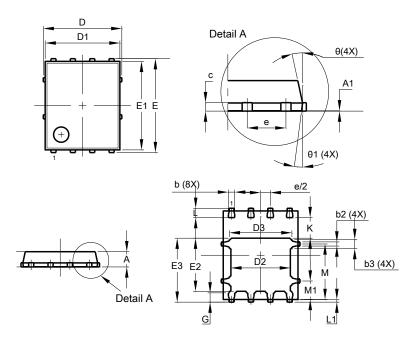


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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

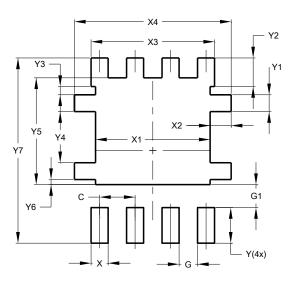


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
c	0.230	0.330	0.277		
D	5.15 BSC				
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
E	6.15 BSC				
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
Μ	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
¥7	6.610

PowerDI5060-8



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