# TMPIM 50 A CIB/CI Module

# NXH50C120L2C2ESG, NXH50C120L2C2ES1G

The NXH50C120L2C2ESG is a transfer-molded power module with low thermal resistance substrate containing a converter-inverter-brake circuit consisting of six 50 A, 1600 V rectifiers, six 50 A, 1200 V IGBTs with inverse diodes, one 35 A, 1200 V brake IGBT with brake diode and an NTC thermistor.

The NXH50C120L2C2ES1G is a transfer-molded power module with low thermal resistance substrate containing a converter-inverter circuit consisting of six 50 A, 1600 V rectifiers, six 50 A, 1200 V IGBTs with inverse diodes, and an NTC thermistor.

#### Features

- Low Thermal Resistance Substrate for Low Thermal Resistance
- Lower Package Height than Standard Case Modules
- 6 mm Clearance distance between pin to heatsink
- Compact 73 mm  $\times$  40 mm  $\times$  8 mm Package
- Solderable Pins
- Thermistor
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- Industrial Motor Drives
- Servo Drives

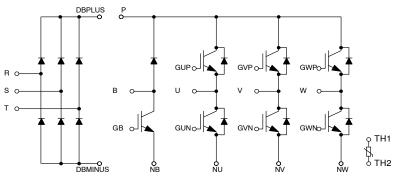


Figure 1. NXH50C120L2C2ESG Schematic Diagram

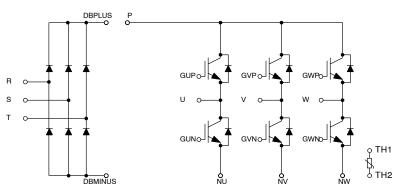
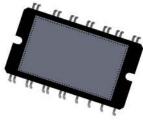


Figure 2. NXH50C120L2C2ES1G Schematic Diagram



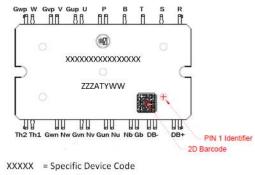
### **ON Semiconductor®**

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DIP26 67.8x40 CASE 181AD

#### MARKING DIAGRAM



- ZZZ = Assembly Lot Code
- AT = Assembly & Test Site Code
- YYWW = Year and Work Week Code

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>	
NXH50C120L2C2ESG	DIP26 (Pb-Free)	6 Units /	
NXH50C120L2C2ES1G		Tube	

Rating	Symbol	Value	Unit
IGBT (INVERTER, BRAKE)	•		
Collector-emitter Voltage	V <sub>CES</sub>	1200	V
Gate-emitter Voltage	V <sub>GE</sub>	±20	V
Inverter IGBT Continuous Collector Current @ $T_C$ = 100°C ( $T_{VJmax}$ = 175°C)	۱ <sub>C</sub>	50	А
Inverter IGBT Pulsed Collector Current (T <sub>VJmax</sub> = 175°C)	I <sub>Cpulse</sub>	150	А
Brake IGBT Continuous Collector Current @ $T_C$ = 100°C ( $T_{VJmax}$ = 175°C)	Ι <sub>C</sub>	35	A
Brake IGBT Pulsed Collector Current (T <sub>VJmax</sub> = 175°C)	I <sub>Cpulse</sub>	105	А
DIODE (INVERTER, BRAKE)			
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	1200	V
Inverter Diode Continuous Forward Current @ $T_c = 80^{\circ}C (Tv_{Jmax} = 17^{\circ}C)$	١ <sub>F</sub>	50	А
Inverter Diode Repetitive Peak Forward Current ( $T_{VJmax} = 175^{\circ}C$ )	I <sub>FRM</sub>	150	А
Inverter Diode I <sup>2</sup> t value (60 Hz single half-sine wave)	l <sup>2</sup> t	94	A <sup>2</sup> t
Brake Diode Continuous Forward Current @ Tc = $80^{\circ}C$ (T <sub>VJmax</sub> = $175^{\circ}C$ )	١ <sub>F</sub>	35	А
Brake Diode Repetitive Peak Forward Current (T <sub>VJmax</sub> = 175°C)	I <sub>FRM</sub>	105	А
Brake Diode I2t value (60 Hz single half-sine wave)	l <sup>2</sup> t	46	A <sup>2</sup> t
RECTIFIER DIODE			
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	1600	V
Continuous Forward Current @ T <sub>C</sub> = 80°C (T <sub>VJmax</sub> = 150°C)	١ <sub>F</sub>	50	A
Repetitive Peak Forward Current (T <sub>VJmax</sub> = 150°C)	I <sub>FRM</sub>	150	А
l <sup>2</sup> t value (60 Hz single half-sine wave) @ 25ºC	l <sup>2</sup> t	1126	A <sup>2</sup> t
(60 Hz single half-sine wave) @ 150°C		510	
Surge current (10ms sin180º) @ 25ºC	IFSM	520	A
MODULE THERMAL PROPERTIES			
Storage Temperature Range	T <sub>stg</sub>	-40 to 125	°C
INSULATION PROPERTIES		-	
Isolation Test Voltage, t = 1 s, 50 Hz	V <sub>is</sub>	3000	V <sub>RMS</sub>
Internal Isolation		HPS	
Creepage Distance		6.0	mm
Clearance Distance		6.0	mm
Comperative Tracking Index	CTI	>400	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
Refer to ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION for Safe Operating parameters.

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
INVERTER IGBT CHARACTERISTICS						
Collector-emitter Cutoff Current	$V_{GE}$ = 0 V, $V_{CE}$ = 1200 V	I <sub>CES</sub>	-	-	250	μA
Collector-emitter Saturation Voltage	$V_{GE}$ = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>CE(sat)</sub>	-	1.8	2.4	V
	$V_{GE}$ = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 150°C		-	2	-	
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 6 \text{ mA}$	V <sub>GE(TH)</sub>	4.8	6	6.8	V
Gate Leakage Current	$V_{GE} = 20 \text{ V}, \text{ V}_{CE} = 0 \text{ V}$	I <sub>GES</sub>	-	-	400	nA
Turn-on Delay Time	$T_J = 25^{\circ}C$	t <sub>d(on)</sub>	-	144	_	ns
Rise Time	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 50 A V <sub>GE</sub> = ±15 V, R <sub>G</sub> = 15 Ω	t <sub>r</sub>	-	104	_	1
Turn-off Delay Time		t <sub>d(off)</sub>	-	380	-	
Fall Time		t <sub>f</sub>	-	52	_	
Turn-on Switching Loss per Pulse		Eon	-	5870	_	μJ
Turn-off Switching Loss per Pulse		E <sub>off</sub>	-	1700	_	
Turn-on Delay Time	$T_J = 150^{\circ}C$	t <sub>d(on)</sub>	-	136	_	ns
Rise Time	$V_{CE} = 600 \text{ V}, \text{ I}_{C} = 50 \text{ A}$ $V_{GE} = \pm 15 \text{ V}, \text{ R}_{G} = 15 \Omega$	t <sub>r</sub>	-	112	_	
Turn-off Delay Time		t <sub>d(off)</sub>	-	432	_	
Fall Time		t <sub>f</sub>	-	184	_	
Turn-on Switching Loss per Pulse		Eon	-	9530	_	μJ
Turn-off Switching Loss per Pulse		E <sub>off</sub>	-	3800	_	]
Input Capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, \text{ f} = 100$	Cies	-	11897	_	pF
Output Capacitance	kHz	C <sub>oes</sub>	-	416	_	
Reverse Transfer Capacitance		C <sub>res</sub>	-	240	-	
Total Gate Charge		Qg	-	558	_	nC
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance – Chip-to-Case		R <sub>thJC</sub>	-	0.26	_	°C/W
INVERSE DIODE CHARACTERISTICS			•			
Diode Forward Voltage	I <sub>F</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>F</sub>	_	1.9	2.7	V
	I <sub>F</sub> = 50 A, T <sub>J</sub> = 150°C			1.7	_	
Reverse Recovery Charge	$T_J = 25^{\circ}C$	Q <sub>rr</sub>	-	2.58	-	μC
Peak Reverse Recovery Current	$V_{CE}^{}$ = 600 V, I <sub>C</sub> = 50 A V <sub>GE</sub> = ±15 V, R <sub>G</sub> = 15 Ω	I <sub>RRM</sub>	-	20	_	А
Reverse Recovery Energy		Err	-	640	-	μJ
Reverse Recovery Charge	T <sub>J</sub> = 150°C	Q <sub>rr</sub>	-	8.0	_	μC
Peak Reverse Recovery Current	$V_{CE}$ = 600 V, I <sub>C</sub> = 50 A V <sub>GE</sub> = ±15 V, R <sub>G</sub> = 15 Ω	I <sub>RRM</sub>	-	32.5	-	A
Reverse Recovery Energy		E <sub>rr</sub>	_	2300	-	μJ
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance – Chip-to-Case		R <sub>thJC</sub>	_	0.42		°C/W

ELECTRICAL CHARACTERISTICS	$(I_J = 25^{\circ}C \text{ unless otherwise specified})$	) (continued)				
Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
RECTIFIER DIODE CHARACTERISTICS						
Diode Forward Voltage	$I_F = 50 \text{ A}, T_J = 25^{\circ}\text{C}$ $V_F$		-	1.2	1.6	V
	$I_F = 50 \text{ A}, T_J = 150^{\circ}\text{C}$		_	1.1	_	1
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		R <sub>thJC</sub>	_	0.33	_	°C/W
BRAKE IGBT CHARACTERISTICS						
Collector-emitter Cutoff Current	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V	I <sub>CES</sub>	-	-	250	μA
Collector-emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 35 A, T <sub>J</sub> = 25°C	V <sub>CE(sat)</sub>	_	1.8	2.4	V
	$V_{GE}$ = 15 V, I <sub>C</sub> = 35 A, T <sub>J</sub> = 125°C	~ /	_	1.9	_	1
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 4.25$ mA	V <sub>GE(TH)</sub>	4.8	6	6.8	V
Gate Leakage Current	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	IGES	_	_	400	nA
Turn-on Delay Time	$T_{\rm J} = 25^{\circ} \rm C$	t <sub>d(on)</sub>	_	104	_	ns
Rise Time	$V_{CE} = 600 \text{ V}, \text{ I}_{C} = 35 \text{ A}$	t <sub>r</sub>	_	64	_	1
Turn-off Delay Time	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	t <sub>d(off)</sub>	_	277	_	-
Fall Time	-	t <sub>f</sub>	_	53	_	-
Turn–on Switching Loss per Pulse	-	E <sub>on</sub>	_	2900	_	μJ
Turn off Switching Loss per Pulse	-	E <sub>off</sub>	_	1200	_	
Turn-on Delay Time	T <sub>.1</sub> = 150°C	t <sub>d(on)</sub>	_	168	_	ns
Rise Time	$V_{CE} = 600 \text{ V}, \text{ I}_{C} = 35 \text{ A}$	t <sub>r</sub>	_	72	_	1
Turn-off Delay Time	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	t <sub>d(off)</sub>	_	320	_	1
Fall Time	-	t <sub>f</sub>	_	165	_	1
Turn-on Switching Loss per Pulse	-	E <sub>on</sub>	_	4030	_	μJ
Turn off Switching Loss per Pulse	-	E <sub>off</sub>	_	2200	_	
Input Capacitance	V <sub>CE</sub> = 20 V. V <sub>GE</sub> = 0 V.	Cies	_	8333	_	pF
Output Capacitance	f = 100 kHz	C <sub>oes</sub>	_	298	_	1
Reverse Transfer Capacitance	-	C <sub>res</sub>	_	175	_	1
Total Gate Charge	V <sub>CF</sub> = 600 V, I <sub>C</sub> = 35 A,	Q <sub>g</sub>	_	360	_	nC
9		9				
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance – Chip-to-Case		R <sub>thJC</sub>	I	0.42		°C/W
BRAKE DIODE CHARACTERISTICS						
Brake Diode Reverse Leakage Current	VR = 1200 V	IR	—	-	200	μΑ
Diode Forward Voltage	$I_F = 35 \text{ A}, T_J = 25^{\circ}\text{C}$	V <sub>F</sub>	_	2.2	2.7	V
	I <sub>F</sub> = 35 A, T <sub>J</sub> = 150°C		_	2	_	1
Reverse Recovery Time	$T_J = 25^{\circ}C$	t <sub>rr</sub>		224		ns
Reverse Recovery Charge	$V_{CE} = 600 \text{ V}, \text{ I}_{C} = 35 \text{ A}$	Q <sub>rr</sub>	_	1.51	_	°C
Peak Reverse Recovery Current	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	I <sub>RRM</sub>	-	18	_	А
Reverse Recovery Energy		Err	_	410	_	μJ
Reverse Recovery Time	T <sub>J</sub> = 150°C	t <sub>rr</sub>	_	532	_	ns
Reverse Recovery Charge	$V_{CE} = 600 \text{ V}, I_C = 35 \text{ A}$	Q <sub>rr</sub>	_	5,36	_	°C
Peak Reverse Recovery Current	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	I <sub>RRM</sub>	_	30	_	А
Reverse Recovery Energy	1	E <sub>rr</sub>	_	1983	-	μJ
Temperature under switching conditions		Tvj op	-40	1	150	°C
Thermal Resistance – Chip-to-Case		R <sub>thJC</sub>		0.65		°C/W

### FI FCTRICAL CHARACTERISTICS (T. - 25°C unless otherwise specified) (continued)

#### **ELECTRICAL CHARACTERISTICS** ( $T_J$ = 25°C unless otherwise specified) (continued)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
THERMISTOR CHARACTERISTICS						
Nominal Resistance	T = 25°C	R <sub>25</sub>	-	5	—	kΩ
Nominal Resistance	T = 100°C	R <sub>100</sub>	-	493.3	-	Ω
Deviation of R25		$\Delta R/R$	-5	-	5	%
Power Dissipation		PD	-	20	-	mW
Power Dissipation Constant			-	1.4	-	mW/K
B-value	B(25/50), tolerance ±2%		-	3375	_	К
B-value	B(25/100), tolerance ±2%		_	3433	_	К

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### **TYPICAL CHARACTERISTICS – INVERTER IGBT & INVERSE DIODE**

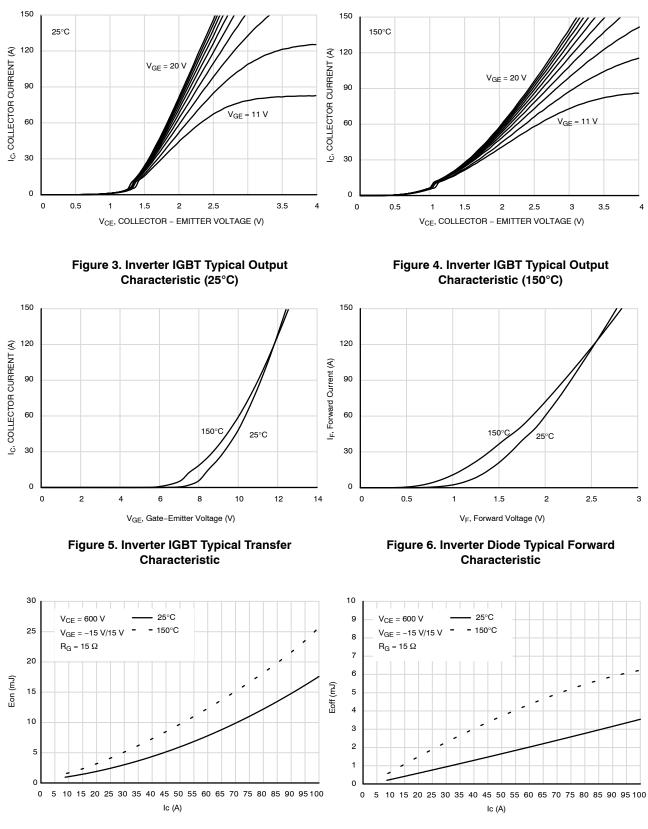




Figure 8. Inverter IGBT Typical Turn Off Loss vs IC

### **TYPICAL CHARACTERISTICS – INVERTER IGBT & INVERSE DIODE**

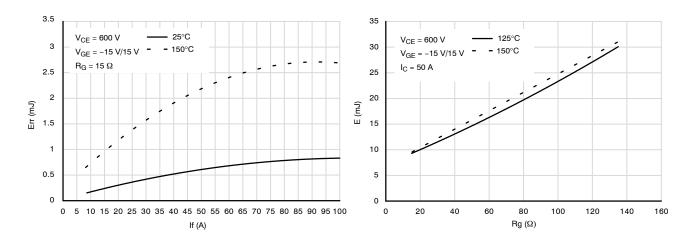


Figure 9. Inverter Diode Typical Reverse Recovery Energy vs IC

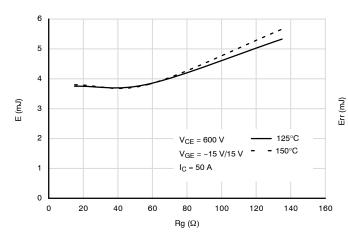


Figure 11. Inverter IGBT Typical Turn Off Loss vs RG

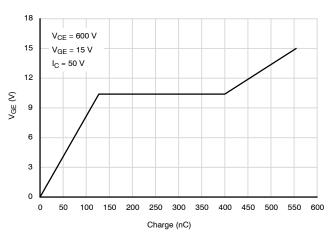




Figure 10. Inverter IGBT Typical Turn On Loss vs RG

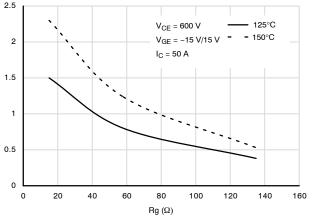


Figure 12. Inverter Diode Typical Reverse Recovery Energy vs RG

### **TYPICAL CHARACTERISTICS – INVERTER IGBT & INVERSE DIODE**

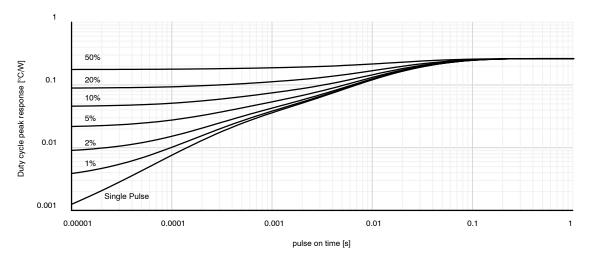


Figure 14. Inverter IGBT Junction-to-case Transient Thermal Impedance

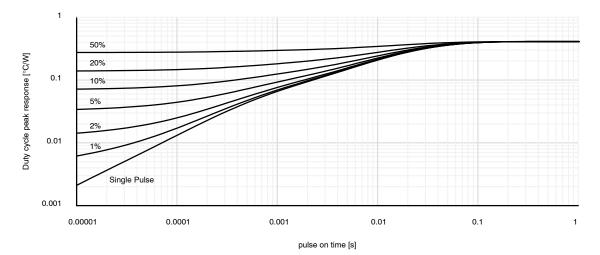
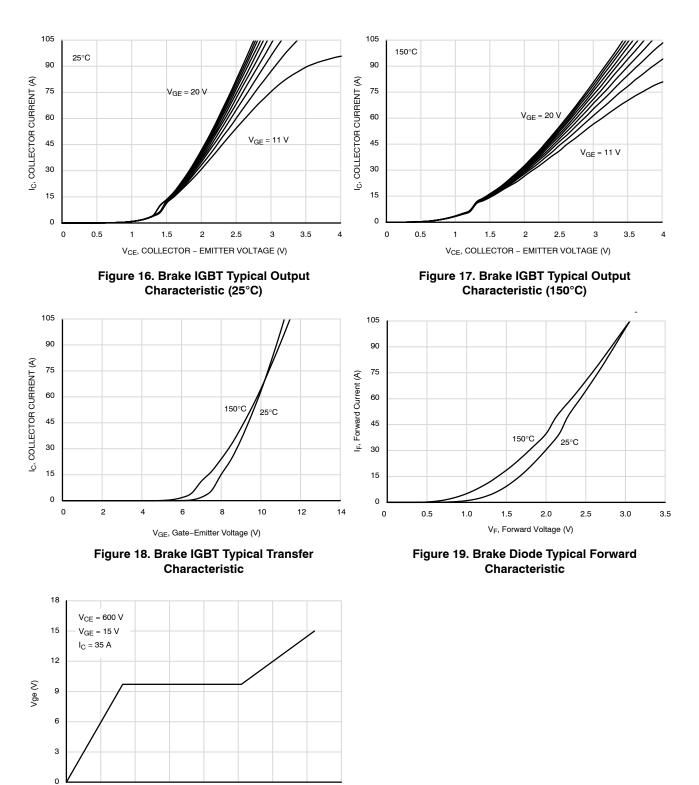


Figure 15. Inverter Diode Junction-to-case Transient Thermal Impedance

#### **TYPICAL CHARACTERISTICS - BRAKE IGBT & BRAKE DIODE**



Charge

**TYPICAL CHARACTERISTICS – RECTIFIER** 

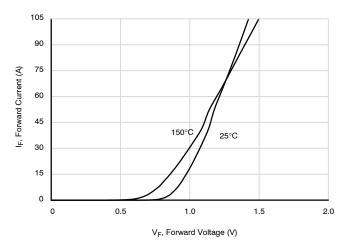


Figure 21. Rectifier Typical Forward Characteristic

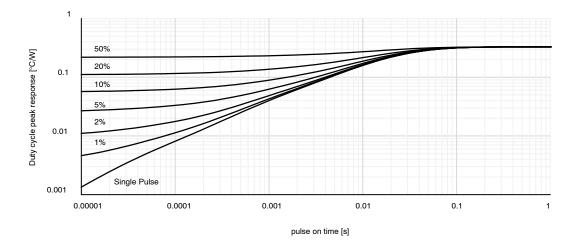
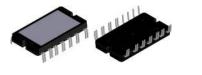
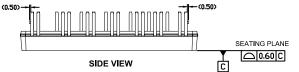
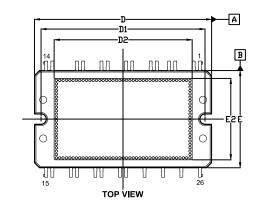
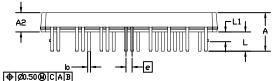


Figure 22. Rectifier Junction-to-Case Transient Thermal Impedance

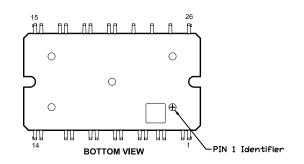








SIDE VIEW



DIP26 67.8x40 CASE 181AD ISSUE B

DATE 05 AUG 2021

#### NOTES:

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END VIEW

- 1. Dimensioning and tolerancing as per ASME Y14.5M, 2009
- 2. Controlling Dimension: Millimeters
- 3. Dimensions are exclusive of Burrs, Mold Flash, and Tiebar extrusions
- 4. Dimensions "b" and "c" apply to plated leads
- 5. Position of the leads is determine at the root of the lead where it exits the package body

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
А	15.50	16.00	16.50		
A2	7.80	8.00	8.20		
A3		6.00 REF			
b	1.10	1.20	1.30		
с	0.70	0.80	0.90		
D	72.70	73.20	73.70		
D1	67.30	67.80	68.30		
D2		57.30 REF			
Е	39.70	40.20	40.70		
E1	46.70	47.20	47.70		
E2		33.87 REF			
е		2.54 BSC			
F	4.00	4.20	4.40		
L	8.00 REF				
L1	3.50	4.00	4.50		
М	4°	5°	6°		

#### GENERIC MARKING DIAGRAM\*



XXX = Specific Device Code

ZZZ = Assembly Lot Code

AT = Assembly & Test Location

Y = Year

WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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