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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR P32N055HHE, NP32N055IHE, NP32N055SHE

SWITCHING N-CHANNEL POWER MOSFET

DESCRIPTION

These products are N-Channel MOS Field Effect Transistors designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance $R_{\text{DS(on)}} = 25 \text{ m}\Omega \text{ MAX.} (\text{V}_{\text{GS}} = 10 \text{ V}, \text{ I}_{\text{D}} = 16 \text{ A})$
- Low Ciss : Ciss = 1100 pF TYP.
- Built-in gate protection diode

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage	VDSS	55	V	
Gate to Source Voltage	Vgss	±20	V	
Drain Current (DC)	ID(DC)	±32	А	
Drain Current (Pulse) ^{Note1}	D(pulse)	±100	А	
Total Power Dissipation (T _A = 25°C)	Рт	1.2	W	
Total Power Dissipation (Tc = 25°C)	Рт	66	W	
Single Avalanche Current Note2	las	26 / 21 / 7	А	
Single Avalanche Energy Note2	Eas	6.7 / 44 / 49	mJ	
Channel Temperature	Tch	175	°C	
Storage Temperature	Tstg	–55 to + 175	°C	



PART NUMBER	PACKAGE	
NP32N055HHE	TO-251 (JEITA) / MP-3	
NP32N055IHE Note	TO-252 (JEITA) / MP-3Z	
NP32N055SHE	TO-252 (JEDEC) / MP-3ZK	

Note Not for new design.



(TO-251)

(TO-252)



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V (See Figure 4.)

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	2.27	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	125	°C/W

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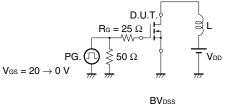
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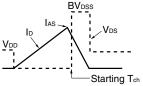
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 55 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 16 A	6	12		S
Drain to Source On-state Resistance	RDS(on)	V _{GS} = 10 V, I _D = 16 A		19	25	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V		1100	1600	pF
Output Capacitance	Coss	V _{GS} = 0 V		180	270	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		95	170	pF
Turn-on Delay Time	td(on)	V _{DD} = 28 V, I _D = 16 A		16	35	ns
Rise Time	tr	V _{GS} = 10 V		11	27	ns
Turn-off Delay Time	td(off)	R _G = 1 Ω		29	58	ns
Fall Time	tr			10	24	ns
Total Gate Charge	QG	V _{DD} = 44 V		21	32	nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		6		nC
Gate to Drain Charge	Qgd	ID = 32 A		8		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 32 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 32 A, VGS = 0 V		40		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		57		nC

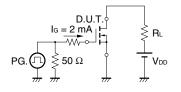
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

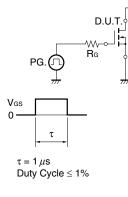


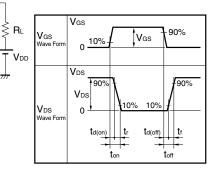


TEST CIRCUIT 3 GATE CHARGE



TEST CIRCUIT 2 SWITCHING TIME





TYPICAL CHARACTERISTICS (TA = 25°C)

NEC

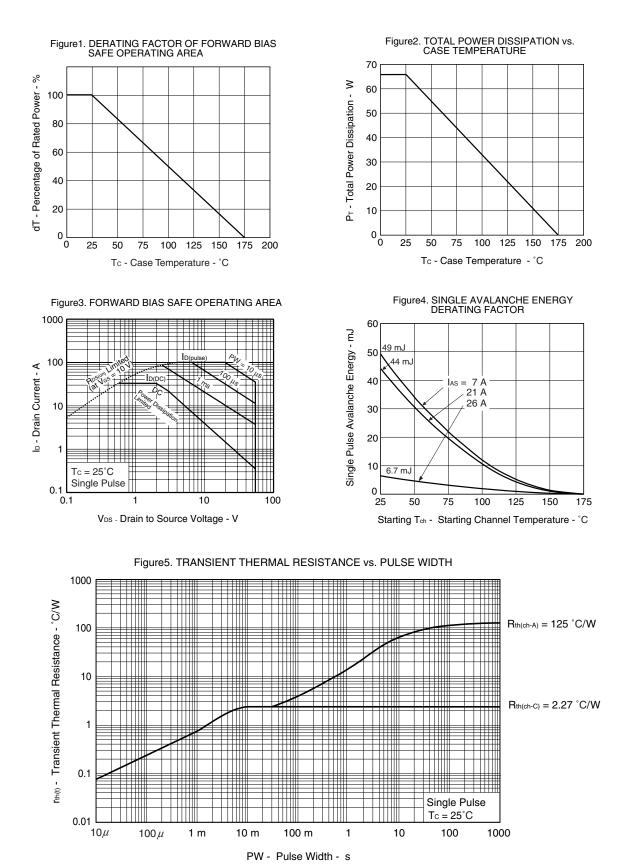
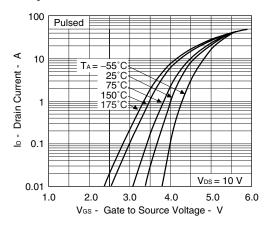
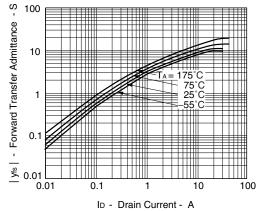


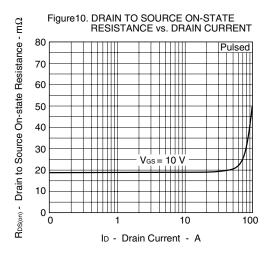
Figure6. FORWARD TRANSFER CHARACTERISTICS

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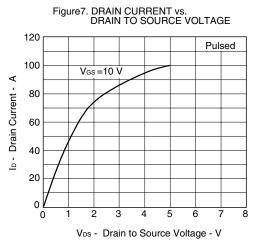


Figure9. DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

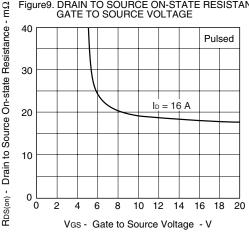
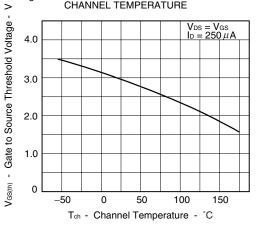
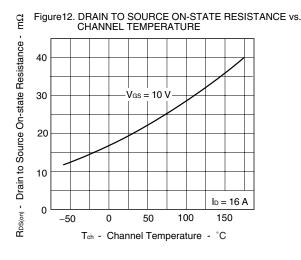
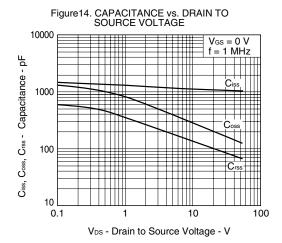


Figure11. GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE







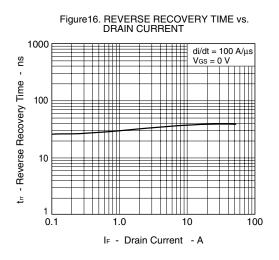


Figure 13. SOURCE TO DRAIN DIODE FORWARD VOLTAGE

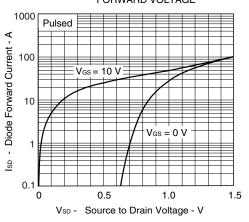


Figure 15. SWITCHING CHARACTERISTICS

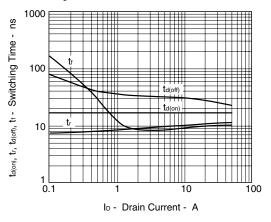
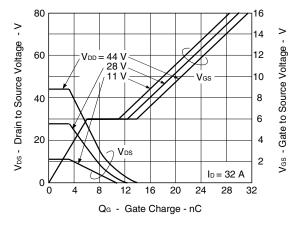


Figure17. DYNAMIC INPUT/OUTPUT CHARACTERISTICS



2) TO-252 (JEITA) / MP-3Z

1

<u>1.1±0.2</u>

4.3 MAX

. Έ

. 8.0

2.3 TYP

6.5±0.2

5.0±0.2

4

з

2.0 MIN.

1.5+0.2

5.5±0.2

0.9 MAX.

2.3 TYP.

0.8 MAX

1. Gate

2. Drain

3. Source

4. Fin (Drain)

10.0 MAX

2.3±0.2

0.5±0.1

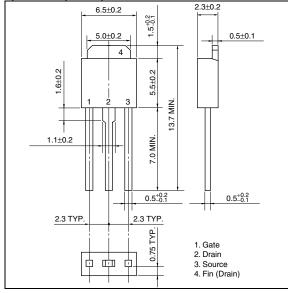
1.0 MIN. 1.8 TYP.

0.7 TYP.

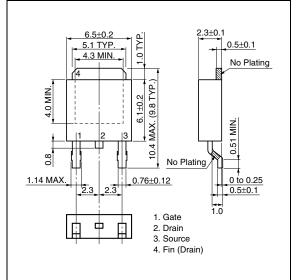
0.8 TYP.

★ PACKAGE DRAWINGS (Unit: mm)

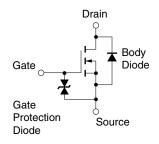
1) TO-251 (JEITA) / MP-3



3) TO-252 (JEDEC) / MP-3ZK



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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