## **DUAL SURFACE MOUNT NPN TRANSISTORS**

MMDT2222ATB6

This device contains two electrically-isolated 2N2222A NPN transistors. The two transistors have well matched hFE and are encapsulated in an ultra-small SOT-563 package. This device is ideal for portable applications where board space is at a premium.

### FEATURES

- Electrically Isolated Dual NPN Switching Transistor
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **APPLICATIONS**

- General Purpose Amplifier Applications
- Hand-Held Computers, PDAs

### **Device Marking Code: TU**

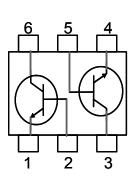
#### **MAXIMUM RATINGS** $T_1 = 25^{\circ}$ C Unless otherwise noted

Rating	Symbol	Value	Units
Collector-Base Voltage	V <sub>CBO</sub>	75	V
Collector-Emitter Voltage	V <sub>CEO</sub>	40	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current	Ι <sub>C</sub>	600	mA
Total Power Dissipation (Note 1)	PD	200	mW
Operating Junction Temperature Range	TJ	-55 to +150	°C
Storage Temperature Range	Tstg	-55 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Units
Thermal Resistance, Junction to Ambient (Note 1)	R <sub>thja</sub>	625	°C/W

Note 1. FR-4 board 60 x 70 x 1mm with minimum recommended pad layout









## **ELECTRICAL CHARACTERISTICS (Each Transistor)** T<sub>J</sub> = 25°C Unless otherwise noted

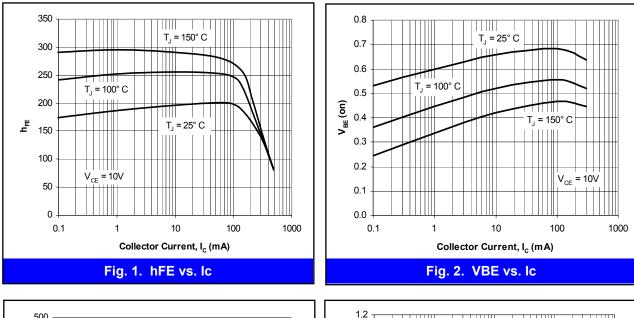
Parameter	Symbol	Conditions	Min	Тур	Max	Units	
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 10mA	40	-	-	V	
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	<sub>BR)CBO</sub> I <sub>C</sub> = 10uA		-	-	V	
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 10uA	6.0	-	-	V	
Collector Cutoff Current	I <sub>CEX</sub>	V <sub>CE</sub> = 60V, V <sub>EB</sub> = 3.0V	-	-	10	nA	
Base Cutoff Current	I <sub>BL</sub>	V <sub>CE</sub> = 60V, V <sub>EB</sub> = 3.0V	-	-	20	nA	
DC Current Gain (Note 2)	hfe	I c= 0.1mA, V c⊨ 10V	35	-	-		
		I c= 1.0mA, V c= 10V	50	-	-	-	
		I c= 10mA, V c= 10V	75	-	-		
		Ic=10mA, Vc⊫10V, T <sub>J</sub> =-55C	50	-	-		
		I c= 150mA, V c⊨ 10V	100	-	300		
		I c= 500mA, V c⊨= 10V	40	-	-		
		I c= 150mA, V c⊨ 1.0V	35	-	-		
Collector-Emitter Saturation Voltage (Note 2)	VCE(SAT)	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA	-	-	0.3	V	
		I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA	-	-	1.0	V	
Base-Emitter Saturation Voltage (Note 2)	V <sub>BE(SAT)</sub>	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA	0.6	-	1.2	v	
		I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA	-	-	2.0	V	
Gain-Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 20mA f = 100MHz	300	-	-	MHz	
Collector-Base Capacitance	Ссво	V <sub>CB</sub> = 10V, f =1.0MHz	-	-	8.0	pF	
Emitter-Base Capacitance	Сево	V <sub>EB</sub> = 0.5V, f =1.0MHz	-	-	25	pF	
Delay Time	td	V <sub>CC</sub> = 30V, I <sub>C</sub> =150mA	-	-	10	ns	
Rise Time	tr	V <sub>BE</sub> (off) = -0.5V, I <sub>B1</sub> = 15mA	-	-	25	ns	
Storage Time	ts	Vcc= 30V, I c=150mA	-	-	225	ns	
Fall Time	tf	I <sub>B1</sub> = I <sub>B2</sub> = 15mA	-	-	60	ns	

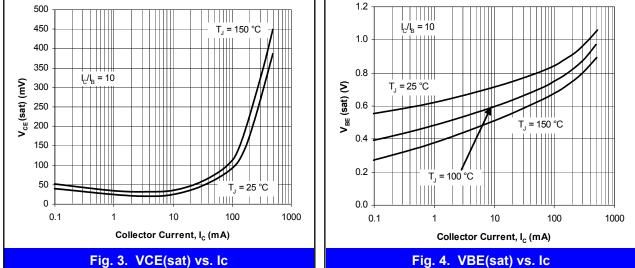
Note 2. Short duration test pulse used to minimize self-heating

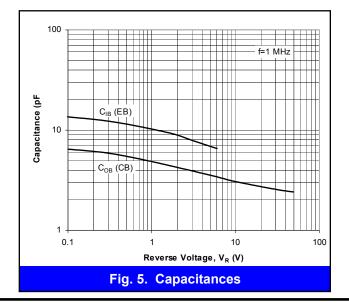




### **CHARACTERISTICS CURVES (Each Transistor)** $T_J = 25^{\circ}C$ Unless otherwise noted



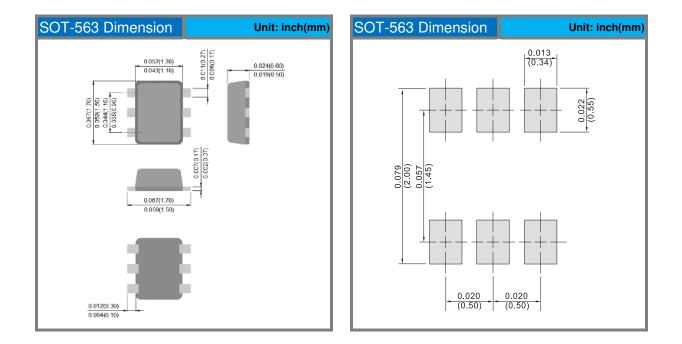








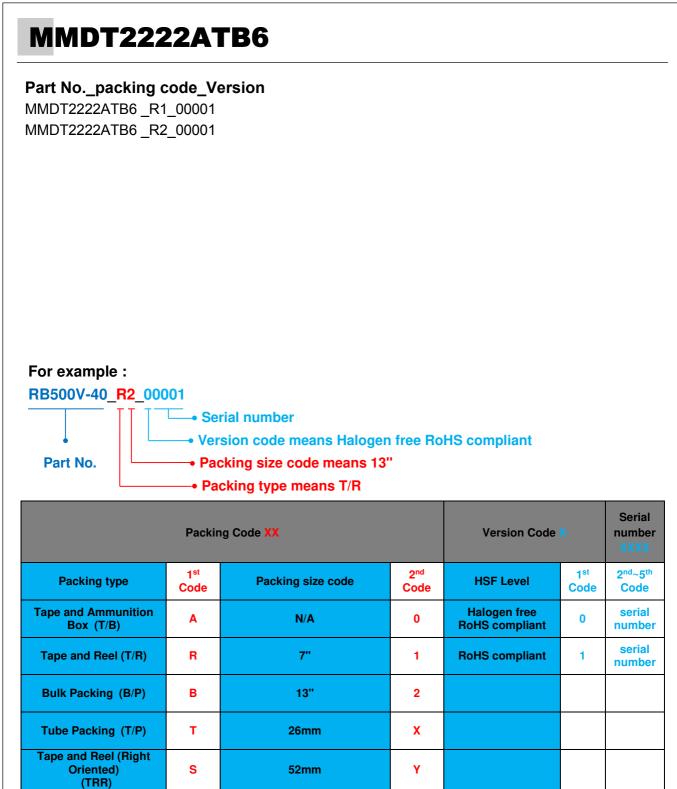
### PACKAGE LAYOUT AND SUGGESTED PAD DIMENSIONS



### **ORDERING INFORMATION**

MMDT2222ATB6 T/R7 - 4,000 units per 7 inch reel MMDT2222ATB6 T/R13 -10,000 units per 13 inch reel





L

F

Tape and Reel (Left

**Oriented**)

(TRL)

FORMING

U

D

PANASERT T/B

CATHODE UP (PBCU)

**PANASERT T/B** 

CATHODE DOWN (PBCD)



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