

(images/product/BusBlaster v4.1a_03.jpg)

Description

Bus Blaster v4 is an experimental, high-speed JTAG debugger from Dangerous Prototypes (http://dangerousprototypes.com/).

Thanks to a reprogrammable buffer, a simple update over USB makes Bus Blaster compatible with many different JTAG debugger types in the most popular open source software.

Bus Blaster v4

SKU: 102990000

- Based on FT2232H with high-speed USB 2.0
- Buffered interface works with 3.3volt to 1.8volt targets
- Reprogrammable buffer is compatible with multiple debugger types
- Compatible with 'jtagkey', 'KT-link' programmer settings in OpenOCD,
- urJTAG, and more
- ships with JTAGkey compatible buffer image pre-programmed
- Should support Serial Wire Debug when available
- · Mini-CPLD development board: self programmable, extra CPLD pins to header
- Open source (CC-BY-SA)

Updates in v4:

- V4 uses a larger CPLD than previous versions. It can now support the SWV (http://infocenter.arm.com/help/index.jsp?topic=/com.arm.doc.ddi0314h/Chdigcfd.html) feature of Cortex microcontroller for advance debugging when software support is available
- SWV is little used and not currently supported in software, most users will be better off with Bus Blaster v3 available here (http://www.seeedstudio.com/depot/bus-blaster-v3-p-1415.html? cPath=63_69)
- Fitted in a DP9056 (90x56 mm) standard PCB
- · Added series resistors to input and output pins to protect against damage and noise

Each unit is tested before it ships.

This open source hardware and software is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.

If you encounter any problems when using this product, please request technical support in the forum (http://dangerousprototypes.com/forum/index.php).