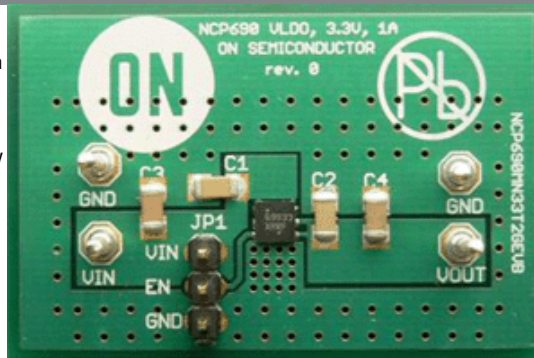




NCP690MN33T2GEVB: 3.3 V / 1 A CMOS LDO Evaluation Board

Evaluation Board Description

The NCP690 CMOS LDO family provides 1 A of output current with enhanced ESD in fixed output voltage options from 1.5 V to 5.0 V. These devices are designed for space constrained and portable battery powered applications and offer additional features such as low Dropout Voltage, high Power Supply Rejection Ratio (PSRR), low Quiescent and Ground Current consumption, low Noise operation, Short Circuit and Thermal Protection. NCP690 is designed to be used with low cost ceramic capacitors and the minimum value of 1µF output capacitance is required. The NCP690 device is equipped with Active



Output Discharge, Current Limit and Thermal Shutdown Protection. Finally the Surface Mount DFN3x3 package with Expose Pad allows saving PCB space and effectively dissipating heat through the PCB copper area. This demonstration board operates from a dc input voltage VIN < 6V and produces fixed output voltage given by the NCP690 internal voltage divider.

Evaluation Board Information

| Evaluation Board | Status | Compliance | Short Description | Parts Used | Action |
|----------------------------------|--------|------------|---------------------------------------|-------------------------------|--------|
| NCP690MN33T2GEVB | Active | Pb-free | 3.3 V / 1 A CMOS LDO Evaluation Board | NCP690MN33T2G | |

Technical Documents

| Type | Document Title | Document ID/ Size | Rev |
|----------------------------|---|--|-----|
| Eval Board: BOM | NCP690MN33T2GEVB Bill of Materials ROHS Compliant | NCP690MN33T2GEVB_BOM_ROHS.PDF - 108.0 KB | 0 |
| Eval Board: Gerber | NCP690MN33T2GEVB Gerber Layout Files (Zip Format) | NCP690MN33T2GEVB_GERBER.ZIP - 19.0 KB | 0 |
| Eval Board: Schematic | NCP690MN33T2GEVB Schematic | NCP690MN33T2GEVB_SCHEMATIC.PDF - 123.0 KB | 0 |
| Eval Board: Test Procedure | NCP690MN33T2GEVB Test Procedure | NCP690MN33T2GEVB_TEST_PROCEDURE.PDF - 200.0 KB | 0 |

