2SK0662 (2SK662)

Silicon N-channel junction FET

For low-frequency and low-noise amplification

Features

- High mutual conductance gm
- Low noise type
- SMini type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Drain-sourse voltage	V _{DS}	30	V	
Drain-gate voltage (Source open)	V _{DGO}	30	V	
Drain current	I _D	20	mA	
Gate current	I _G	10	mA	
Power dissipation	P _D	150	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

- Package
- Code
- SMini3-G1
- Pin Name
 - 1: Source
 - 2: Drain
 - 3: Gate
- Marking Symbol: 10

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source current *	I _{DSS}	$V_{DS} = 10 V, V_{GS} = 0$	0.5	SOL	12	mA
Gate-source cutoff current	I _{GSS}	$V_{GS} = 30 V, V_{DS} = 0$	je je	0-	100	nA
Gate-source cutoff voltage	V _{GSC}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 10 \mu\text{A}$	0.1		1.5	V
Mutual conductance	g _{m1}	$V_{DS} = 10 V, I_D = 0.5 mA, f = 1 kHz$	4			mS
	g _{m2}	$V_{DS} = 10 V, V_{GS} = 0, f = 1 kHz$	4			
Short-circuit forward transfer capacitance (Common source)	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		14		pF
Reverse transfer capacitance (Common source)	C _{rss}	SS HOILMAN		3.5		pF
Noise voltage NV		$\begin{split} V_{DS} &= 30 \text{ V}, \text{ I}_{D} = 1 \text{ mA}, \text{ G}_{V} = 80 \text{ dB} \\ \text{R}_{g} &= 100 \text{ k}\Omega, \text{ Function} = \text{FLAT} \end{split}$		60		mV

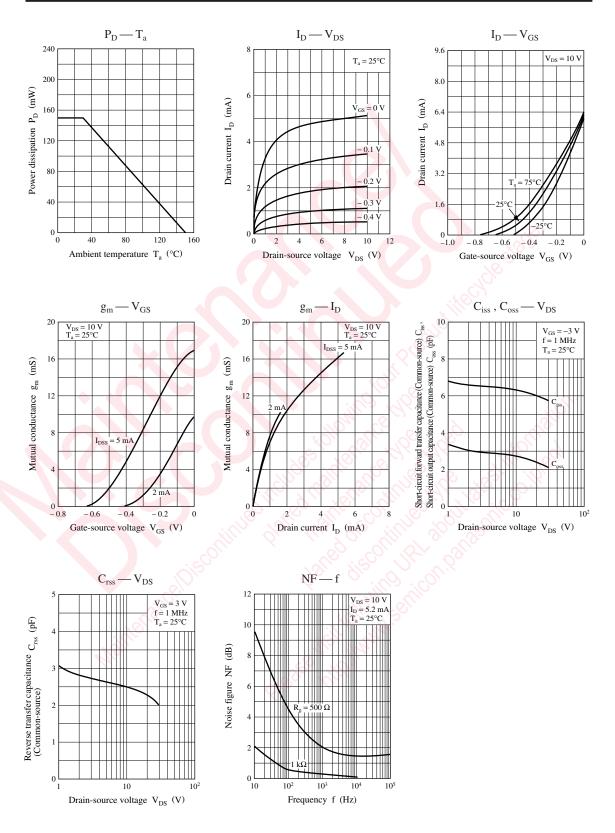
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Rank classification

Rank	Р	Q	R
I _{DSS} (mA)	0.5 to 3.0	2.0 to 6.0	4.0 to 12.0

Note) The part number in the parenthesis shows conventional part number.

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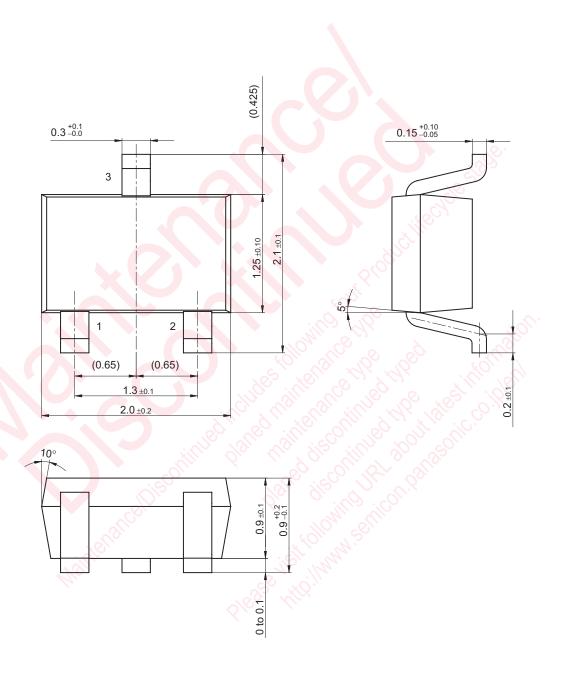




Panasonic

SMini3-G1

Unit: mm



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