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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H5N2007FN

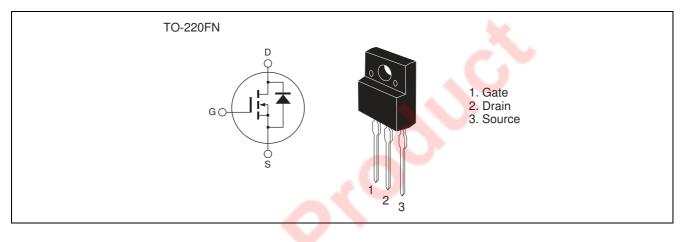
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G0370-0100Z Rev.1.00 May.28.2004

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$		
Item	Symbol	Ratings	Unit		
Drain to Source voltage	V _{DSS}	200	V		
Gate to Source voltage	V _{GSS}	±30	V		
Drain current	ID	25	А		
Drain peak current	Note1	100	А		
Body-Drain diode reverse Drain current	I _{DR}	25	А		
Body-Drain diode reverse Drain peak current	Note1 I _{DR (pulse)}	100	A		
Avalanche current	I _{AP} ^{Note3}	9	A		
Avalanche energy	E _{AR} ^{Note3}	5.4	mJ		
Channel dissipation	Pch ^{Note2}	30	W		
Channel to case thermal impedance	θch-c	4.17	°C/W		
Channel temperature	Tch	150	°C		
Storage temperature	Tstg	-55 to +150	°C		

Notes: 1. $PW \le 10 \propto s$, duty cycle $\le 1\%$

2. Value at Tc = $25^{\circ}C$

3. STch = 25° C, Tch $\leq 150^{\circ}$ C



Electrical Characteristics

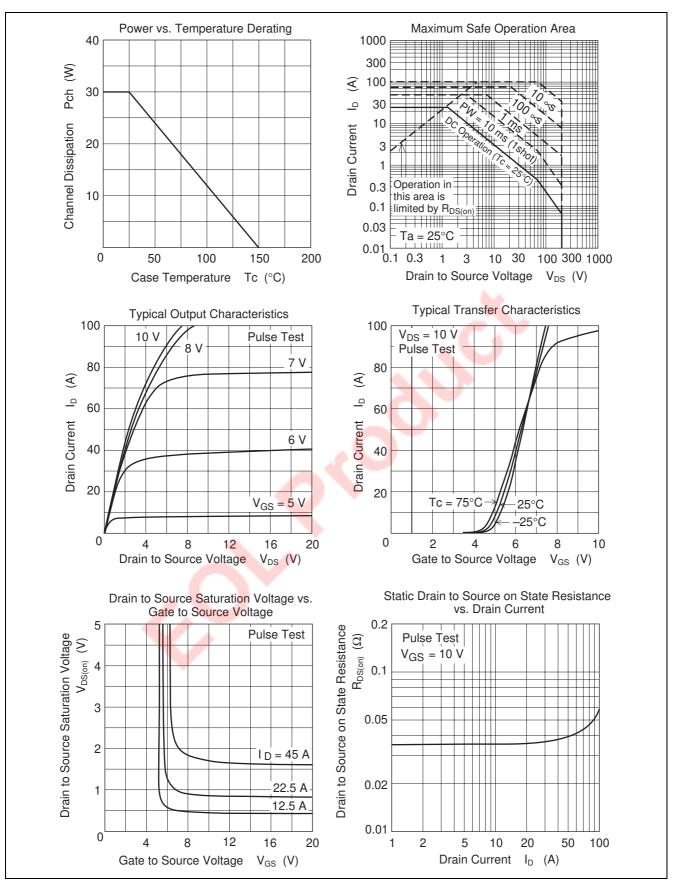
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero Gate voltage drain current	I _{DSS}	—	—	1	∝A	$V_{DS} = 200 V, V_{GS} = 0$
Gate to Source leak current	I _{GSS}	—	—	±0.1	∝A	$V_{GS}=\pm 30~V,~V_{DS}=0$
Gate to Source cutoff voltage	V _{GS(off)}	3.0	—	4.0	V	$V_{DS} = 10 V, I_D = 1 mA$
Forward transfer admittance	yfs	13	22	—	S	$I_D = 12.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Static Drain to Source on state	R _{DS(on)}	—	0.036	0.047	Ω	$I_D = 12.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance						
Input capacitance	Ciss	—	2200	—	рF	V _{DS} = 25 V
Output capacitance	Coss	—	410	—	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	—	54	—	pF	
Turn-on delay time	td(on)	—	35	—	ns	$I_D = 12.5 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 8 \Omega$ $Rg = 10 \Omega$
Rise time	tr	—	120	—	ns	
Turn-off delay time	td(off)	—	110	—	ns	
Fall time	tf	—	85	—	ns	
Total Gate charge	Qg	—	56	—	nC	V _{DD} = 160 V
Gate to Source charge	Qgs	—	13	—	nC	$V_{GS} = 10 V$ $I_D = 25 A$
Gate to Drain charge	Qgd	—	26	—	nC	
Body-Drain diode forward voltage	V _{DF}	_	0.9	1.5	V	$I_{F} = 25 \text{ A}, V_{GS} = 0^{Note4}$
Body-Drain diode reverse recovery time	trr	—	140	-	ns	$I_F = 25 \text{ A}, V_{GS} = 0$
Body-Drain diode reverse recovery	Qrr	—	0.7	_	∞C	diF/dt = 100 A/∝s
charge						

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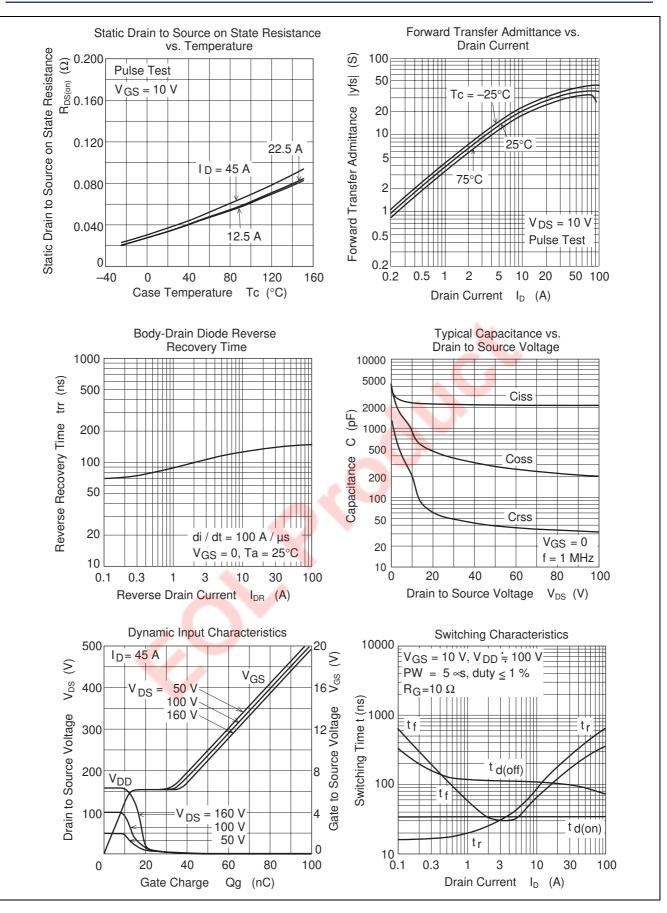
Notes: 4. Pulse test



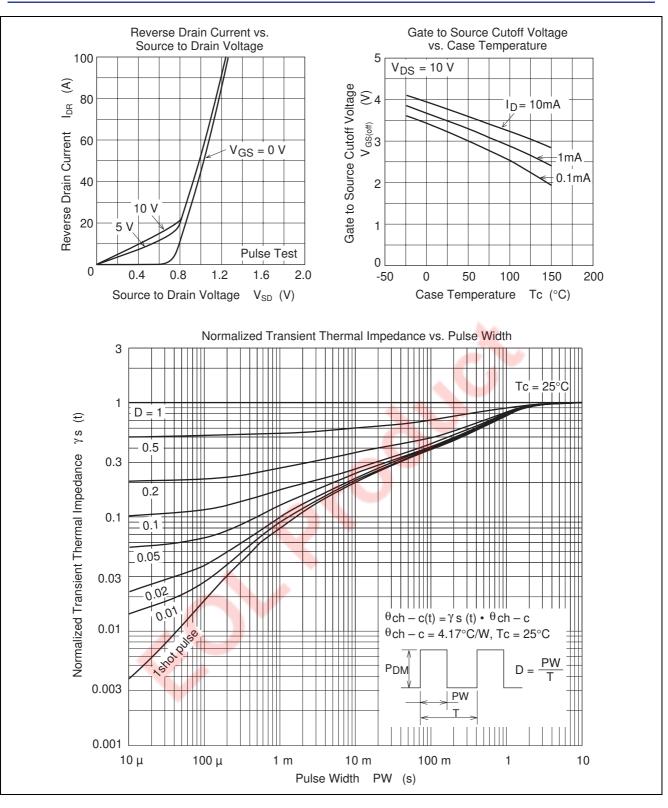
Main Characteristics



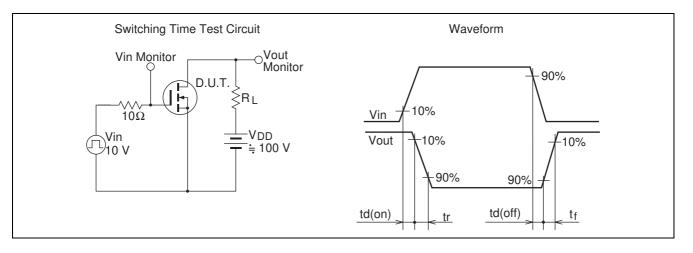




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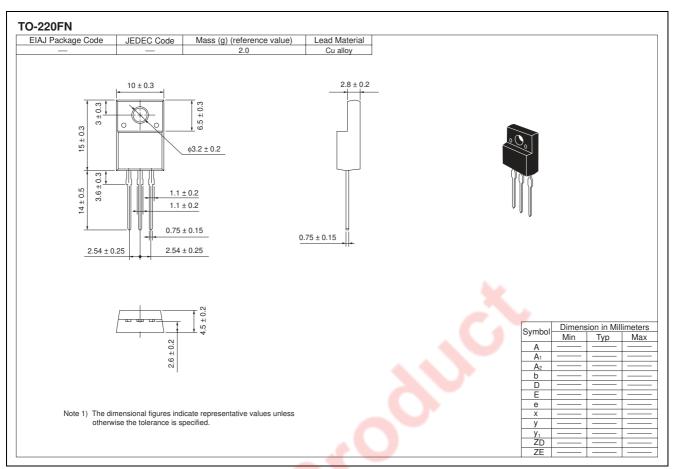


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Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
H5N2007FN-E	50 pcs	Plastic magazine

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